TECHNICAL MANUAL

ORGANIZATIONAL MAINTENANCE MANUAL
RADAR INTERFACE EQUIPMENT MAINTENANCE
RADAR INTEGRATION UNITS 1 AND 2

EXPANDED TROUBLESHOOTING
(LOGIC DIAGRAMS)

GUIDED MISSILE
AIR DEFENSE SYSTEM
AN/TSQ-73

HEADQUARTERS, DEPARTMENT OF THE ARMY

4 APRIL 1985

Change
No. 3

HEADQUARTERS
DEPARTMENT OF THE ARMY
Washington, D.C., 30 November 1992

Organizational Maintenance Manual:
RADAR INTERFACE EQUIPMENT MAINTENANCE
RADAR INTEGRATION UNITS 1 AND 2
MICOM GUIDED MISSILE AIR DEFENSE SYSTEM AN/TSQ-73

TM 9-1430-655-20-3-5, 4 April 1985, is changed as follows:

1. Remove old pages and insert new pages as indicated below. New or changed material is indicated by the applicable change number, i.e., Change 2, at the bottom of the page adjacent to the page number. Revised text has a vertical bar in the margin next to the changed area. Revised illustrations have a suffix change letter added to the identification number.

Remove Pages	*Insert Pages*
A/(B blank)	A/(B blank)
FO-126 (Sheets 1 and 2)	FO-126 (Sheets 1 and 2)
FO-127 thru FO-130	FO-127 thru FO-130
FO-131 (Sheet 2)	FO-131 (Sheet 2)
FO-132	FO-132
FO-133 (Sheet 1)	FO-133 (Sheet 1)
FO-134 (Sheet 1)	FO-134 (Sheet 1)
FO-137	FO-137
FO-139 and FO-140	FO-139 and FO-140
FO-141 (Sheet 2)	FO-141 (Sheet 2)
FO-143 thru FO-147	FO-143 thru FO-147
FO-149	FO-149
FO-150 (Sheet 1)	FO-150 (Sheet 1)
FO-151 thru FO-160	FO-151 thru FO-160
FO-161 (Sheets 3 thru 5)	FO-161 (Sheets 3 thru 5)
FO-162 (Sheets 1 thru 4)	FO-162 (Sheets 1 thru 4)
FO-165	FO-165
FO-169	FO-169
FO-175	FO-175
FO-183	FO-183
FO-184 (Sheets 1, 2 and 4)	FO-184 (Sheets 1, 2 and 4)
FO-187	FO-187
FO-188 (Sheet 1)	FO-188 (Sheet 1)
FO-189	FO-189
FO-191 (Sheet 3)	FO-191 (Sheet 3)
FO-192 and FO-193	FO-192 and FO-193
FO-195 (Sheet 1)	FO-195 (Sheet 1)
FO-197 and FO-198	FO-197 and FO-198
FO-199 (Sheet 1)	FO-199 (Sheet 1)
FO-200 (Sheets I thru 5)	FO-200 (Sheets 1 thru 5)
FO-201	FO-201
FO-202 (Sheet 2)	FO-202 (Sheet 2)
FO-204	FO-204

2. File this change sheet in front of the publication for reference.

By Order of the Secretary of the Army:

GORDON R. SULLIVAN
General, United States Army
Chief of Staff

Official:

MILTON H. HAMILTON
Administrative Assistant to the
Secretary of the Army
03488

DISTRIBUTION:

To be distributed in accordance with DA Form 12-32E, Block 2336, requirements for TM 9-1430-655-20-3-5, AN/TSQ-73 Missile System.

Change

No. 2

HEADQUARTERS
DEPARTMENT OF THE ARMY
Washington, D.C., 1 August 1991

ORGANIZATIONAL MAINTENANCE MANUAL
RADAR INTERFACE EQUIPMENT MAINTENANCE
RADAR INTEGRATION UNITS 1 AND 2

EXPANDED TROUBLESHOOTING
(LOGIC DIAGRAMS)
GUIDED MISSILE AIR DEFENSE SYSTEM
AN/TSQ-73

TM 9-1430-655-20-3-5, 4 April 1985, is changed as follows:

1. Remove old pages and insert new pages as indicated below. New or changed material is indicated by the applicable change number, i.e., Change 2, at the bottom of the page adjacent to the page number. Revised text has a vertical bar in the margin next to the changed area. Revised illustrations have a suffix change letter added to the identification number.

Remove Pages	*Insert Pages*
A/(B blank)	A/(B blank)
5-785 - 5-787/(5-788 blank)	5-785 - 5-787/(5-788 blank)
FO-126 (Sheet 1 of 3)	FO-126 (Sheet 1 of 3)
FO-126 (Sheet 2 of 3)	FO-126 (Sheet 2 of 3)
FO-129	FO-129
FO-130	FO-130
FO-131 (Sheet 1 of 2)	FO-131 (Sheet 1 of 2)
FO-131 (Sheet 2 of 2)	FO-131 (Sheet 2 of 2)
FO-134 (Sheet 2 of 2)	FO-134 (Sheet 2 of 2)
FO-135	FO-135
FO-136	FO-136
FO-137	FO-137
FO-140	FO-140
FO-150 (Sheet 1 of 2)	FO-150 (Sheet 1 of 2)
FO-157	FO-157
FO-160	FO-160
FO-181 (Sheet 1 of 2)	FO-181 (Sheet 1 of 2)
FO-181 (Sheet 2 of 2)	FO-181 (Sheet 2 of 2)
FO-182	FO-182
FO-183	FO-183
FO-184 (Sheet 1 of 4)	FO-184 (Sheet 1 of 4)
FO-184 (Sheet 2 of 4)	FO-184 (Sheet 2 of 4)
FO-184 (Sheet 3 of 4)	FO-184 (Sheet 3 of 4)
FO-184 (Sheet 4 of 4)	FO-184 (Sheet 4 of 4)
FO-185	FO-185

2. File this change sheet in front of the publication for reference.

By Order of the Secretary of the Army:

CARL E. VUONO
General, United States Army
Chief of Staff

Official:

PATRICIA P. HICKERSON
Brigadier General, United States Army
The Adjutant General

DISTRIBUTION:

To be distributed in accordance with DA Form 12-32-E, Block 2336, requirements for TM 9-1430-655-20-3-5.

TM 9-1430-655-20-3-5
C1

Change
No. 1

HEADQUARTERS
DEPARTMENT OF THE ARMY
Washington, D.C., *25 September 1990*

ORGANIZATIONAL MAINTENANCE MANUAL
RADAR INTERFACE EQUIPMENT MAINTENANCE
RADAR INTEGRATION UNITS 1 AND 2
EXPANDED TROUBLESHOOTING
(LOGIC DIAGRAMS)

GUIDED MISSILE AIR DEFENSE SYSTEM
AN/TSQ-73

TM 9-1430-655-20-3-5, 4 April 1985, is changed as follows:

1. Remove old pages and insert new pages as indicated below. New or changed material is indicated by the applicable change number, i.e., Change 1, at the bottom of the page adjacent to the page number. Revised text has a vertical bar in the margin next to the changed area. Revised illustrations have a suffix change letter added to the identification number.

Remove Pages	*Insert Pares*
A/(B blank)	A/(B blank)
i thru iii/(iv blank)	i thru iii/(iv blank)
5-371 thru 5-383/(5-384 blank)	5-775 thru 5-787/(5-788 blank)
FO-137	FO-137
FO-144	FO-144

2. File this change sheet in front of the publication for reference.

By Order of the Secretary of the Army:

CARL E. VUONO
General, United States Army
Chief of Staff

Official:

THOMAS F. SIKORA
Brigadier General, United States Arm
The Adjutant General

Distribution:

To be distributed in accordance with DA Form 12-32, Unit Maintenance requirements for AN/TSQ-73 Missile System.

WARNING

DANGEROUS VOLTAGE

is used in the operation of this equipment

DEATH ON CONTACT

may result if personnel fail to observe safety precautions

Never work on electronic equipment unless there is another person nearby who is familiar with the operation and hazards of the equipment and who is competent in administering first aid. When the technician is aided by operators, he must warn them about dangerous areas.

Whenever possible, the power supply to the equipment must be shut off before beginning work on the equipment. Take particular care to ground every capacitor likely to hold a dangerous potential. When working inside the equipment, after the power has been turned off, always ground every part before touching it.

Be careful not to contact high-voltage connections when installing or operating this equipment.

Whenever the nature of the operation permits, keep one hand away from the equipment to reduce the hazard of current flowing through vital organs of the body.

WARNING

Do not be misled by the term "low voltage". Potentials as low as 50 volts may cause death under adverse conditions.

For Artificial Respiration refer to FM 21-11.

EXTREMELY DANGEROUS POTENTIALS

greater than 500 volts exist in the following units:

Display console high voltage power supply

Display console CRT

WARNING

For emergencies requiring immediate shutdown of system power, press SYSTEM POWER OFF switch located on power cabinet power transfer unit. Observe that SYSTEM POWER ON indicator light goes off.

LIST OF EFFECTIVE PAGES

Insert latest change pages, dispose of superseded pages in accordance with applicable regulations.

NOTE: The portion of the text affected by the latest change is indicated by a vertical line in the outer margin of the page.

Dates of issue for original and change pages are:
Original.....0..... 4 April 1985
Change.....1..... 25 Sep 1990
Change.....2..... 1 Aug 1991
Change.....3.....30 Nov 1992

TOTAL NUMBER OF PAGES IN THIS PUBLICATION IS 130 CONSISTING OF THE FOLLOWING:

Page No.	Change No.
a	0
b blank	1
A	3
B blank	3
i-iii	1
iv blank	1
5-775 - 5-714	1
5-785	1
5-786 - 5-787	2
5-788 blank	2
FO-126 (Sheets 1 and 2)	3
FO-126 (Sheet 3)	0
FO-127	3
FO-128	3
FO-129	3
FO-130	3
FO-131 (Sheet 1)	2
FO-131 (Sheet 2)	3
FO-132	3
FO-133 (Sheet 1)	3
FO-133 (Sheet 2)	0
FO-134 (Sheet 1)	3
FO-134 (Sheet 2)	2
FO-135	2
FO-136	2
FO-137	3
FO-138	0
FO-139	3
FO-140	3
FO-141 (Sheet 1)	0
FO-141 (Sheet 2)	3
FO-142 (3 Sheets)	0
FO-143	3
FO- 144	3
FO-145	3
FO-146	3
FO-147	3
FO-148.	0
FO-149	3
FO-150 (Sheet 1)	3
FO-150 (Sheet 2)	0
FO-151	3
FO-152	3
FO-153	3
FO-154	3
FO-155	3
FO-156	3
FO-157	3
FO-158	3
FO-159	3
FO-160	3
FO-161 (Sheets 1 and 2)	0
FO-161 (Sheets 3 thru 5)	3
FO-162 (4 sheets)	3
FO-163	0
FO-164	0
FO-165	3
FO-166	0
FO-167	1
FO-168	0
FO-169	3
FO-171	0
FO-171 (2 sheets)	0
FO-172	3
FO-173	0
FO-174	0
FO-175	3
FO-176	1
FO-177	0
FO-178	0
FO-179	0
FO-180	0
FO-181 (2 sheets)	2
FO-182	2
FO-183	3
FO-184 (Sheets 1 and 2)	3
FO-184 (Sheet 3)	2
FO-184 (Sheet 4)	3
FO-185	2
FO-180	0
FO-187	3
FO-188 (Sheet 1)	3
FO-188 (Sheet 2)	0
FO-189	3
FO-190	0
FO-191 (Sheets 1 and 2)	0
FO-191 (Sheet 3)	3
FO-192	3
FO-193	3
FO-194	0
FO-195 (Sheet 1)	3
FO-195 (Sheet 2)	0
FO-196	0
FO-197	3
FO-198	3
FO-199 (Sheet 1)	3
FO-199 (Sheets 2 and 3)	0
FO-200 (5 sheets)	3
FO-201	3
FO-202 (Sheet 1)	0
FO-202 (Sheet 2)	3
FO-203	3
FO-204	3
FO-205 (2 sheets)	0

☆U.S. GOVERNMENT PRINTING OFFICE 1993-733 -161/60204

TECHNICAL MANUAL

No. 9-1430-655-20-3-5

HEADQUARTERS
DEPARTMENT OF THE ARMY
Washington, D.C., *4 April 1985*

ORGANIZATIONAL MAINTENANCE MANUAL
RADAR INTERFACE EQUIPMENT MAINTENANCE
RADAR INTEGRATION UNITS 1 AND 2

EXPANDED TROUBLESHOOTING
(LOGIC DIAGRAMS)

GUIDED MISSILE AIR DEFENSE SYSTEM

AN/TSQ-73

REPORTING OF ERRORS

You can help improve this publication. If you find any mistakes, or if you know of a way to improve the procedures, please let us know. Mail your letter, DA Form 2028 (Recommended Changes to Publications and Blank Forms), or DA Form 2028-2 located in back of this manual direct to: Commander, U.S. Army Missile Command, ATTN: AMSMI-LC-ME-P, Redstone Arsenal, AL 35898-5238. A reply will be furnished to you.

TABLE OF CONTENTS

LIST OF ILLUSTRATIONS

LIST OF ILLUSTRATIONS (Continued)

LIST OF TABLES

SECTION XV. RADAR INTEGRATION UNITS

5-48. General. This manual is Volume 5 of TM 9-1430-655-20-3, Radar Interface Equipment Maintenance for Guided Missile Air Defense System AN/TSQ-73. It contains the logic diagrams covering radar integration units (RIU) 1 and 2 for use and guidance of advanced personnel responsible for repair of the RIE. Foldouts 126 through 165 cover RIU 1 and foldouts 166 through 205 cover RIU 2. RIU 1 and 2 are located in equipment rack 1, 1A1A1, door A, in two card cage bays. Figure 5-4 illustrates bay 1 and figure 5-5 illustrates bay 2.

5-49. Logic Diagrams. Logic diagrams provide the maintenance technician pin to pin signal flow, traceable by signal mnemonics and I/O tables, to help identify faulty cards and to troubleshoot faults in the backplane wiring and other areas that are beyond fault isolation capabilities of the MTS.

5-50. Using Logic Diagrams. Logic diagrams in this manual show signal flow in functional subsystems of RIU 1 and 2. Signal flow is traceable between circuit card pin numbers and is shown as inputs and outputs of integrated circuit logic devices on the circuit card. A specific signal can be followed between foldouts by using the signal mnemonic and the logic diagram input/output table. The circuit card slot is shown within the integrated circuit card device symbol. Table 5-39 contains the circuit card slot and the part number of the card. Table 5-40 contains, by card part number, the test point for each of the 80 pins of MTS testable cards.

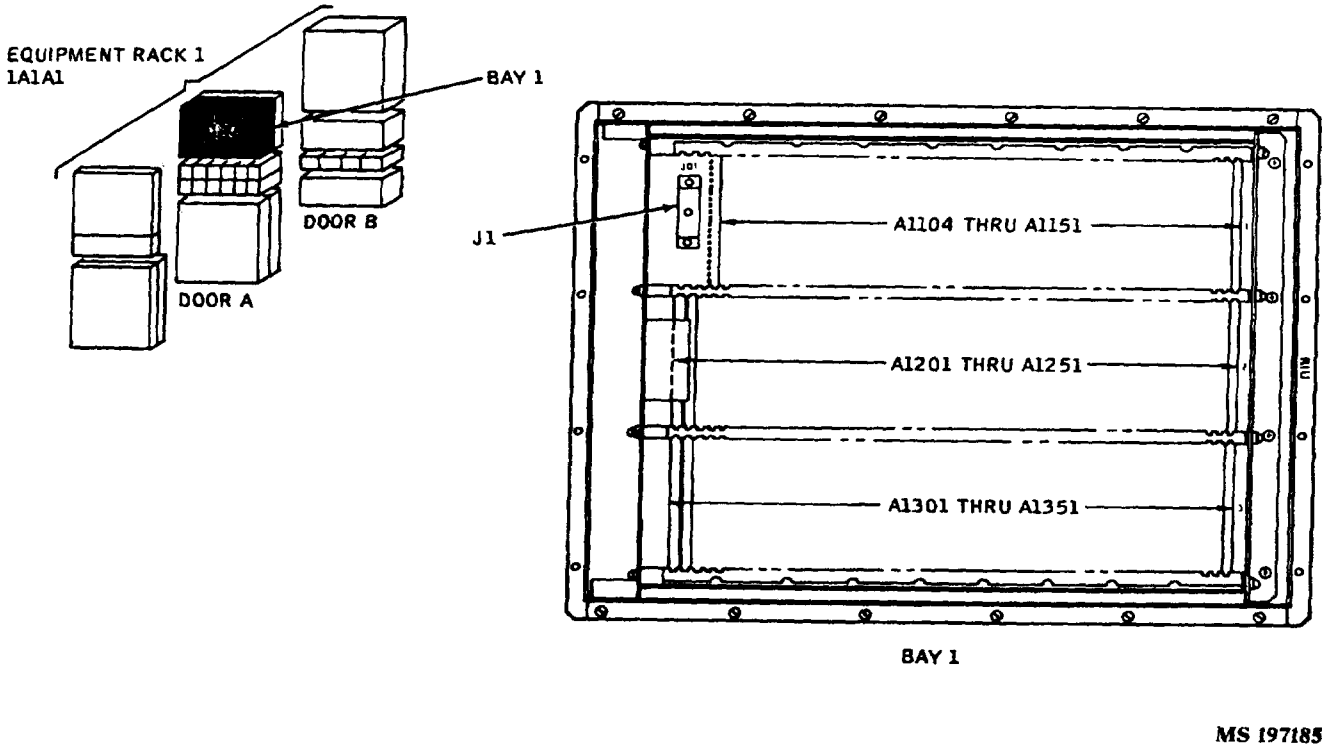


Figure 5-4. Radar Integration Unit 1A1A1A4 Bay 1, Component Location

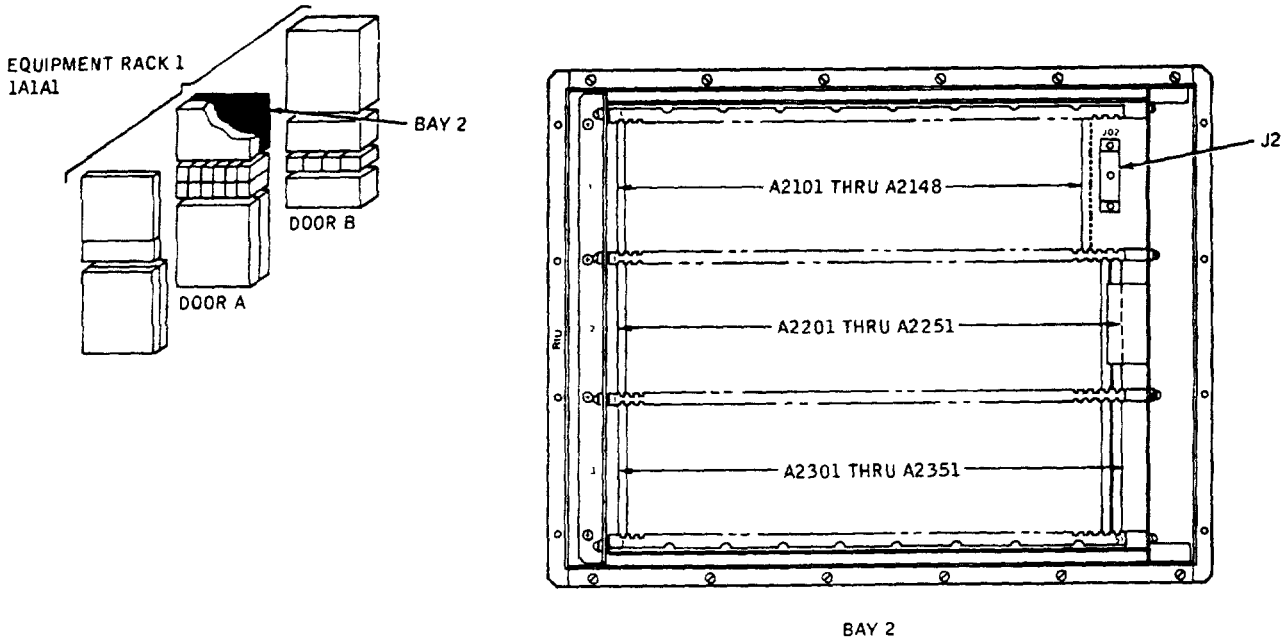


Figure 5-5. Radar Integration Unit 1A1A1A4 Bay 2, Component Location

Table 5-39. Radar Integration Unit 1A1A1A4, Circuit Card Location

Card slot	Part number	Card type	Color code 1	2	3	4
		BAY 1-SHELF 1				
A1101	-	-	-	-	-	-
A1102	-	-	-	-	-	-
A1102	-	-	-	-	-	-
A1103	-					
A1104	W308	Connector	-	-	-	-
A1105	W309	Connector	-	-	-	-
A1106	W528	Connector	-	-	-	-
A1107	W529	Connector	-	-	-	-
A108	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A109	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A110	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A111	587102-102	Quad 2-input NAND gate	-	-	Red	-
A1112	587102-102	Quad 2-input NAND gate	-	-	Red	-
A1113	587100-102	4/8 MHz oscillator	-	-	-	-
A1114	10281652	3-input J-K flip-flop	Brown	Blue	Green	Red
A1115	10281652	3-input J-K flip-flop	Brown	Blue	Green	Red
A1116	10281652	3-input J-K flip-flop	Brown	Blue	Green	Red
A1117	10281652	3-input J-K flip-flop	Brown	Blue	Green	Red
A1118	10281652	3-input J-K flip-flop	Brown	Blue	Green	Red
A1119	587102-102	Quad 2-input NAND gate	-	-	-	Red
A1120	587102-102	Quad 2-input NAND gate	-	-	-	Red
A1121	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A1122	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A1123	587119-100	240-ohm resistor	-	-	-	-
A1124	587108-102	Single 8-input NAND gate	-	-	Gray	-
A11251	10281610	Hex 4-bit comparator	Brown	Blue	Brown	Black
A1126	10281609	Quint 4-bit adder	Brown	Blue	Black	White
A1127	10281609	Quint 4-bit adder	Brown	Blue	Black	White
A1128	587102-102	Quad 2-input NAND gate	-	-	Red	-
A1129	587102-102	Quad 2-input NAND gate	-	-	Red	-
A1130	587102-102	Quad 2-input NAND gate	-	-	Red	-
A1131	587102-102	Quad 2-input NAND gate	-	-	Red	-
A11321	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A1133	587102-102	Quad 2-input NAND gate	-	-	Red	-
A1134	587102-102	Quad 2-input NAND gate	-	-	Red	-

See footnote at end of table.

Table 5-39. Radar Integration Unit 1A1A1A4, Circuit Card Location -Continued

Card slot	Part number	Card type	Color code 1	2	3	4
A1135	587102-102	Quad 2-input NAND gate	-	-	Red	-
A1136	587106-102	Quad 2-input lamp driver	-	-	Blue	-
A1137	587106-102	Quad 2-input lamp driver	-	-	Blue	-
A1138	10281652	3-input J-K flip-flop	Brown	Blue	Green	Red
A1139	587119-100	240-ohm resistor	-	-	-	-
A1140[1]	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A1141[1]	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A1142	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A1143[1]	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A1144	587102-102	Quad 2-input NAND gate	-	-	Red	-
A1145	587102-102	Quad 2-input NAND gate	-	-	Red	-
A1146	587102-102	Quad 2-input NAND gate	-	-	Red	-
A1147	587102-102	Quad 2-input NAND gate	-	-	Red	-
A1148	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A1149	587119-100	240-ohm resistor	-	-	-	-
A1150	-	-	-	-	-	-
A1151	10283505	Test set interface	Orange	Green	Black	Green
		BAY 1-SHELF 2				
A1201	W320	Connector	-	-	-	-
A1202	W317	Connector	-	-	-	-
A1203	W318	Connector	-	-	-	-
A1204	W319	Connector	-	-	-	-
A1205	587102-102	Quad 2-input NAND gate	-	-	Red	-
A1206	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A1207	10281610	Hex 4-bit comparator	Brown	Blue	Brown	Blue
A1208	587102-102	Quad 2-input NAND gate	-	-	Red	-
A1209	587104-102	Dual 4-input NAND gate		-	Yellow	-
A1210	587117-102	Hex inverter	Brown	-	Violet	-
A1211	-	-	-	-	-	-
A1212	10281652	3-input J-K flip-flop	Brown	Blue	Green	Red
A1213	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A1214	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A1215	10281652	3-input J-K flip-flop	Brown	Blue	Green	Red
A1216	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A1217	10281652	3-input J-K flip-flop	Brown	Blue	Green	Red

See footnote at end of table.

Table 5-39. Radar Integration Unit 1A1A1A4, Circuit Card Location -Continued

Card slot	Part number	Card type	Color code 1	2	3	4
A1218	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A1219	587102-102	Quad 2-input NAND gate	-	-	Red	-
A1220	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A1221	10281609	Quint 4-bit adder	Brown	Blue	Black	White
A1222	587102-102	Quad 2-input NAND gate	-	-	Red	-
A1223	587117-102	Hex inverter	Brown	-	Violet	-
A1224	587108-102	Single 8-input NAND gate	-	-	Gray	-
A1225	-	-				-
A1226	-	-	-	-	-	-
A1227	587119-100	240-ohm resistor	-	-	-	-
A1228	10281603	4-bit multiplexer	Brown	Blue	Black	Orange
A1229	-	-	-	-	-	-
A1230	587102-102	Quad 2-input NAND gate	-	-	Red	-
A1231	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A1232	10281610	Hex 4-bit comparator	Brown	Blue	Brown	Black
A1233	587102-102	Quad 2-input NAND gate	-	-	Red	-
A1234	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A1235	587117-102	Hex inverter	Brown	-	Violet	-
A1236	-	-	-	-	-	-
A1237	10281652	3-input J-K flip-flop	Brown	Blue	Green	Red
A1238	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A1239	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A1240	10281652	3-input J-K flip-flop	Brown	Blue	Green	Red
A1241	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A1242	10281652	3-input J-K flip-flop	Brown	Blue	Green	Red
A1243	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A1244	587102-102	Quad 2-input NAND gate	-	-	Red	-
A1245	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A1246	10281609	Quint 4-bit adder	Brown	Blue	Black	White
A1247	587102-102	Quad 2-input NAND gate	-	Red	-	-
A1248	587117-102	Hex inverter	Brown	-	Violet	-
A1249	-	-	-	-	-	-
A1250	-	-	-	-	-	-
A1251	-	-	-	-	-	-

Table 5-39. Radar Integration Unit 1A1A1A4, Circuit Card Location -Continued

Card slot	Part number	Card type	Color code 1	2	3	4
		BAY 1-SHELF 3				
A1301	W531	Connector	-	-	-	-
A1302	W348	Connector	-	-	-	-
A1303	W349	Connector	-	-	-	-
A1304	587107-102	AC-coupled I/O	-	-	Violet	-
A1305	587107-102	AC-coupled I/O	-	-	Violet	-
A1306	587107-102	AC-coupled I/O	-	-	Violet	-
A1307	587107-102	AC-coupled I/O	-	-	Violet	-
A1308	587107-102	AC-coupled I/O	-	-	Violet	-
A1309	587107-102	AC-coupled I/O	-	-	Violet	-
A1310	687107-102	AC-coupled I/O	-	-	Violet	-
A1311	587102-102	Quad 2-input I/O	-	-	Red	-
A1312	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A1313	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A1314	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A1315	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A1316	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A1317	587102-102	Quad 2-input NAND gate	-	-	Red	-
A1318	587102-102	Quad 2-input NAND gate	-	-	Red	-
A1319	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A1320	587108-102	Single 8-input NAND gate	-	-	Gray	-
A1321	10281609	Quint 4-bit adder	Brown	Blue	Black	White
A1322	10281609	Quint 4-bit adder	Brown	Blue	Black	White
A1323	587102-102	Quad 2-input NAND gate	-	-	Red	-
A1324	587102-102	Quad 2-input NAND gate	-	-	Red	-
A1325	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A1326	587108-102	Single 8-input NAND gate	-	-	Gray	-
A1327	10281652	3-input J-K flip-flop	Brown	Blue	Green	Red
A1328	10281652	3-input J-K flip-flop	Brown	Blue	Green	Red
A1329	10281652	3-input J-K flip-flop	Brown	Blue	Green	Red
A1330	10281652	3-input J-K flip-flop	Brown	Blue	Green	Red
A1331	-	-	-	-	-	-
A1332	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A1333	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A1334	587108-102	Single 8-input NAND gate	-	-	Gray	-

Table 5-39. Radar Integration Unit 1A1A1A4, Circuit Card Location -Continued

Card slot	Part number	Card type	Color code 1	2	3	4
A1335	587108-102	Single 8-input NAND gate	-	-	Gray	-
A1336	587102-102	Quad 2-input NAND gate	-	-	Red	-
A1337	587102-102	Quad 2-input NAND gate	-	-	Red	-
A1338	587102-102	Quad 2-input NAND gate	-	-	Red	-
A1339	587102-102	Quad 2-input NAND gate	-	-	Red	-
A1340	10281606	Hex 4-bit shift register	Brown	Blue	Black	-
A1341	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A1342	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A1343	587102-102	Quad 2-input NAND gate	-	-	Red	-
A1344	587108-102	Single 8-input NAND gate	-	-	Gray	-
A1345	587102-102	Quad 2-input NAND gate	-	-	Red	-
A1346	587102-102	Quad 2-input NAND gate	-	-	Red	-
A1347	-	-	-	-	-	-
A1348	-	-	-	-	-	-
A1349	-	-	-	-	-	-
A1350	-	-	-	-	-	-
A1351	-	-	-	-	-	-
		BAY 2-SHELF 1				
A2101	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A2102	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2103	10281652	3-input J-K flip-flop	Brown	Blue	Green	Red
A2104	10281603	4-bit multiplexer	Brown	Blue	Black	Orange
A2105	10281603	4-bit multiplexer	Brown	Blue	Black	Orange
A2106	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A2107	10281652	3-input J-K flip-flop	Brown	Blue	Green	Red
A2108	10281602	Counter/decoder	Brown	Blue	Black	Red
A2109	10281602	Counter/decoder	Brown	Blue	Black	Red
A2110	10281652	3-input J-K flip-flop	Brown	Blue	Green	Red
A2111	587102-102	Quad 2-input NAND gate	-			Red
A2112	10281652	3-input J-K flip-flop	Brown	Blue	Green	Red
A2113	587117-102	Hex inverter	Brown	-	Violet	-
A2114	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A2115	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A2116	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2117	587104-102	Dual 4-input NAND gate	-	-	Yellow	-

Table 5-39. Radar Integration Unit 1A1A1A4, Circuit Card Location -Continued

Card slot	Part number	Card type	Color code 1	2	3	4
A2118	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2119	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2120	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A2121	10281603	4-bit multiplexer	Brown	Blue	Black	Orange
A2122	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A2123	10281603	4-bit multiplexer	Brown	Blue	Black	Orange
A2124	10281606	Hex 4-bit shift register	Brown	Black	Black	Blue
A2125	10281603	4-bit multiplexer	Brown	Blue	Black	Orange
A2126	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A2127	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A2128	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A2129	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A2130	10281602	Counter/decoder	Brown	Blue	Black	Red
A2131	587108-102	Single 8-input NAND gate	-	-	Gray	-
A2132	10281602	Counter/decoder	Brown	Blue	Black	Red
A2133	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2134	10281602	Counter/decoder	Brown	Blue	Black	Red
A2135	10281610	Hex 4-bit comparator	Brown	Blue	Brown	Black
A2136	587117-102	Hex inverter	Brown	-	Violet	-
A2137	10281652	3-input J-K flip-flop	Brown	Blue	Green	Red
A2138	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2139	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A2140	10281652	3-input J-K flip-flop	Brown	Blue	Green	Red
A2141	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A2142	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A2143	10281610	Hex 4-bit comparator	Brown	Blue	Brown	Black
A2144	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A2145	W305	Connector	-	-	-	-
A2146	W304	Connector	-	-	-	-
A2147	W306	Connector	-	-	-	-
A2148	W307	Connector	-	-	-	-
A2149		-		-	-	-
A2150		-	-	-	-	-
A2151		-		-	-	-
A2201	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue

Table 5-39. Radar Integration Unit 1A1A1A4, Circuit Card Location -Continued

Card slot	Part number	Card type	Color code 1	2	3	4
A2202	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A2203	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A2204	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A2205	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A2206	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A2207	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2208	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2209	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2210	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2211	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2212	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2213	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2214	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2215		-	-	-	-	-
A2216	10281787	11.03 MHz oscillator	Brown	Violet	Gray	Violet
A2217	587119-100	240-ohm resistor	-	-	-	-
A2218[1]	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A2219	10281652	3-input J-K flip-flop	Brown	Blue	Green	Red
A2220	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2221	587117-102	Hex inverter	Brown	-	Violet	-
A2222	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A2223	10281602	Counter/decoder	Brown	Blue	Black	Red
A2224	10281602	Counter/decoder	Brown	Blue	Black	Red
A2225	10281610	Hex 4-bit comparator	Brown	Blue	Brown	Black
A2226	10281603	4-bit multiplexer	Brown	Blue	Black	Orange
A2227[1]	10281603	4-bit multiplexer	Brown	Blue	Black	Orange
A2228[1]	10281603	4-bit multiplexer	Brown	Blue	Black	Orange
A2229	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A2230	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2231	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2232	-	-	-	-	-	-
A2233	10281610	Hex 4-bit comparator	Brown	Blue	Brown	Black
A2234	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A2235	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A2236	587108-102	Single 8-input NAND gate	-	-	Gray	-

See footnote at end of table.

Table 5-39. Radar Integration Unit 1A1A1A4, Circuit Card Location -Continued

Card slot	Part number	Card type	Color code 1	2	3	4
A2237	10281602	Counter/decoder	Brown	Blue	Black	Red
A2238	10281609	Quint 4-bit adder	Brown	Blue	Black	White
A2239	10281603	4-bit multiplexer	Brown	Blue	Black	Orange
A2240	10281652	3-input J-K flip-flop	Brown	Blue	Green	Red
A2241	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A2242	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2243	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2244	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A2245	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A2246	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A2247	W314	Connector	-	-	-	-
A2248	W313	Connector	-	-	-	-
A2249	W315	Connector	-	-	-	-
A2250	W316	Connector	-	-	-	-
A2251	W320	Connector	-	-	-	-
		BAY 2-SHELF 3				
A2301	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A2302	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A2303	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2304	10281652	3-input J-K flip-flop	Brown	Blue	Green	Red
A2305	10281652	3-input J-K flip-flop	Brown	Blue	Green	Red
A2306	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2307	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2308	10281609	Quint 4-bit adder	Brown	Blue	Black	White
A2309	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A2310	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A2311	10281652	3-input J-K flip-flop	Brown	Blue	Green	Red
A2312	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2313	10281609	Quint 4-bit adder	Brown	Blue	Black	White
A2314	587102-102	Quad 2-input NAND gate	-	Red	-	-
A2315	587108-102	Single 8-input NAND gate		-	Gray	-
A2316	587104-102	Dual 4-input NAND gate		-	Yellow	-
A2317	587117-102	Hex inverter	Brown	-	Violet	-
A2318	587102-102	Quad 2-input NAND gate		-	Red	-
A2319	10281602	Counter/decoder	Brown	Blue	Black	Red

Table 5-39. Radar Integration Unit 1A1A1A4, Circuit Card Location -Continued

Card slot	Part number	Card type	Color code 1	2	3	4
A2320	10281602	Counter/decoder	Brown	Blue	Black	Red
A2321	10281602	Counter/decoder	Brown	Blue	Black	Red
A2322	10281780	Quad exclusive OR gate	Brown	Violet	Gray	Black
A2323	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A2324	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A2325	587117-102	Hex inverter	Brown	-	Violet	-
A2326	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2327	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A2328	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A2329	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A2330	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A2331	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A2332	587119-100	240-ohm resistor	-	-	-	-
A2333	10281602	Counter/decoder	Brown	Blue	Black	Red
A2334	587117-102	Hex inverter	Brown	-	Violet	-
A2335	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2336	10281780	Quad exclusive OR gate	Brown	Violet	Gray	Black
A2337	10281602	Counter/decoder	Brown	Blue	Black	Red
A2338	10281652	3-input J-K flip-flop	Brown	Blue	Green	Red
A2339	587108-102	Single 8-input NAND gate	-	-	Gray	-
A2340	10281609	Quint 4-bit adder	Brown	Blue	Black	White
A2341	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2342	587102-102	Quad 2-input NAND gate	-		Red	-
A2343	10281609	Quint 4-bit adder	Brown	Blue	Black	White
A2344	10281780	Quad exclusive OR gate	Brown	Violet	Gray	Black
A2345	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2346	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2347	W345	Connector	-	-	-	-
A2348	W530	Connector	-	-	-	-
A2349	W346	Connector	-	-	-	-
A2350	W347	Connector	-	-	-	-
A2351	W358	Connector	-	-	-	-

[1] Card Retainers are marked white indicating that the card must be removed from the cabinet and installed in the MTS for testing.

Table 5-40. Card Pin to Test Point Correlation

	Card Type 587XXX[1]				Card type 10281XXX					
Pin	101 to 110	107	117	124	602 603 643[2]	606 610 652 780	601	609	629	645[2]
1	2B	2A	2B	5A		2B			3B	2B
2	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND
3	2A	3A	3A	6A	2B	3B	2B	2B	3A	
4	4A		2A		2A	2A	2A	2A	2B	4A
5	3B	4B	3B		3B	4B	3B	3B	4A	3B
6	5A	5A	14A	3A	3A	3A	3A	4B	5A	
7	3A	5B	4B		4B	5B	4B	4B	6B	3A
8	6A	8B	4A	12A	4A	4A	4A	4A	6A	6A
9	4B	10B	5B	7A	5B	6B	5B	5B	2A	4B
10	7A	9B	6B	13A	5A	5A	5A	5A	7A	7A
11	5B	11B	7A		6B	7B	6B	6B	5A	5B
12	+5V	+5V	+5V	+5V	+5V	+5V	+5V	+5v	+5V	+5V
13	6B		6A		7A	7A	7A	7A		6B
14	9A		8B		6A	6A	6A	6A		9A
15	7B		7B		7B	8B	7B	7B	5B	
16	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND
17	8B		9B	25A	8B	9B	8B	8B	17A	
18	10A		9A	18A	9A	9A	9A	9A	8B	10A
19	9B		10B	24A	9B	10B	9B	9B	15B	9B
20	11A		10A	19A	10A	10A	10A	10A	15A	11A
21	10B		11B	26A	10B	11B	10B	10B		10B
22	12A		14A	2B	11A	11A	11A	11A	12B	12A
23	11B		12A		11B	12B	11B	11B		11B
24	13A		13A		12A	12A	12A	12A		13A
25	12B		11A		12B	14A	12B	12B		12B
26	14A		12B	3B	13A	13A	13A	13A	16A	14A
27	13B		13B		14A	13B	14A	14A		
28	+5V	+5V	+5V	+5V	+5V	+5V	+5V	+5V	+5V	+5V
29	14B		15B	10A	13B	14B	13B	13B		
30	15A		14B		15A	15A	15A	16A	13A	15A
31	15B		16B		14B	15B	14B	15A		15B
32	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND
33	16B	17B	9A	16A	16B	16A	14B	18B	16B	
34	16A	12B	15A	15A	15B	16A	15B	15B	14B	16A
35	17B		13A		16B	17B	16B	16B	19B	17B
36	17A	13B	16A	16A	17A	17A	17A	17A	19A	17A
37	18B	17A	22A	17B	18B	17B	17B	22B	18B	
38	18A	6B	20A		18A	18A	18A	18A	20A	18A
39	19B		19B	21A	15B	19B	18B	18B	24B	
40	19A	7B		19A	19A	19A	19A	19A	31A	19A
41	22B		23B		19B	22B	19B	19B	26B	
42	20A	23B			20A	20A	20A	20A	23B	20A
43	23B	24B			22B	23B	22B	22B	23B	23B
44	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND

See footnotes at end of table.

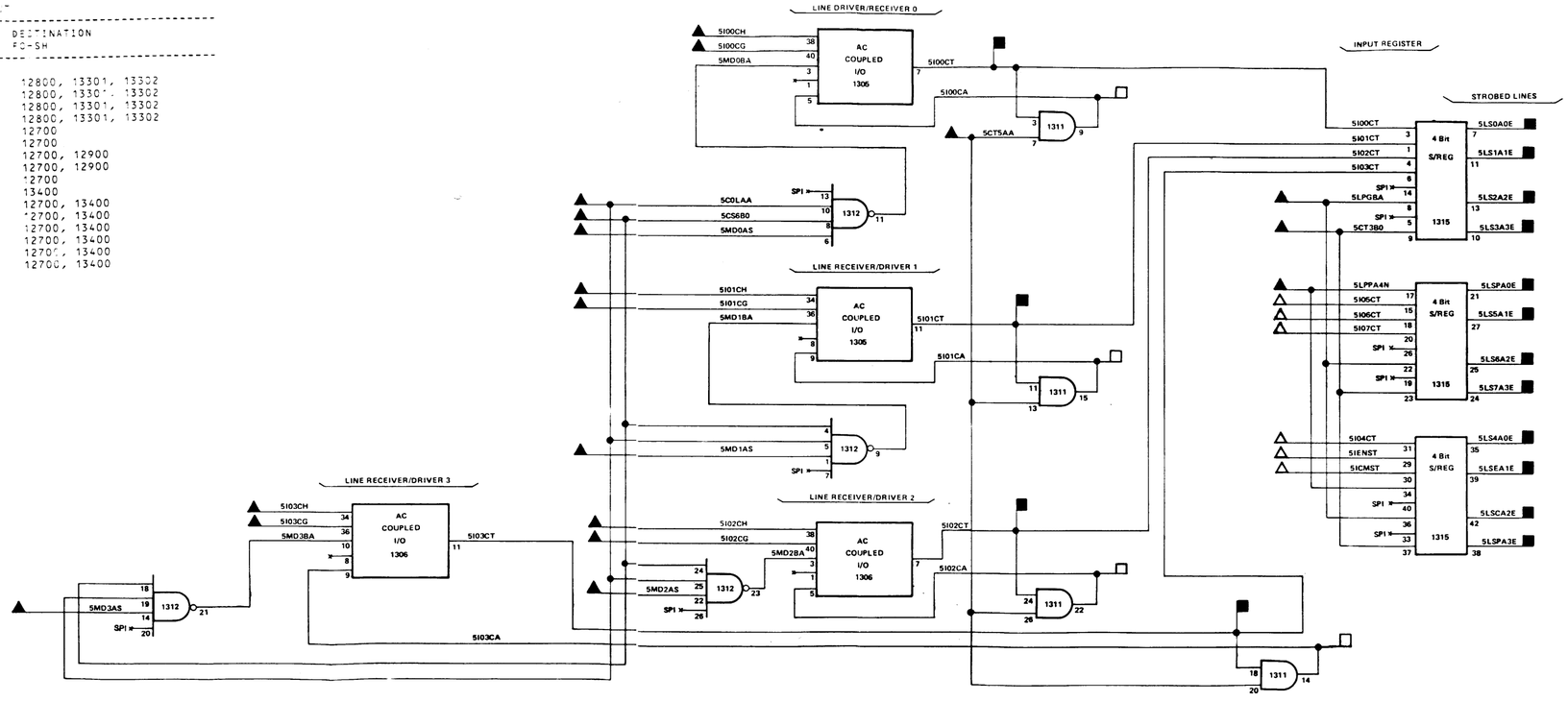
Table 5-40. Card Pin to Test Point Correlation-Continued

	Card Type 587XXX[1]				Card type 10281XXX					
Pin	101 to 110	107	117	124	602 603 643[2]	606 610 652 780	601	609	629	645[2]
45	24B		25B		23B	24B	23B	23B		24B
46	21A		23B		21A	21A	21A	21A	25B	21A
47	25B		24A		23A	25B	23A	23A		25B
48	22A		21A		22A	22A	22A	22A	31B	22A
49	26B		23A		24B	26B	24A	24B		26B
50	23A		22A		24A	23A		24A	28A	23A
51	27B		27B		25B	27B	25B	25A		
52	24A		26A	20A	25A	24A	26B	25B	29B	24A
53	28B		26B		26B	28B	26A			26A
54	25A		25A		26A	25A	27B	26B		26A
55	29B		29B		27B	29B	28B	28B		29B
56	26A		28B		28B	26A	28A	27B		26A
57	30B		30B		29B	28A	29B	29B		30B
58	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND
59	31B		31B		30B	30B	30B	30B		31B
60	28A		28A		28A	29A	29A	28A		28A
61	32B		31A		31B	31B	31B	31B		32B
62	29A		29A		29A	30A	30A	29A	32A	29A
63	33B		30A		31A	32B				31A
64	30A		33A		30A	31A	31A	30A		30A
65	34B		33B		33B		33B			
66	31A		32A		32B	32A	33A	32B	31A	
67	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND
68	32A		32B		33A	33A	34A	32A		32A
69	35A		35B		32A	34B	33B	33B	32B	35A
70	33A		34B		34A	34A	35A	33A	34B	33A
71	36A		36B		33B	35B	34B	34B	36B	36A
72	34A		34A		35A	35A	36A	34A	35B	34A
73	36B		37B		34B	36B	35B	35B	38B	36B
74	35B		35A		36A	36A	26A	25A	33A	35B
75	36B		39A		35B	36B	36B	36B	38A	37B
76	37A		37A		37A	37A	36A	36A	37B	37A
77	35B		38A		36B	38B	37B	37B		38B
78	33A		36A		39A	38A		37A	34A	38A
79	39B		39B		37B	39B	38B	38B	39B	39B
80	39A		38B		38B	39A		38A		

[1]10283XXX card types have identical IC/test point/card pin correlation as 587XXX card types as follows:
10283626 is same as 587102
10283627 is same as 587108
10283628 is same as 587103
10283629 is same as 587106
10283630 is same as 587107
[2]10283XXX

☆ U S GOVERNMENT PRINTING OFFICE 1991-531-038/40372

INPUT		OUTPUT
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
5CS6BC	13000	5100CT	12800, 13301, 13302
5CT3BO	12800	5101CT	12800, 13301, 13302
5CT5AA	12800	5102CT	12800, 13301, 13302
5C0LAA	13400	5103CT	12800, 13301, 13302
5C0LAA	26802	5LSCA2E	12700
5C0LAA	27001	5LSEA1E	12700
5100CG	26803	5LSPA0E	12700, 12900
5100CG	31702	5LSPA3E	12700, 12900
5100CH	26803	5LS0A0E	12700
5100CH	31702	5LS1A1E	13400
5101CG	26803	5LS2A2E	12700, 13400
5101CG	31702	5LS3A3E	12700, 13400
5101CH	26803	5LS4A0E	12700, 13400
5101CH	31702	5LS5A1E	12700, 13400
5102CG	26803	5LS6A2E	12700, 13400
5102CG	31702	5LS7A3E	12700, 13400
5102CH	26803		
5102CH	31702		
5103CG	26803		
5103CG	31702		
5103CH	26803		
5103CH	31702		
5LPGBA	12800		
5LPPA4N	12800		
5MD0AS	13800		
5MD1AS	13800		
5MD2AS	13800		
5MD3AS	13800		



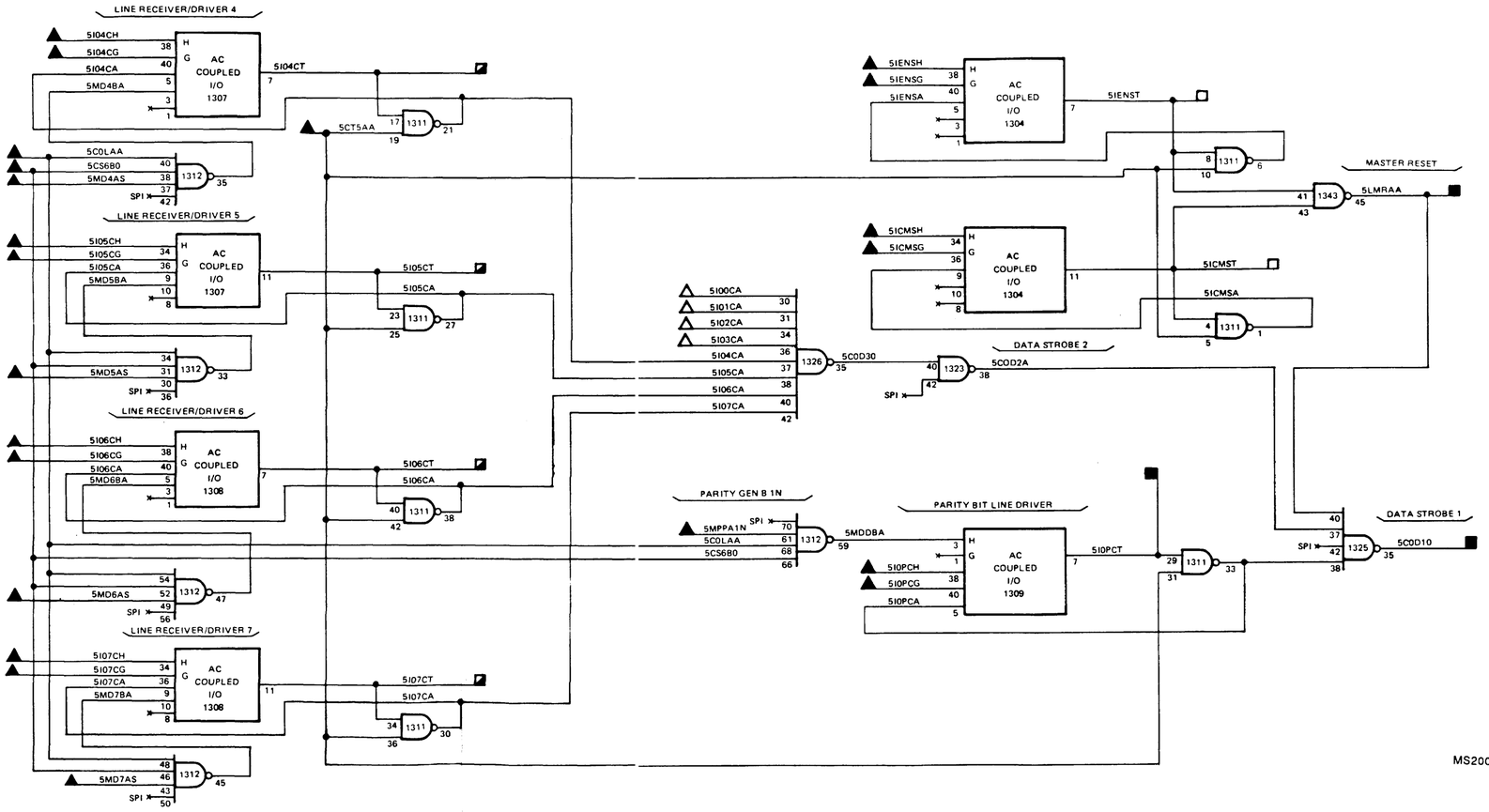
NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS PREFIX WITH APPLICABLE UNIT ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1. MJ BAY 1 CARD CAGE (1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - INDICATES INPUT FROM ANOTHER FIGURE
   - INDICATES INPUT FROM THE SAME FIGURE
   - INDICATES OUTPUT TO ANOTHER FIGURE
   - INDICATES OUTPUT TO THE SAME FIGURE
   - INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT CABLING.
7. REFER TO FIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
   A. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
   B. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1123, A1139, A1149 AND A1227

MS2000727B

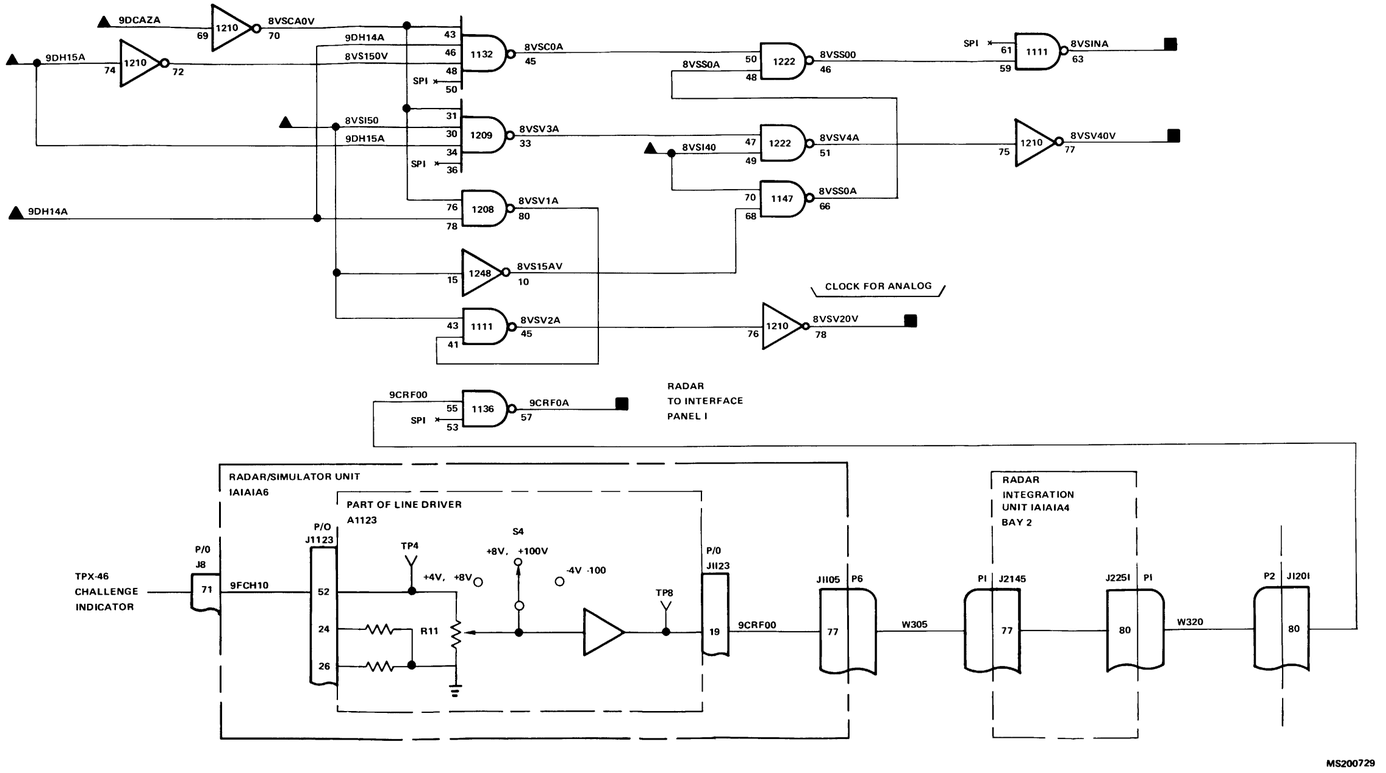
Change 3 FO-126. IOB Register and Interface Circuits Logic Diagram (Sheet 1 of 3)

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
5CS6B0	13000	5C0D10	12800
5CT5AA	12800	510PCT	12800
5C0LAA	13400	5104CT	12800, 13301
5C0LAA	26802	5105CT	12800, 13301
5C0LAA	27001	5106CT	12800, 13301
51CMSG	26803	5107CT	12800, 13301
51CMSG	28301	5LMRAA	12900, 13400
51CMSG	31702		
51CMSH	26803		
51CMSH	28301		
51CMSH	31702		
51ENSG	26803		
51ENSG	28301		
51ENSG	31702		
51ENSH	26803		
51ENSH	28301		
51ENSH	31702		
510PCG	26803		
510PCG	31702		
510PCH	26803		
510PCH	31702		
5104CG	26803		
5104CG	31702		
5104CH	26803		
5104CH	31702		
5105CG	26803		
5105CG	31702		
5105CH	26803		
5105CH	31702		
5106CG	26803		
5106CG	31702		
5106CH	26803		
5106CH	31702		
5107CG	26803		
5107CG	31702		
5107CH	26803		
5107CH	31702		
5MD4AS	13800		
5MD5AS	13800		
5MD6AS	13800		
5MD7AS	13800		
5MPPA1N	12800		



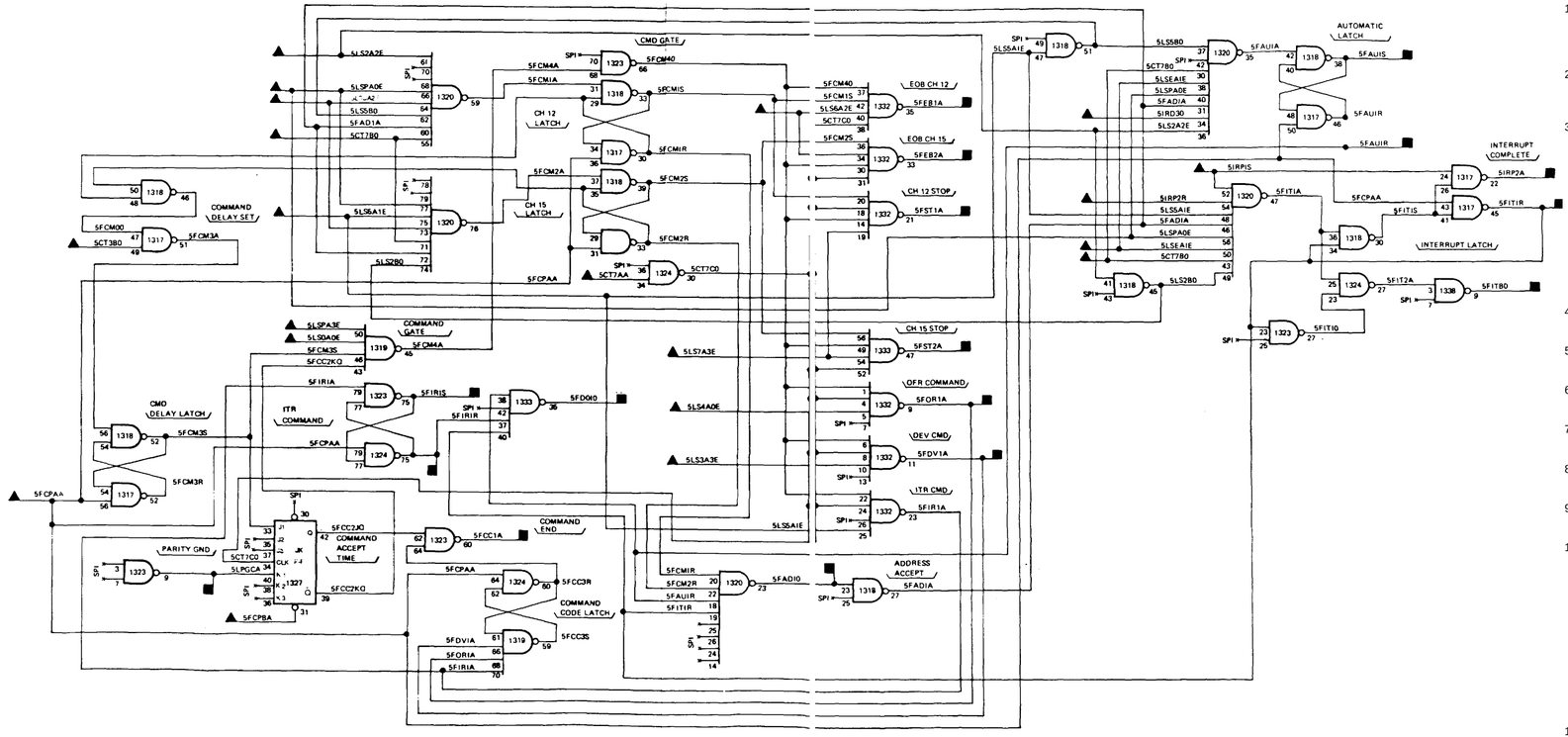
Change 3 FO-126. IOB Input Register and Interface Circuits Logic Diagram (Sheet 2 of 3)

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
8VSI40	14000	8VSINA	18600, 18700, 26803
8VSI40	26901	8VSV20V	26803
8VSI50	14000	8VSV40V	26803
8VSI50	26901	9CRF0A	26803, 27001
9DCAZA	18700		
9DCAZA	26803		
9DH14A	18600		
9DH14A	26803		
9DH15A	18600		
9DH15A	26803		



FO-126. IOB Input Register and Interface Circuits Logic Diagram (Sheet 3 of 3)

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
5CT3B0	12800	5FAD10	12900
5CT7AA	12800	5FAU1R	13200, 13700
5CT7B0	12800	5FAU1S	13000, 13200
5FCPAA	12900	5FCC1A	12900
5FCPBA	12900	5FDV1A	12900
5IRD30	13200	5FDD1D	12800, 12900, 13000
5IRP1S	13500	5FEB1A	13102
5IRP2R	13500	5FEB2A	13102
5LSCA2E	12601	5FIR1R	12900
5LSEA1E	12601	5FIR1S	12900, 13000, 13600
5LSPA0E	12601	5FITB0	13101, 13102
5LSPA3E	12601	5FIT1R	12900, 13000
5LSQA0E	12601	5FST1A	13200, 13400
5LS2A2E	12601	5FST2A	13400
5LS3A3E	12601	5FOR1A	12900
5LS4A0E	12601	5IRP2A	13500
5LS5A1E	12601	5LPGCA	13101, 13102, 13200
5LS6A2E	12601		
5LS7A3E	12601		

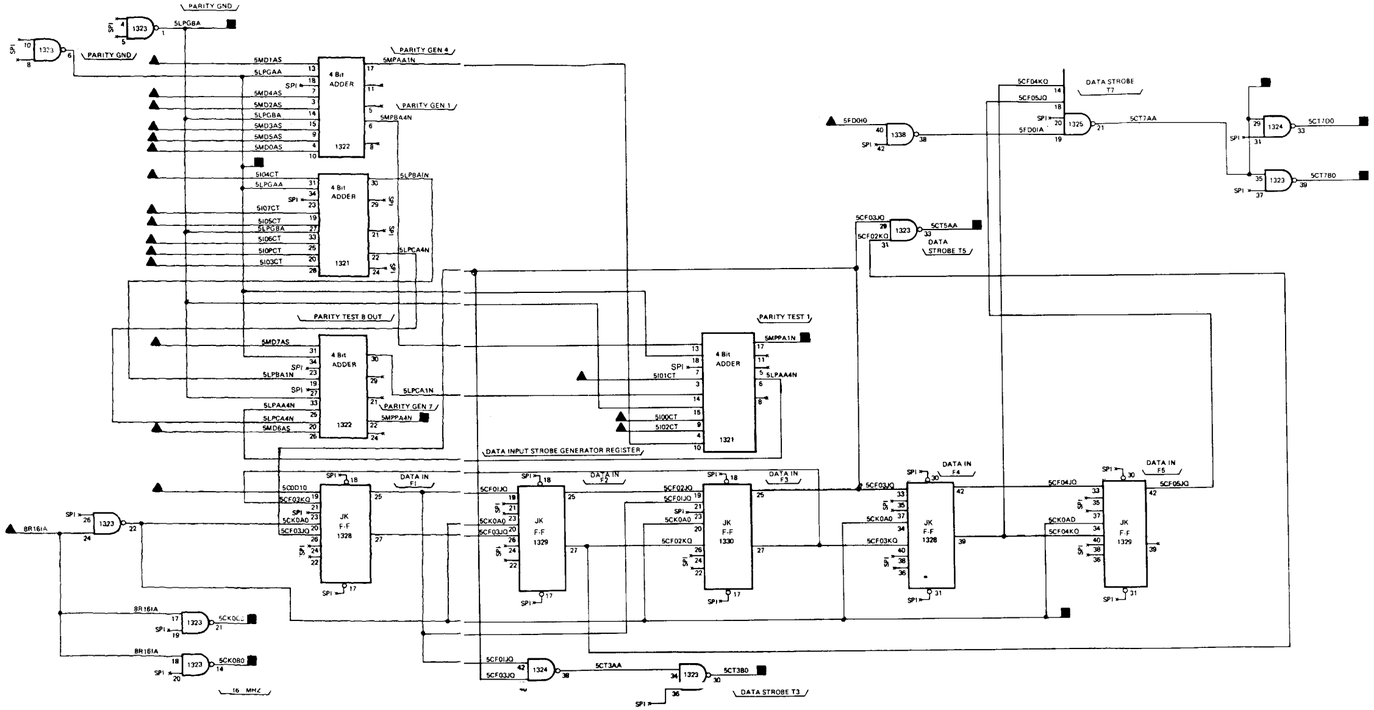


Change 3 FO-127. IOB Function Commands Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1. RIU BAY 1 CARD CAGE (1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1123. A1139. A1149 AND A1227

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
SCDD10	12602	SCK0AD	13301
SFD010	12700	SCK0BD	12900, 13000
SI0PCT	12602	SCK0CD	12900, 13200, 13500
SI00CT	12601	SCT3BD	12601, 12700, 12900, 13301
SI01CT	12601	SCT5AA	12601, 12602
SI02CT	12601	SCT7AA	12700
SI03CT	12601	SCT7BD	12700, 12900, 13101, 13102
SI04CT	12602	SCT7DD	12900, 13400
SI05CT	12602	SLPGAA	12900, 13000, 13302
SI06CT	12602	SLPGBA	12601
SI07CT	12602	SLPPA4N	12601
SMD0AS	13800	SMPPA1N	12602
SMD1AS	13800		
SMD2AS	13800		
SMD3AS	13800		
SMD4AS	13800		
SMD5AS	13800		
SMD6AS	13800		
SMD7AS	13800		
BR161A	14300		

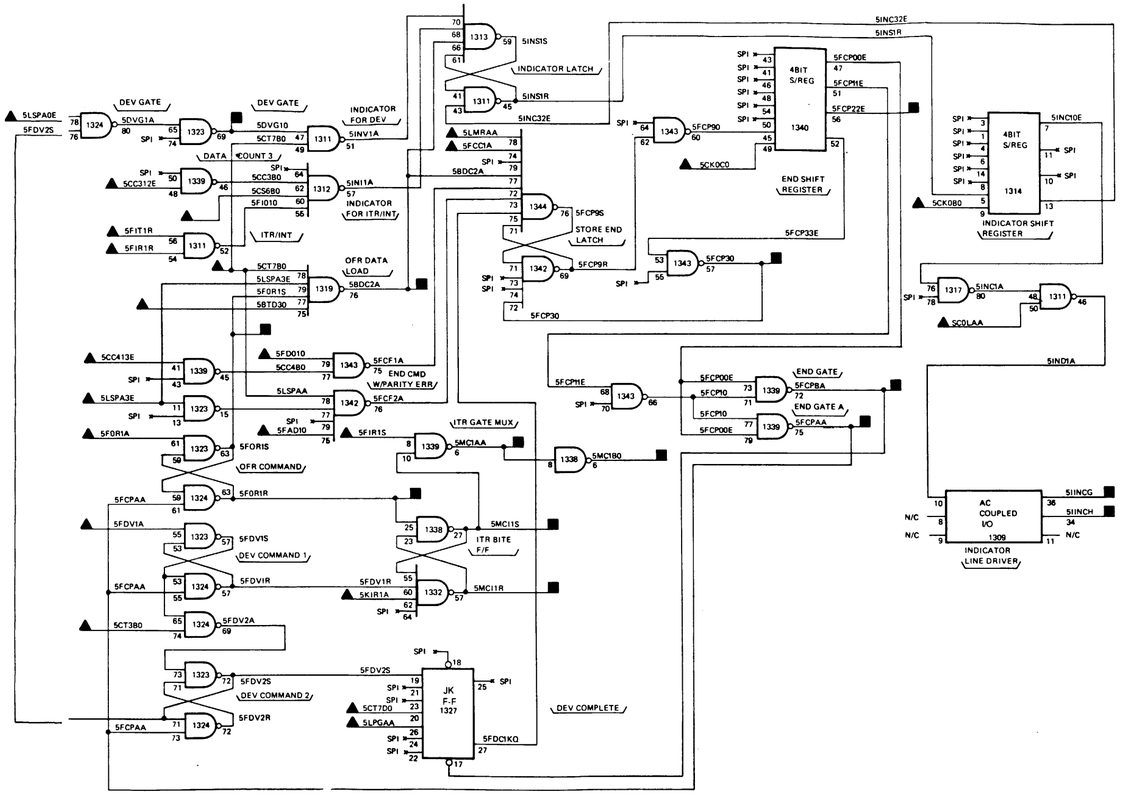


Change 3 FO-128. IOB Parity Generation Checker Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1. RIU BAY 1 CARD CAGE (1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1123, A1139 A1149 AND A1227

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
5BTD30	13301	5BDC2A	13301
5CC312E	13000	5DVG10	13400
5CC413E	13000	5FCPAA	12700
5CK0B0	12800	5FCPBA	12700
5CK0C0	12800	5FCP22E	13000
5CS6B0	13000	5FCP30	13000
5CT3B0	12800	5FOR1R	13301
5CT7B0	12800	5FOR1S	13301
5CT7D0	12800	5I1NCG	26803, 31702
5C0LAA	13400	5I1NCH	26803, 31702
5C0LAA	26802	5MC11R	13000
5C0LAA	27001	5MC11S	13000
5FAD10	12700	5MC1AA	13000
5FCC1A	12700	5MC1B0	13800
5FDV1A	12700		
5FD010	12700		
5FIR1R	12700		
5FIR1S	12700		
5FIT1R	12700		
5FOR1A	12700		
5KIR1A	13400		
5LMRAA	12602		
5LPGAA	12800		
5LSPA0E	12601		
5LSPA3E	12601		



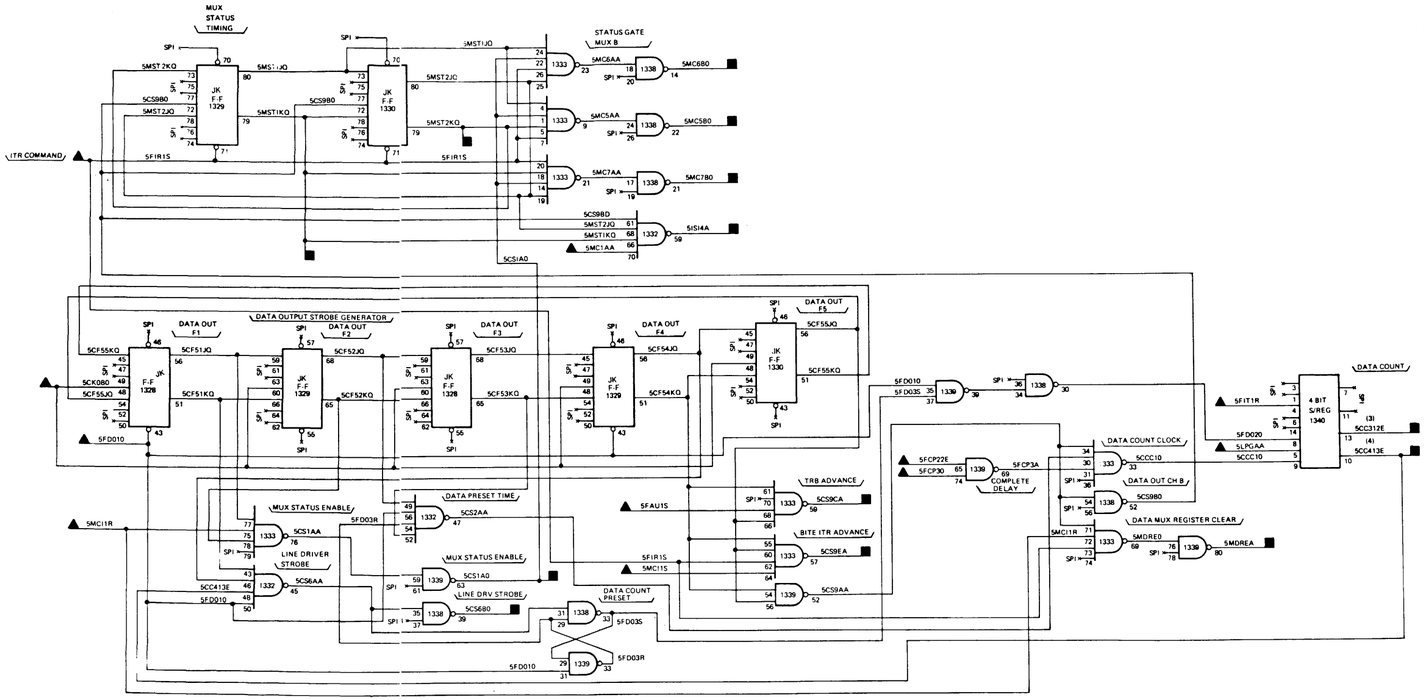
Change 3 FO-129. IOB Command Control Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1. RIU BAY 1 CARD CAGE (1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1123, A1139 A1149 AND A1227

INPUT SIGNAL	INPUT SOURCE FO-SH
5CK0B0	12800
5FAU1S	12700
5FCP22E	12900
5FCP30	12900
5FD010	12700
5FIR1S	12700
5FIT1R	12700
5LPGAA	12800
5MC11R	12900
5MC11S	12900
5MC1AA	12900

OUTPUT SIGNAL	OUTPUT DESTINATION FO-SH
5CC312E	12900
5CC413E	12900
5CS1A0	13600
5CS6B0	12601, 12602, 12900
5CS9CA	06700, 26803, 28101
5CS9EA	05400, 26803, 28202
5IS14A	13500, 13600
5MC5B0	13400, 13600, 13800
5MC6B0	13600
5MC7B0	13600, 13800
5MDREA	13800
5MST1KQ	13600
5MST2KQ	13600



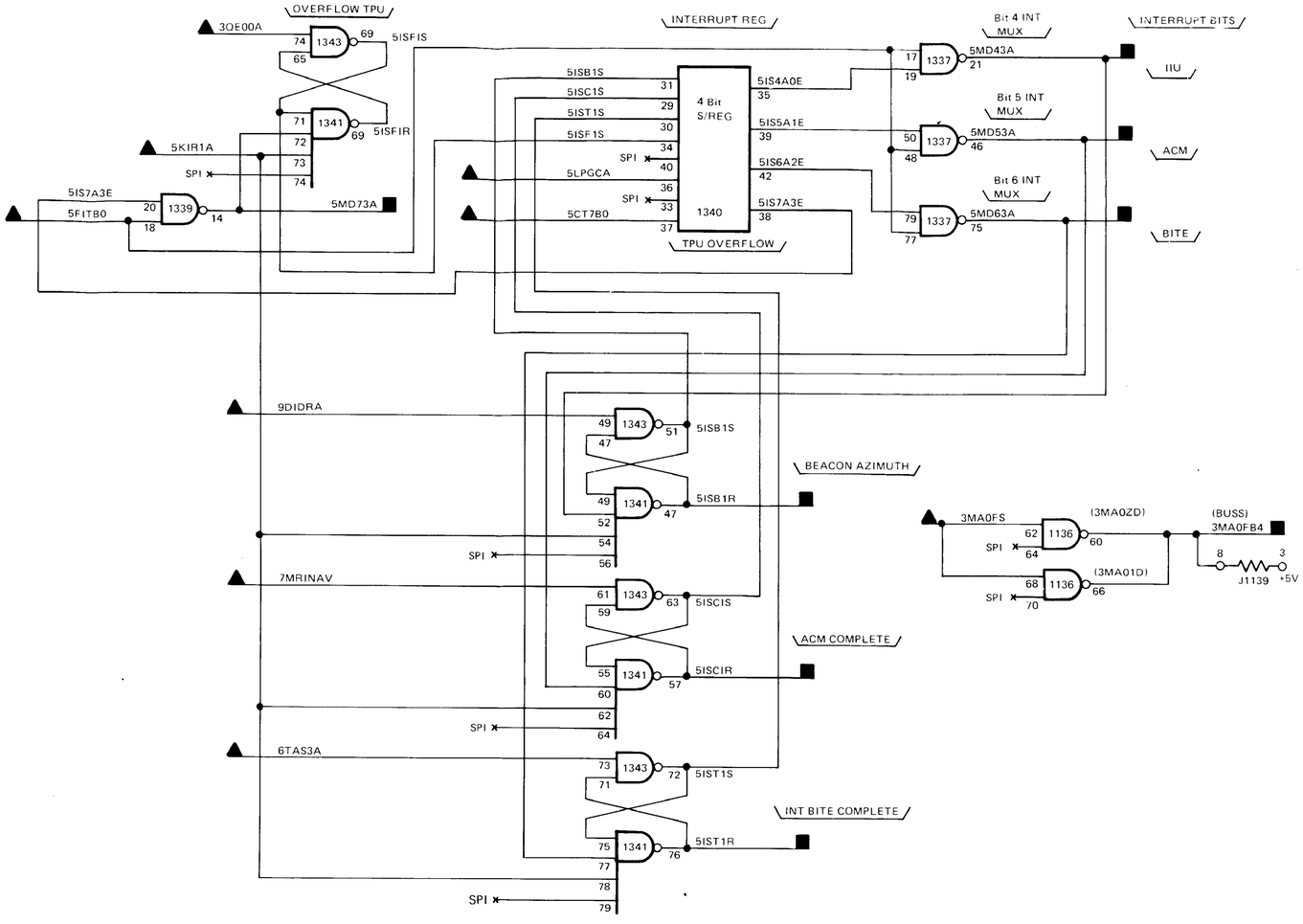
Change 3 FO-130. IOB Data Output Strobe Generator Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1.
   RIU BAY 1 CARD CAGE (1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1123, A1139 A1149 AND A1227

INPUT	
SIGNAL	SOURCE FO-SH
3MA0FS	11900
3MA0FS	26803
3MA0FS	28102
3QE00A	07701
3QE00A	26803
3QE00A	28102
5CT7B0	12800
5FITB0	12700
5KIR1A	13402
5LPGCA	12700
6TAS3A	05200
6TAS3A	26803
6TAS3A	28202
7MRINAV	02400
7MRINAV	26803
7MRINAV	28201
9DIDRA	18600
9DIDRA	26803

OUTPUT	
SIGNAL	DESTINATION FO-SH
3MA0FB4	26803
5ISB1R	13500
5ISC1R	13500
5IST1R	13500
5MD43A	13800
5MD53A	13800
5MD63A	13800
5MD73A	13800



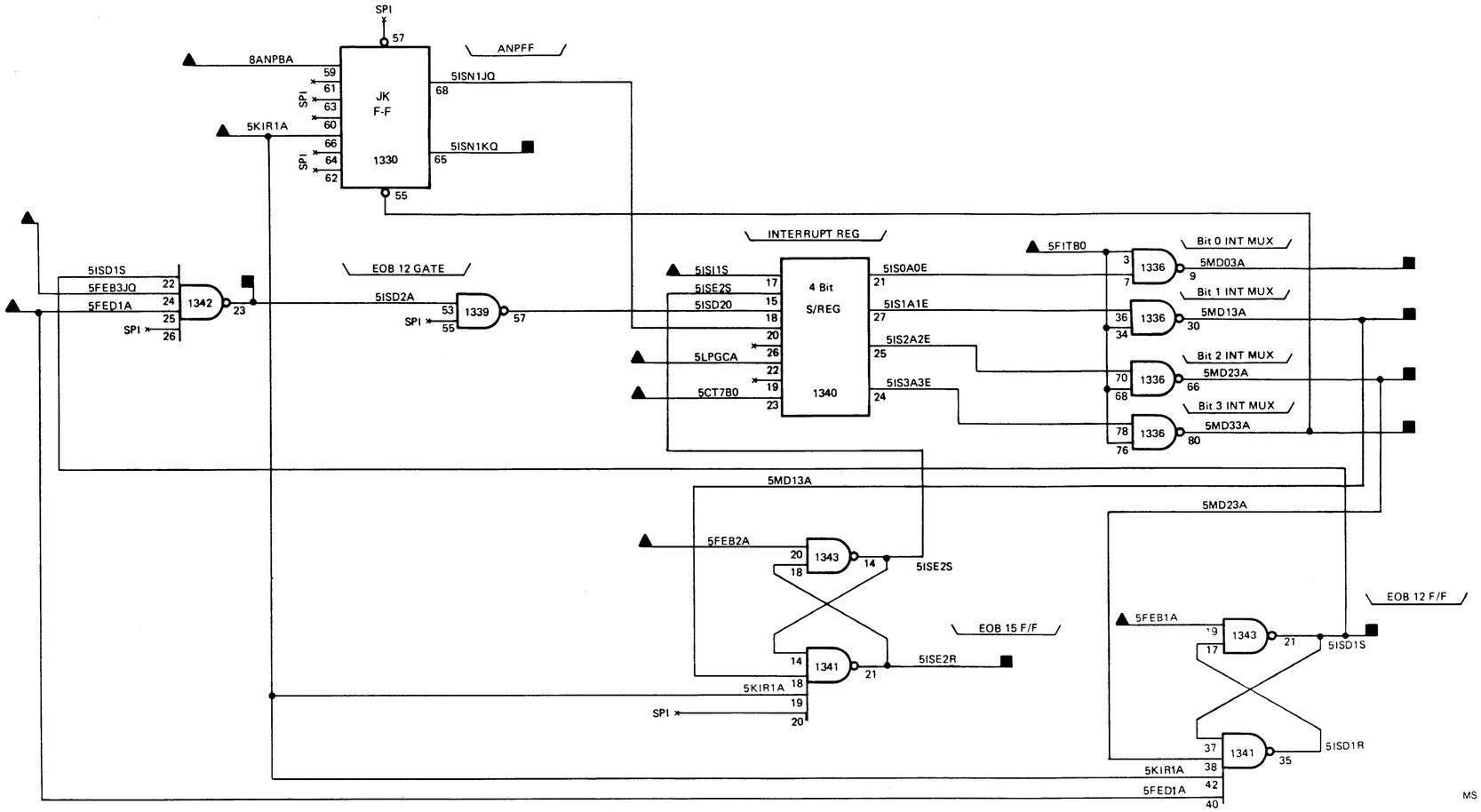
NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN; FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, RIU BAY 1 CARD CAGE (1A1A1A4).
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE.
   - △ INDICATES INPUT FROM THE SAME FIGURE.
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE.
   - □ INDICATES OUTPUT TO THE SAME FIGURE.
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE.
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1123, A1139, A1149 AND A1227

MS200734A

FO-131. IOB Interrupt Register Logic Diagram (Sheet 1 of 2)

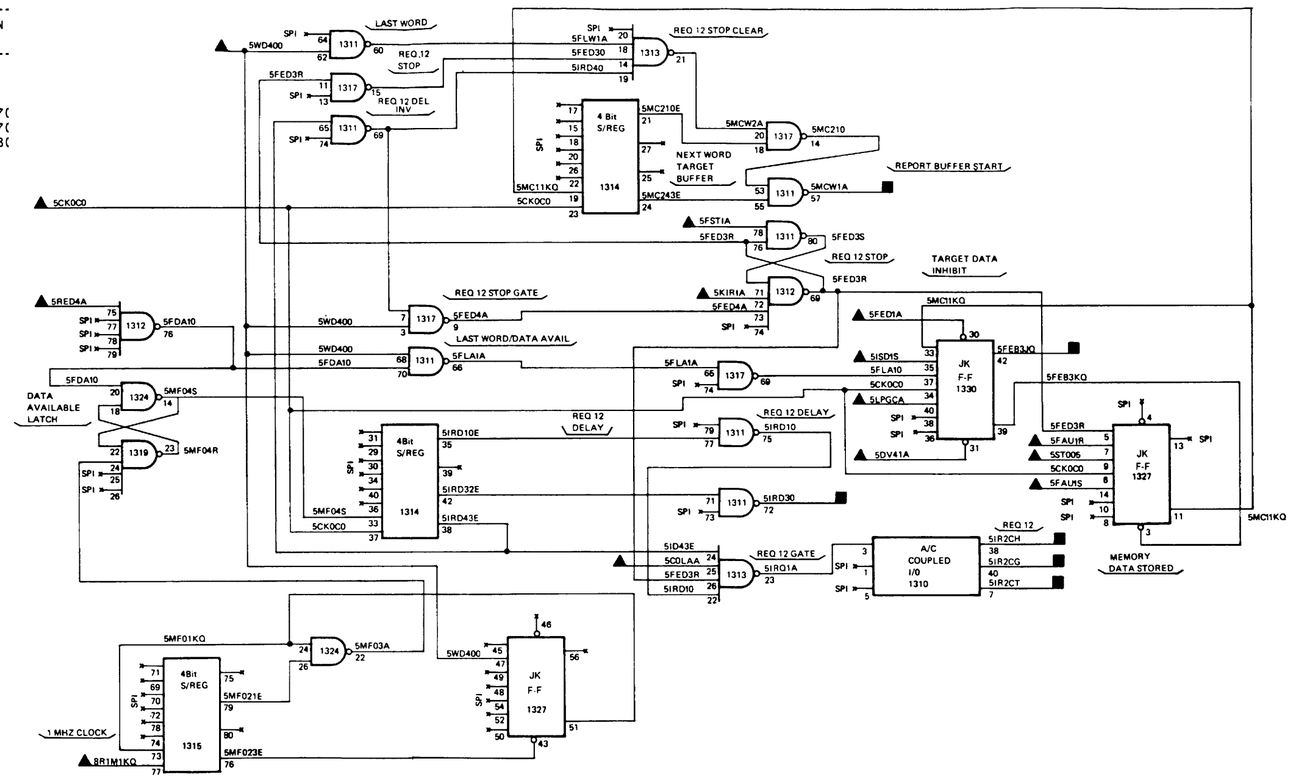
INPUT		OUTPUT		
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH	
5CT7B0	12800	5ISD1S	13200	
5FEB1A	12700	5ISD2A	13500	
5FEB2A	12700	5ISE2R	13500	
5FEB3JQ	13200	5ISN1KQ	13500	
5FED1A	13400	5MD03A	13500	13800
5FITB0	12700	5MD13A	13800	
5ISI1S	13500	5MD23A	13800	
5KIR1A	13400	5MD33A	13800	
5LPGCA	12700			
8ANPBA	17200			



Change 3 *FO-131. IOB Interrupt Register Logic Diagram (Sheet 2 of 2)*

INPUT	
SIGNAL	SOURCE FO-SH
5CK0C0	12800
5C0LAA	13400
5C0LAA	26802
5C0LAA	27001
5DV41A	13400
5FAU1R	12700
5FAU1S	12700
5FED1A	13400
5FST1A	12700
5ISD1S	13102
5KIR1A	13400
5LPGCA	12700
5RED4A	06700
5RED4A	26803
5RED4A	28101
5ST00S	07202
5ST00S	26803
5ST00S	28101
5WD400	07000
5WD400	26803
5WD400	28101
8R1M1JQ	14300

OUTPUT	
SIGNAL	DESTINATION FO-SH
5FEB3JQ	13102
5IRD3D	12700
5IR2CG	26803, 317(
5IR2CH	26803, 317(
5MCW1A	06700, 268(



NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1.
   RIU BAY 1 CARD CAGE (1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1123, A1149 AND A1227

MS200736A

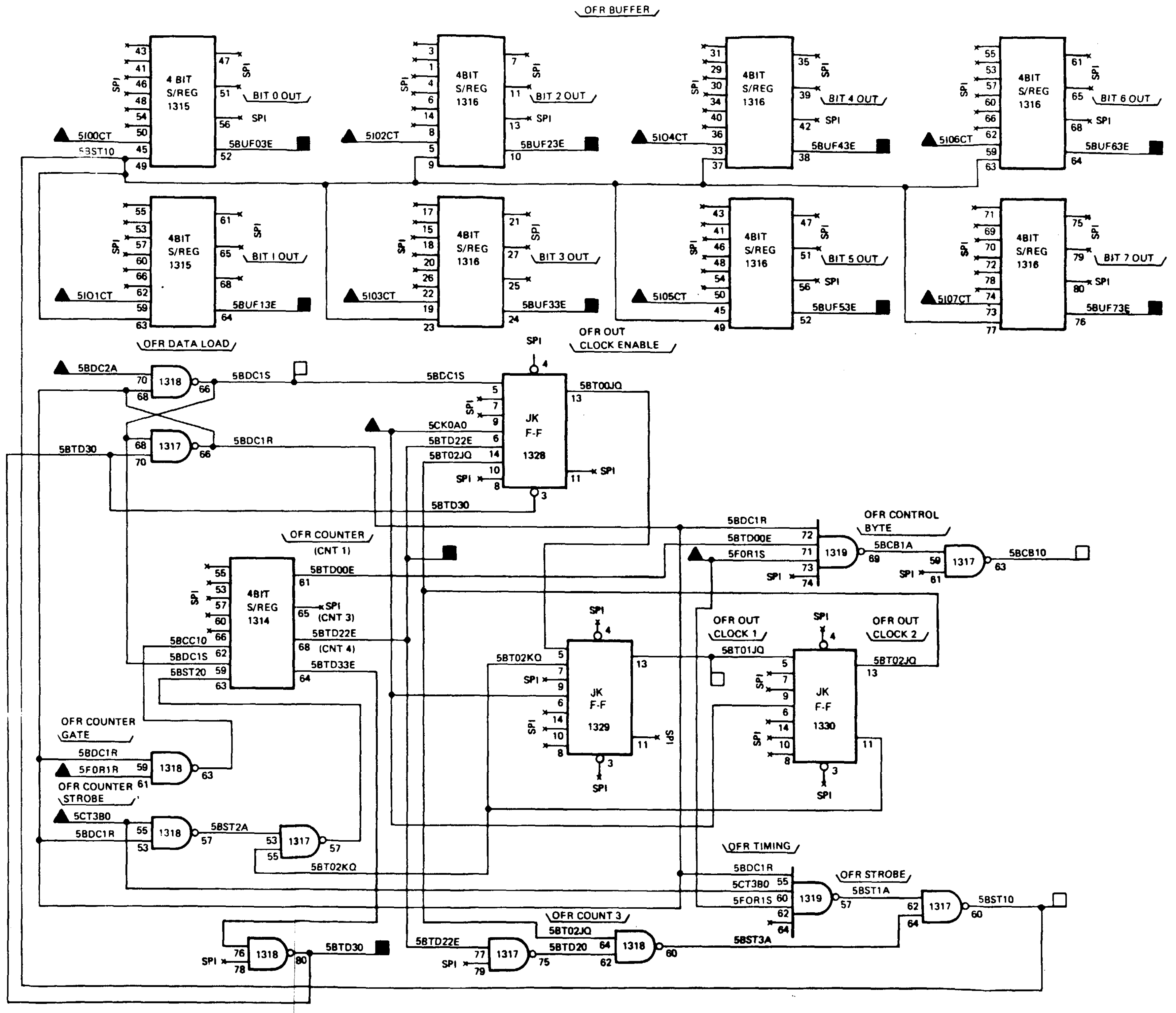
Change 3 FO-132. IOB I/O Logic Diagram

INPUT

SIGNAL	SOURCE FO-SH
5BDC2A	12900
5CKDA0	12800
5CT3B0	12800
5FOR1R	12900
5FOR1S	12900
5I00CT	12601
5I01CT	12601
5I02CT	12601
5I03CT	12601
5I04CT	12602
5I05CT	12602
5I06CT	12602
5I07CT	12602

OUTPUT

SIGNAL	DESTINATION FO-SH
5BTD22E	14000
5BTD30	12900
5BUF03E	02300, 05300, 05400, 13900, 18600, 26802, 26803, 28201
5BUF13E	02300, 05300, 05400, 13900, 18600, 26802, 26803, 28201
5BUF23E	02300, 05300, 05400, 13900, 18600, 26802, 26803, 28201
5BUF33E	02300, 05300, 05400, 13900, 18600, 26802, 26803, 28201
5BUF43E	02300, 05300, 05400, 13900, 18600, 26802, 26803, 28201
5BUF53E	02300, 05300, 05400, 13800, 13900, 18600, 26802, 26803, 28201
5BUF63E	02300, 05300, 05400, 13800, 13900, 18600, 26802, 26803, 28201
5BUF73E	02300, 05300, 05400, 13800, 13900, 18600, 26802, 26803, 28201

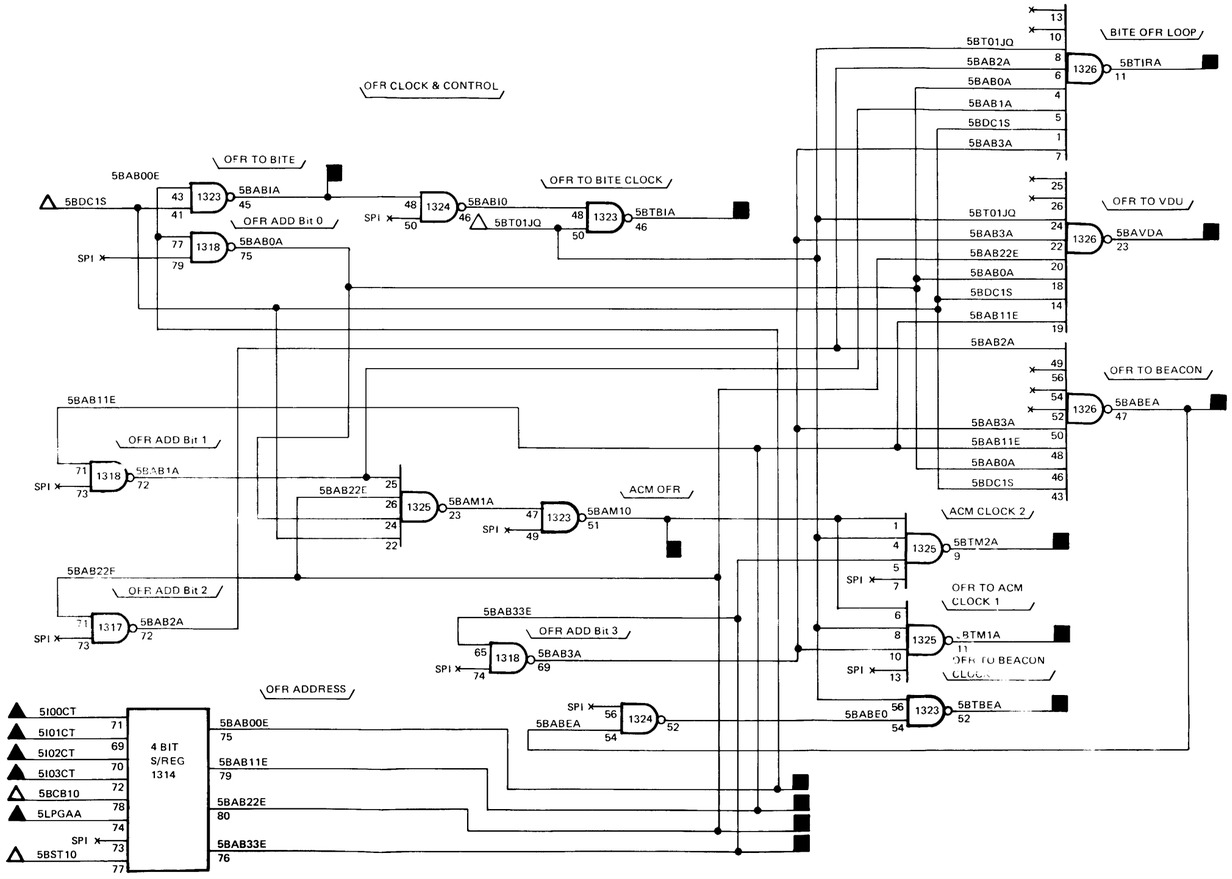


NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1. RIU BAY 1 CARD CAGE (1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1123, A1149 AND A1227

Change 3 FO-133. IOB OFR Data Address and Clock Logic Diagram (Sheet 1 of 2)

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
5I00CT	12601	5BABEA	18600, 26803
5I01CT	12601	5BABIA	05200, 05300, 26802, 26803
5I02CT	12601	5BAB00E	13600
5I03CT	12601	5BAB11E	13600
5LPGAA	12800	5BAB22E	13600
		5BAB33E	13600
		5BAM10	01501, 02300, 26802, 26803, 28201
		5BAVDA	13900
		5BTBEA	18600, 26803
		5BTBIA	05300, 26803, 28202
		5BTIRA	05400, 26803, 28201
		5BTM1A	01501, 26803, 28201
		5BTM2A	01501, 26803, 28201



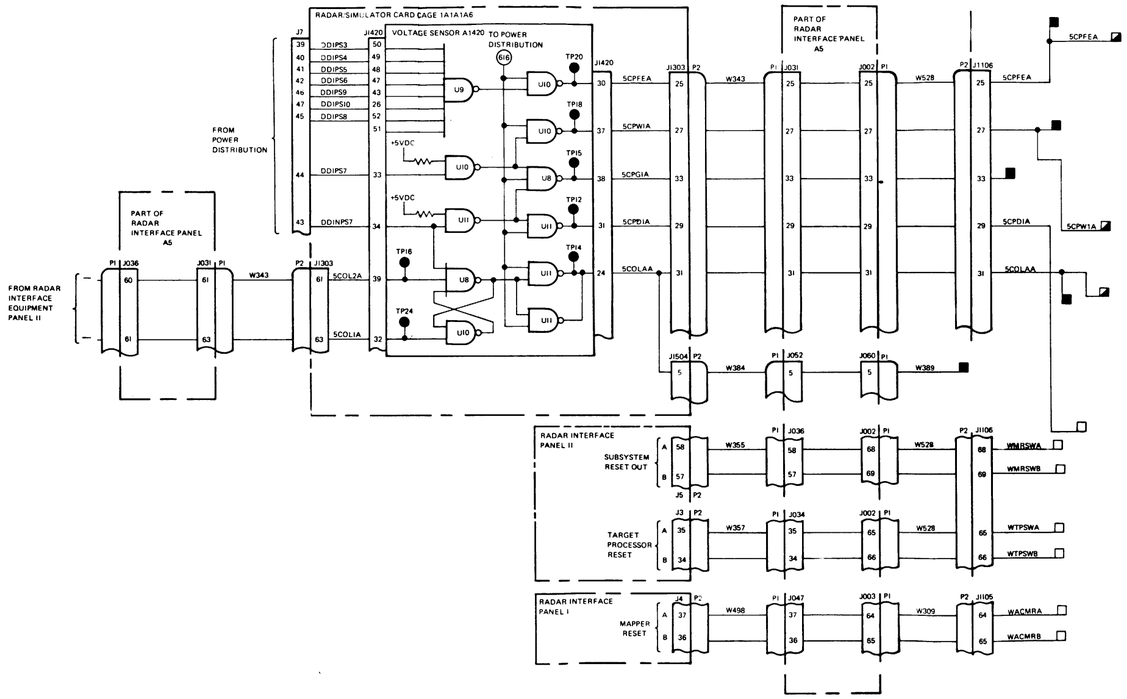
NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1.
   RIU BAY 1 CARD CAGE (1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1123, A1149 AND A1227

MS200738

Change 3 FO-133. IOB OFR Data Address and Clock Logic Diagram (Sheet 2 of 2)

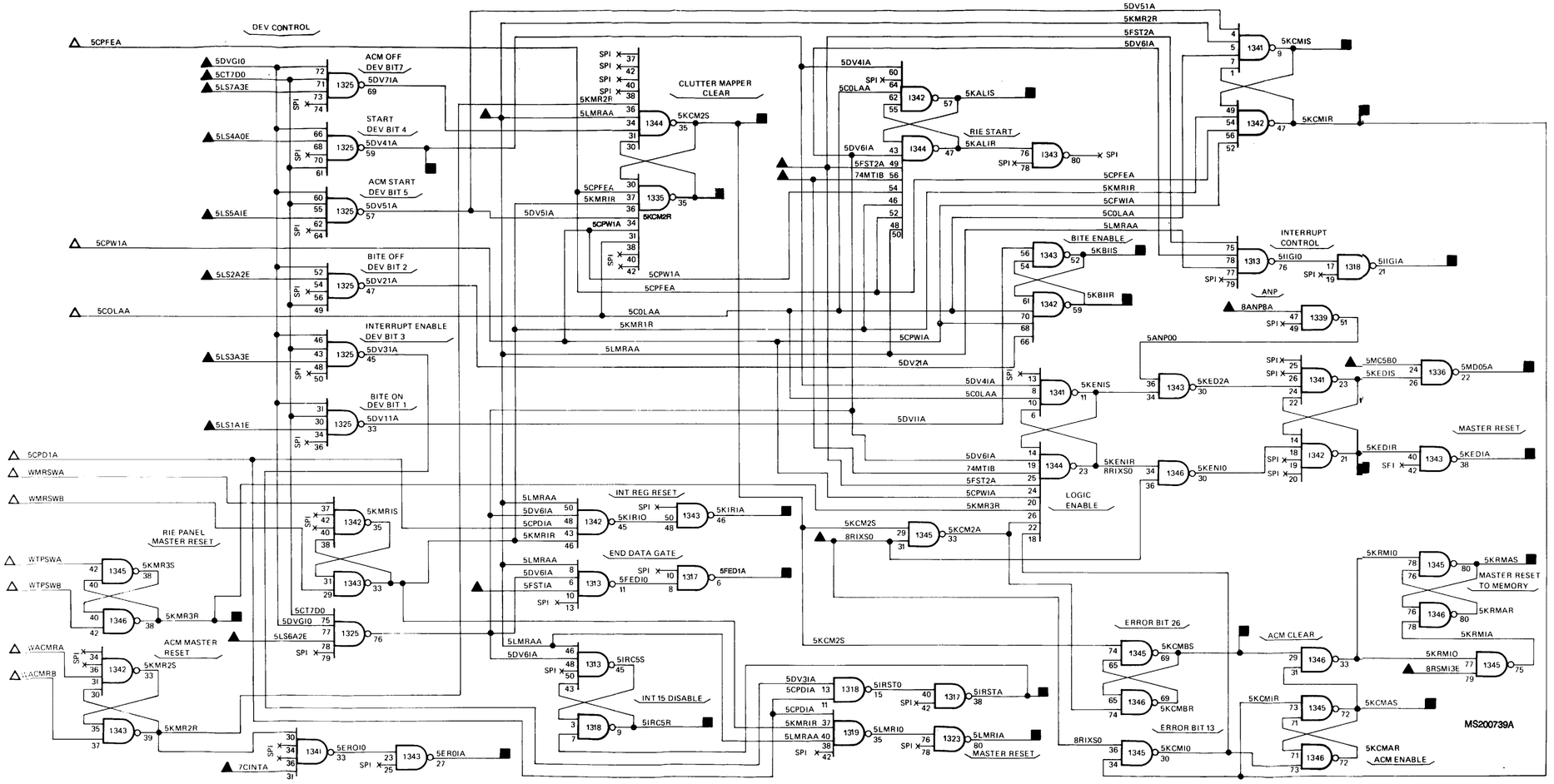
INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
5CT7D0	12800	5CPFEA	13500, 28901
5DVG10	12900	5CPG1A	13600, 28901
5FST1A	12700	5CPW1A	13500, 28901
5FST2A	12700	5COLAA	05101, 07202, 12601, 12602,
5LMR4A	12602		12900, 13200, 13500, 13600,
5LS1A1E	12601		26803, 28901
5LS2A2E	12601	5DV41A	13200
5LS3A3E	12601	5ERD1A	13500
5LS4A0E	12601	5FED1A	13102, 13200
5LS5A1E	12601	5IIG1A	13500
5LS6A2E	12601	5IRC5R	13500
5LS7A3E	12601	5IRSTA	13500
5MC5B0	13000	5KAL1S	13800
7CINTA	01501	5KBI1S	13800, 28201
7CINTA	26803	5KCMAS	26803
74MT1B	05101	5KCMBS	26803
74MT1B	28202	5KCM1S	00100, 01501, 01700, 02700,
8ANPBA	17200		05101, 13600, 26802
8RSM13E	14300	5KCM2S	13800
8R1XS0	14300	5KED1A	06600, 06700, 07701, 12300,
			13500, 15100, 15200, 15800,
			18300, 18500, 19600, 26802,
			26803, 28101
		5KIR1A	12900, 13101, 13102, 13200,
			13500
		5KMR3R	13500
		5KRMAS	13700
		5LMR1A	14300
		5MD05A	13800



Change 3 FO-134. IOB Devise Interrupt and Reset Control Logic Diagram (Sheet 1 of 2)

NOTES: UNLESS OTHERWISE SPECIFIED

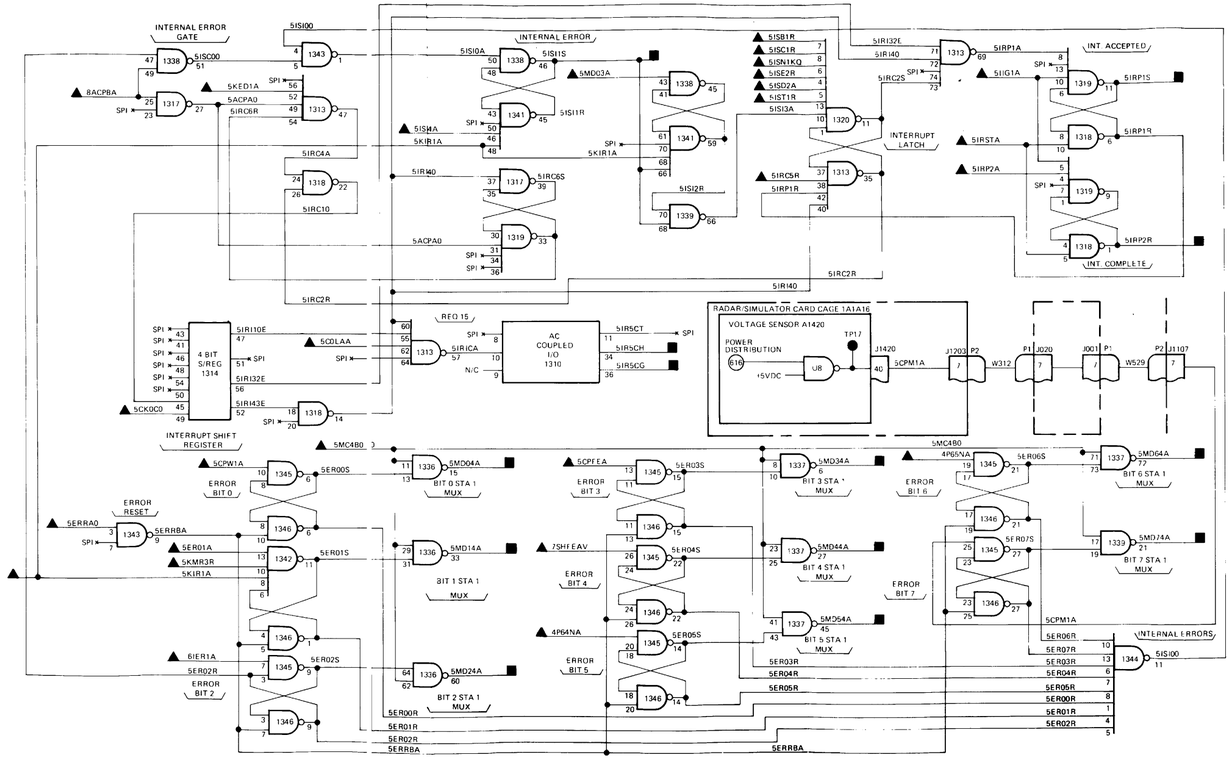
1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1. RIU BAY 1 CARD CAGE (1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1123, A1139, A1149 AND A1227



Change 3 FO-134. IOB Devise Interrupt and Reset Control Logic Diagram (Sheet 2 of 2)

INPUT	
SIGNAL	SOURCE FO-SH
4P64NA	06500
4P64NA	26803
4P64NA	28102
4P65NA	06500
4P65NA	26803
4P65NA	28102
5CK0C0	12800
5CPFEA	13400
5CPFEA	27001
5CPW1A	13401
5CPW1A	27001
5COLAA	13401
5COLAA	26802
5COLAA	27001
5ERRA0	13600
5ERQ1A	13402
5IIG1A	13402
5IRC5R	13402
5IRP2A	12700
5IRSTA	13402
5ISB1R	13101
5ISC1R	13101
5ISD2A	13102
5ISE2R	13102
5ISI4A	13000
5ISN1KQ	13102
5IST1R	13101
5KED1A	13402
5KED1A	26803
5KED1A	31301
5KIR1A	13402
5KMR3R	13402
5MC4B0	13600
5MD03A	13102
6IER1A	05500
6IER1A	26803
6IER1A	28201
7SHFEAV	26803
7SHFEAV	28202
8ACPBA	17200
8ACPBA	26803

OUTPUT	
SIGNAL	DESTINATION FO-SH
5IRP1S	12700, 13600
5IRP2R	12700
5IR5CG	26803, 31702
5IR5CH	26803, 31702
5ISI1S	13102
5MD04A	13800
5MD14A	13800
5MD24A	13800
5MD34A	13800
5MD44A	13800
5MD54A	13800
5MD64A	13800
5MD74A	13800



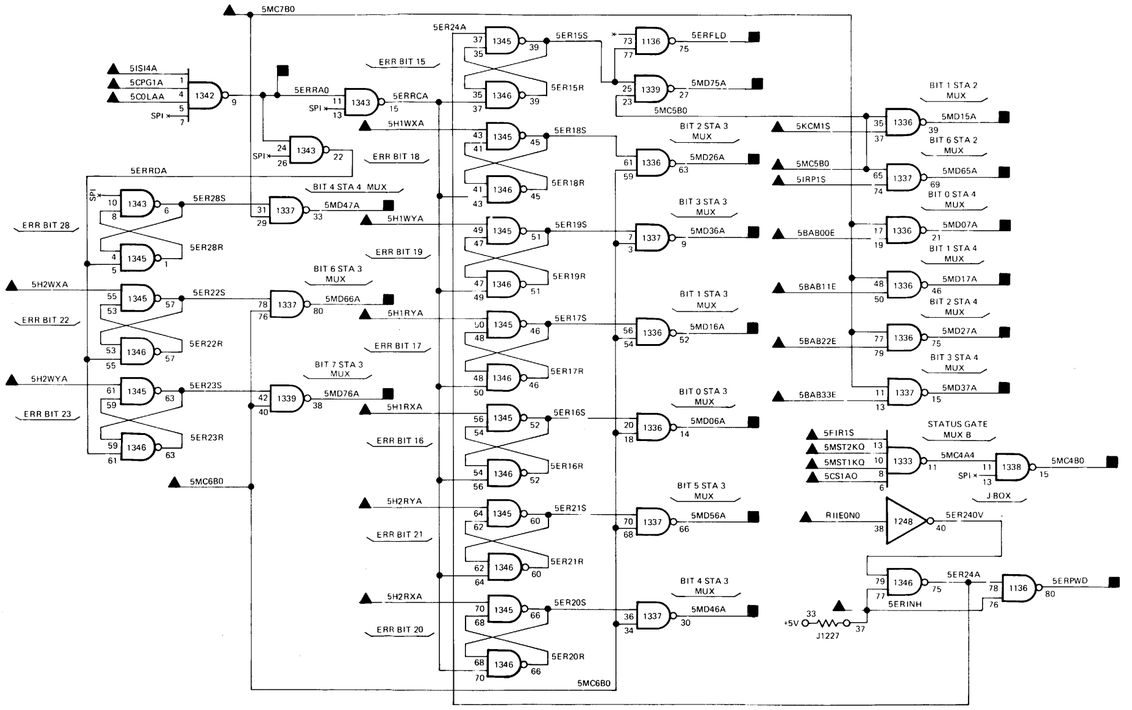
Change 2 FO-135. IOB Status 1 Error Register and Control Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1. RIU BAY 1 CARD CAGE (1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1123, A1139, A1149 AND A1227

INPUT	
SIGNAL	SOURCE FO-SH
RIIEON0	13700
5BAB00E	13302
5BAB11E	13302
5BAB22E	13302
5BAB33E	13302
5CPG1A	13402
5CPG1A	27001
5CS1A0	13000
5COLAA	13402
5COLAA	26802
5COLAA	27001
5ERINH	26803
5ERINH	28102
5FIR1S	12700
5H1RXA	26803
5H1RXA	28102
5H1RYA	26803
5H1RYA	28102
5H1WXA	26803
5H1WXA	28102
5H1WYA	26803
5H1WYA	28102
5H2RXA	26803
5H2RXA	28102
5H2RYA	26803
5H2RYA	28102
5H2WXA	26803
5H2WXA	28102
5H2WYA	26803
5H2WYA	28102
5IRP1S	13500
5IS14A	13000
5KCM1S	13400
5MC5B0	13000
5MC6B0	13000
5MC7B0	13000
5MST1KQ	13000
5MST2KQ	13000

OUTPUT	
SIGNAL	DESTINATION FO-SH
5ERFLD	26803
5ERPWD	26803
5ERRA0	13500
5MC4B0	13500
5MD06A	13800
5MD07A	13800
5MD15A	13800
5MD16A	13800
5MD17A	13800
5MD26A	13800
5MD27A	13800
5MD36A	13800
5MD37A	13800
5MD46A	13800
5MD47A	13800
5MD56A	13800
5MD65A	13800
5MD66A	13800
5MD75A	13800
5MD76A	13800



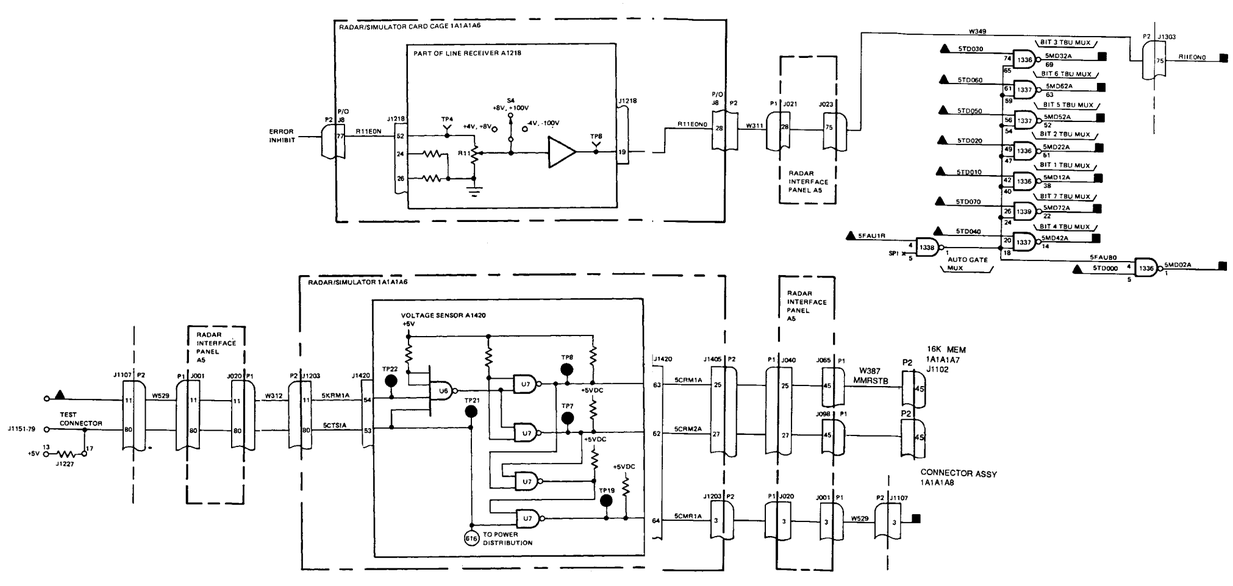
Change 2 FO-136. IOB Status 2, 3 and 4 Error Register Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1.
   RIU BAY 1 CARD CAGE (1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1123, A1139, A1149 AND A1227

MS200741A

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
5FAU1R	12700	R11EON0	13600, 28001, 28102
5KRMAS	13400	5CRM1A	13800
5TD000	07400	5CRM2A	13800
5TD000	26803	5MD02A	13800
5TD000	28101	5MD12A	13800
5TD020	07400	5MD22A	13800
5TD020	26803	5MD32A	13800
5TD020	28101	5MD42A	13800
5TD030	07400	5MD52A	13800
5TD030	26803	5MD62A	13800
5TD030	28101	5MD72A	29501
5TD040	07400		29501
5TD040	26803		
5TD040	28101		
5TD050	07400		
5TD050	26803		
5TD050	28101		
5TD060	07400		
5TD060	26803		
5TD060	28101		
5TD070	07400		
5TD070	26803		
5TD070	28101		



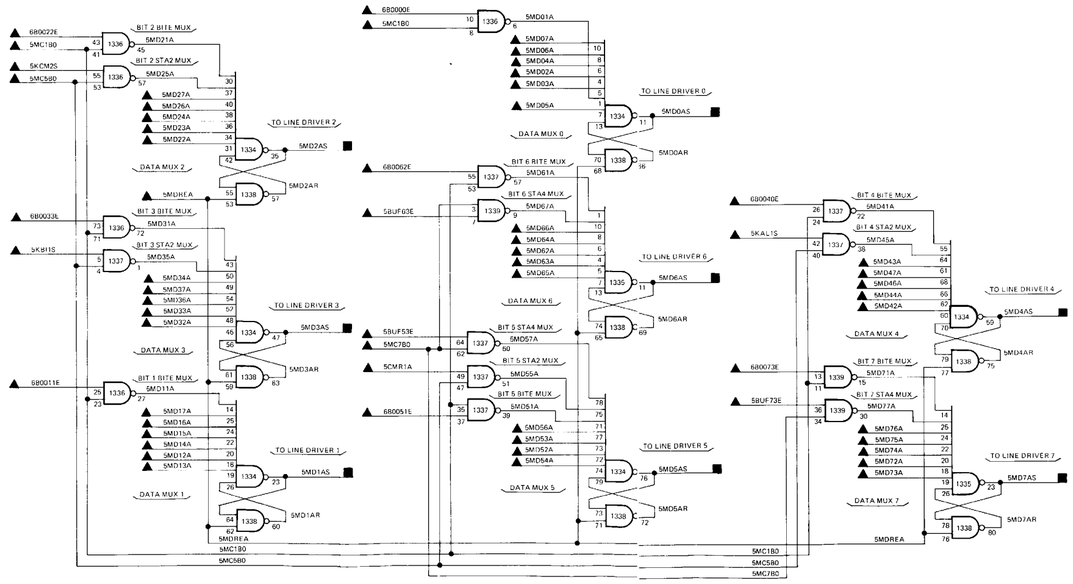
FO-137. IOB Automatic Gate Multiplexer Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1. RIU BAY 1 CARD CAGE (1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1123, A1139, A1149 AND A1227

MS200742C

INPUT		INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
5BUF53E	13301	5MD53A	13101	5MD0AS	12601, 12800
5BUF53E	26803	5MD54A	13500	5MD1AS	12601, 12800
5BUF53E	28201	5MD56A	13600	5MD2AS	12601, 12800
5BUF63E	13301	5MD62A	13700	5MD3AS	12601, 12800
5BUF63E	26803	5MD63A	13101	5MD4AS	12602, 12800
5BUF63E	28201	5MD64A	13500	5MD5AS	12602, 12800
5BUF73E	13301	5MD65A	13600	5MD6AS	12602, 12800
5BUF73E	26803	5MD66A	13600	5MD7AS	12602, 12800
5BUF73E	28201	5MD72A	13700		
5CMR1A	13700	5MD73A	13101		
5KAL1S	13400	5MD74A	13500		
5KB11S	13400	5MD75A	13600		
5KB11S	28201	5MD76A	13600		
5KCM2S	13400	5TD010	07400		
5MC1B0	12900	5TD010	26803		
5MC5B0	13000	5TD010	28101		
5MC7B0	13000	6B0000E	05400		
5MDREA	13000	6B0000E	26803		
5MD02A	13700	6B0000E	28202		
5MD03A	13102	6B0011E	05400		
5MD04A	13500	6B0011E	26803		
5MD05A	13400	6B0011E	28202		
5MD06A	13600	6B0022E	05400		
5MD074	13600	6B0022E	26803		
5MD12A	13700	6B0022E	28202		
5MD13A	13102	6B0033E	05400		
5MD14A	13500	6B0033E	26803		
5MD15A	13600	6B0033E	28202		
5MD16A	13600	6B0040E	05400		
5MD17A	13600	6B0040E	26803		
5MD22A	13700	6B0040E	28202		
5MD23A	13102	6B0051E	05400		
5MD24A	13500	6B0051E	26803		
5MD26A	13600	6B0051E	28202		
5MD27A	13600	6B0062E	05400		
5MD32A	13700	6B0062E	26803		
5MD33A	13102	6B0062E	28202		
5MD34A	13500	6B0073E	05400		
5MD36A	13600	6B0073E	26803		
5MD37A	13600	6B0073E	28202		
5MD42A	13700				
5MD43A	13101				
5MD44A	13500				
5MD46A	13600				
5MD47A	13600				
5MD52A	13700				



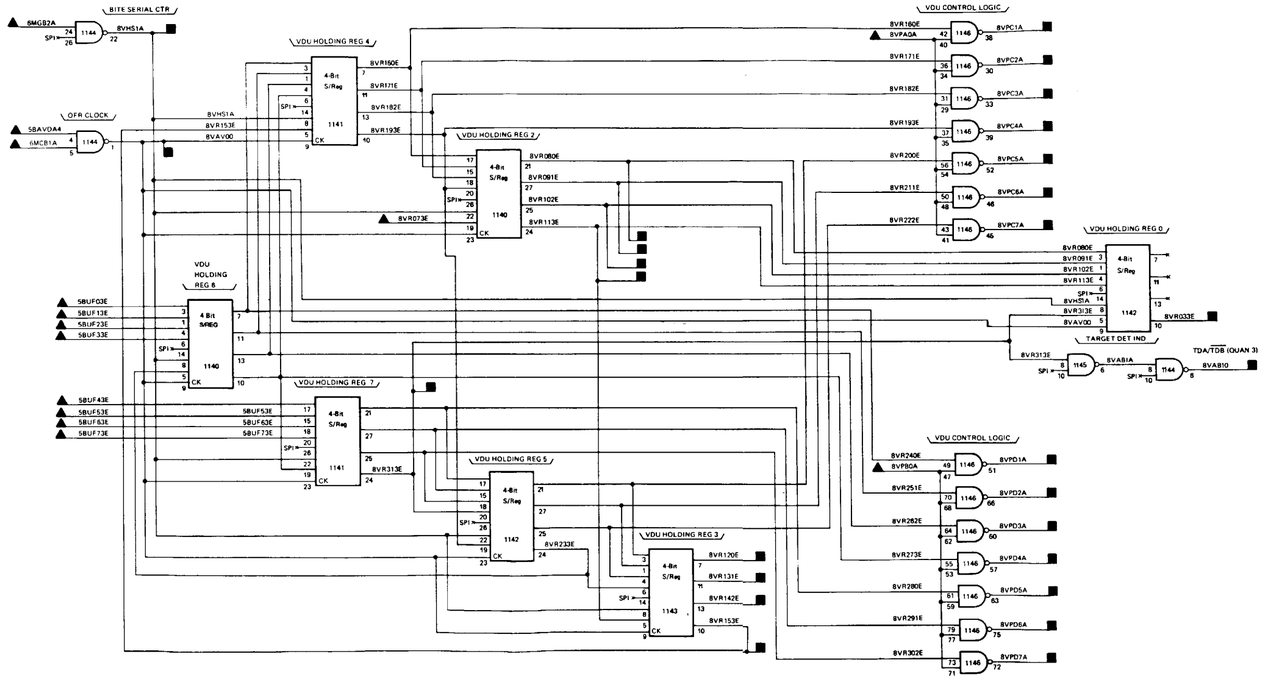
FO-138 IOB Input/Output Data Multiplexer Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1. RIU BAY 1 CARD CAGE (1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1123, A1139, A1149 AND A1227

MS200743

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
5BAV0A	13302	8VPE1D	15300
5BUF03E	13301	8VAV00	14000
5BUF03E	26803	8VHS1A	14000
5BUF03E	28201	8VPC1A	14100
5BUF13E	13301	8VPC2A	14100
5BUF13E	26803	8VPC3A	14100
5BUF13E	28201	8VPC4A	14100
5BUF23E	13301	8VPC5A	14100
5BUF23E	26803	8VPC6A	14100
5BUF23E	28201	8VPC7A	14100
5BUF33E	13301	8VPD1A	14100
5BUF33E	26803	8VPD2A	14100
5BUF33E	28201	8VPD3A	14100
5BUF43E	13301	8VPD4A	14100
5BUF43E	26803	8VPD5A	14100
5BUF43E	28201	8VPD6A	14100
5BUF53E	13301	8VPD7A	14100
5BUF53E	26803	8VR033E	14000
5BUF53E	28201	8VR080E	14000
5BUF63E	13301	8VR091E	14000
5BUF63E	26803	8VR102E	14000
5BUF63E	28201	8VR113E	14000
5BUF73E	13301	8VR120E	14000
5BUF73E	26803	8VR131E	14000
5BUF73E	28201	8VR142E	14000
6MCB1A	05600	8VR153E	14000
6MCB1A	27201	8VR313E	16500
6MGB2A	05600		
8VPA0A	14100		
8VPB0A	14100		
8VR073E	14000		



Change 3 FO-139. VDU Holding Registers Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

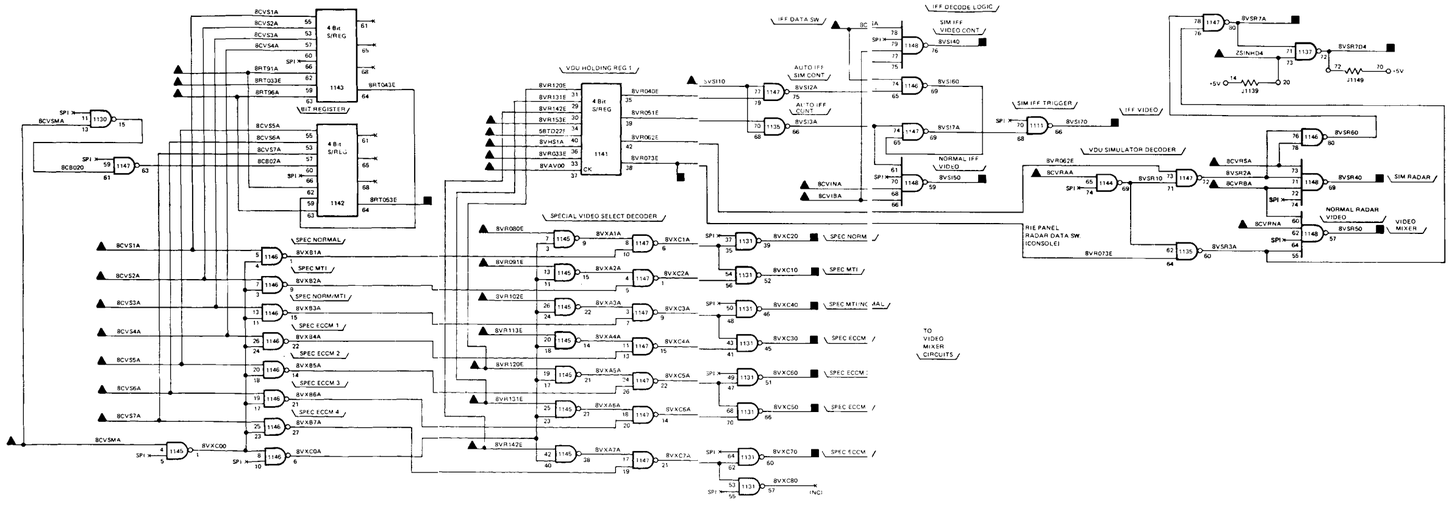
1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1. RIU BAY 1 CARD CAGE (1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1123, A1139, A1149 AND A1227

MS200744A

INPUT SIGNAL	SOURCE FO-SH
ZSINHD4	26803
ZSINHD4	27002
ZSINHD4	27803
5BT022E	13301
BCVIBA	26803
BCVIBA	27102
BCVINA	26803
BCVINA	27102
BCVISA	26803
BCVISA	27102
BCVRAA	27102
BCVRBA	26803
BCVRBA	27102
BCVRNA	26803
BCVRNA	27102
BCVRSA	26803
BCVRSA	27102
BCVSMA	26803
BCVSMA	27102
BCVS1A	26803
BCVS1A	27102
BCVS2A	26803
BCVS2A	27102
BCVS3A	26803
BCVS3A	27102
BCVS4A	26803
BCVS4A	27102
BCVS5A	26803
BCVS5A	27102
BCVS6A	26803
BCVS6A	27102
BCVS7A	26803
BCVS7A	27102
BRT033E	14100
BRT91A	16500
BRT96A	16500

INPUT SIGNAL	SOURCE FO-SH
8VAV00	13900
8VHS1A	13900
8VR033E	13900
8VR080E	13900
8VR091E	13900
8VR102E	13900
8VR113E	13900
8VR120E	13900
8VR131E	13900
8VR142E	13900
8VR153E	13900
8VS110	15002

OUTPUT SIGNAL	DESTINATION FO-SH
8RT053E	14700
8VR073E	13900
8VS140	12603, 15002, 18900, 26901
8VS150	12603, 15002, 18900, 26901
8VS170	18900, 26803, 27002
8VSR40	14202, 15002, 18100, 26803, 26901
8VSR50	14202, 14700, 15002, 18100, 26803, 26901
8VSR7A	14400, 14500, 14800, 16900, 17000, 17200, 18000, 26803
8VSR7D4	26803
8VXC10	14202, 26803, 26902
8VXC20	14202, 26803, 26902
8VXC30	14202, 26803, 26902
8VXC40	14202, 26803, 26902
8VXC50	14202, 26803, 26902
8VXC60	14202, 26803, 26902
8VXC70	14202, 26803, 26902



Change 3 FO-140. VDU Special Video Select and IFF Decode Logic Diagram

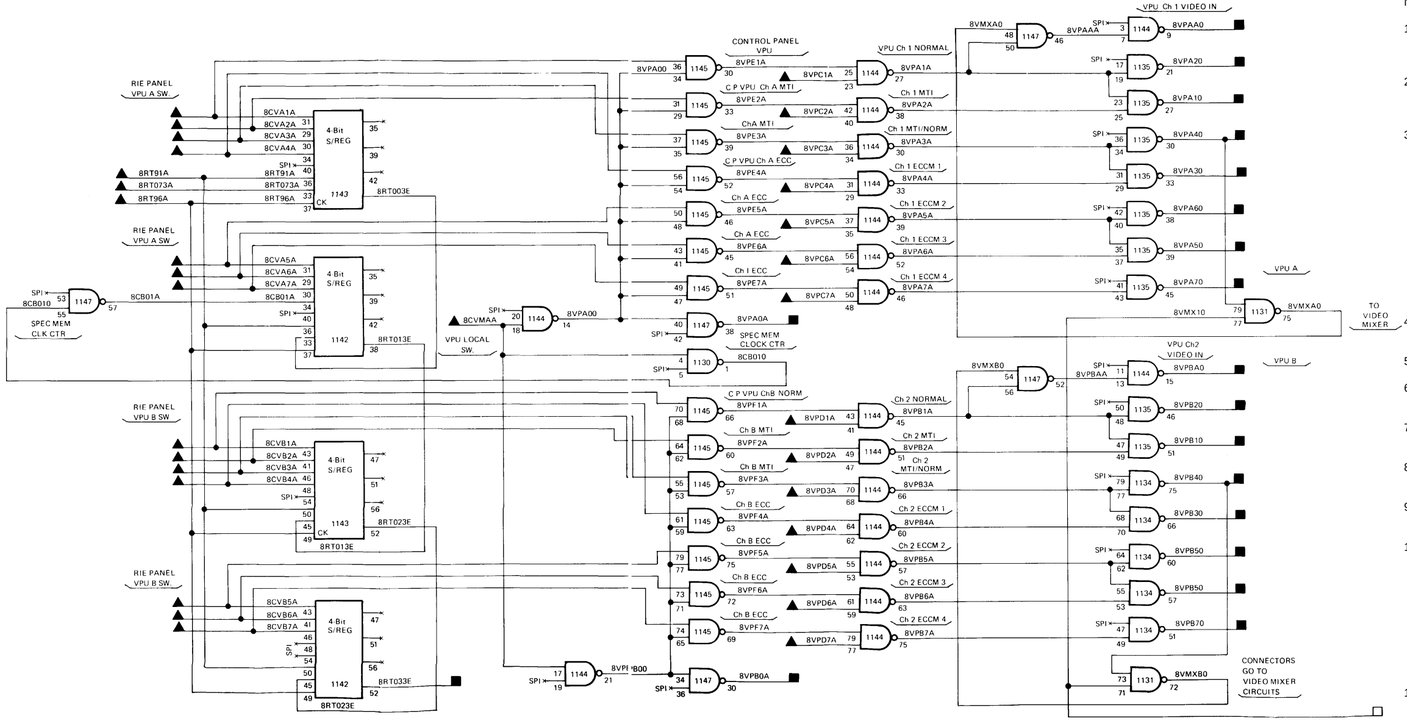
NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1. RIU BAY 1 CARD CAGE (1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1123, A1139, A1149 AND A1227

INPUT SIGNAL	SOURCE FO-SH
8CMTIS	26803
8CMTIS	27001
8CVA1A	26803
8CVA1A	27101
8CVA2A	26803
8CVA2A	27101
8CVA3A	26803
8CVA3A	27101
8CVA4A	26803
8CVA4A	27101
8CVA5A	26803
8CVA5A	27101
8CVA6A	26803
8CVA6A	27101
8CVA7A	26803
8CVA7A	27101
8CVB1A	26803
8CVB1A	27101
8CVB2A	26803
8CVB2A	27101
8CVB3A	26803
8CVB3A	27101
8CVB4A	26803
8CVB4A	27101
8CVB5A	26803
8CVB5A	27101
8CVB6A	26803
8CVB6A	27102
8CVB7A	26803
8CVB7A	27102
8CVMAA	26803
8CVMAA	27102
8RT073E	14700
8RT91A	16500
8RT96A	16500
8VMTNA	18900
8VMTNA	26803
8VMTNA	27002
8VPC1A	13900
8VPC2A	13900
8VPC3A	13900
8VPC4A	13900
8VPC5A	13900
8VPC6A	13900
8VPC7A	13900
8VPD1A	13900

INPUT SIGNAL	SOURCE FO-SH
8VPD2A	13900
8VPD3A	13900
8VPD4A	13900
8VPD5A	13900
8VPD6A	13900
8VPD7A	13900

OUTPUT SIGNAL	DESTINATION FO-SH
8RT033E	14000
8VMNSA	14202, 26803, 27002
8VMNS0	14202, 26803, 27002
8VPAA0	15300
8VPA0A	13900
8VPA10	14202, 26803, 26901
8VPA20	14202, 26803, 26901
8VPA30	14202, 26803, 26901
8VPA40	14202, 26803, 26901
8VPA50	14202, 26803, 26902
8VPA60	14202, 26803, 26902
8VPA70	14202, 26803, 26902
8VPBA0	15300
8VPB0A	13900
8VPB10	14202, 26803, 26902
8VPB20	14202, 26803, 26902
8VPB30	14202, 26803, 26902
8VPB40	14202, 26803, 26902
8VPB50	14202, 26803, 26902
8VPB60	14202, 26803, 26902
8VPB70	14202, 26803, 26902

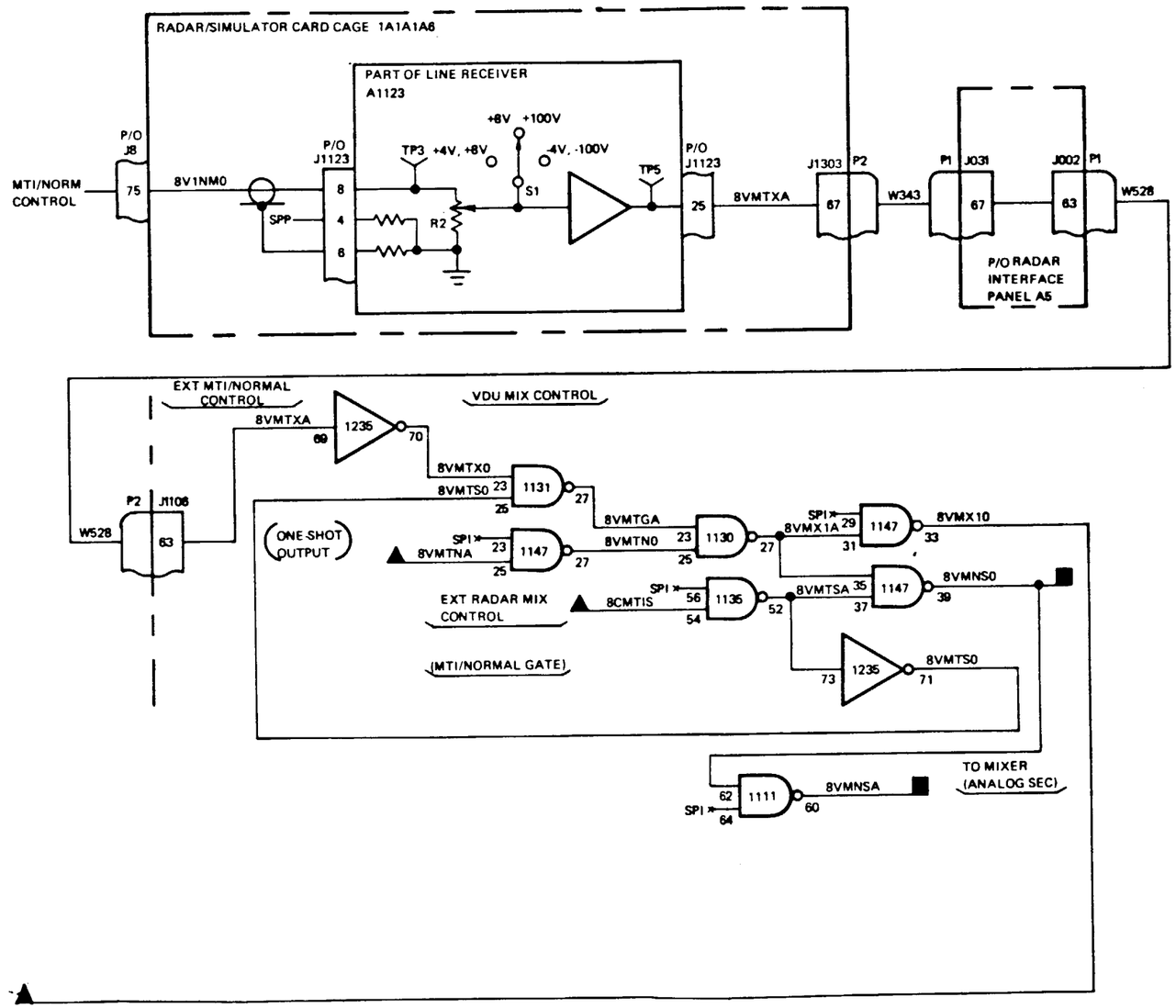


FO-141. VDU Video Selection and Decoding Logic Diagram (Sheet 1 of 2)

NOTES: UNLESS OTHERWISE SPECIFIED

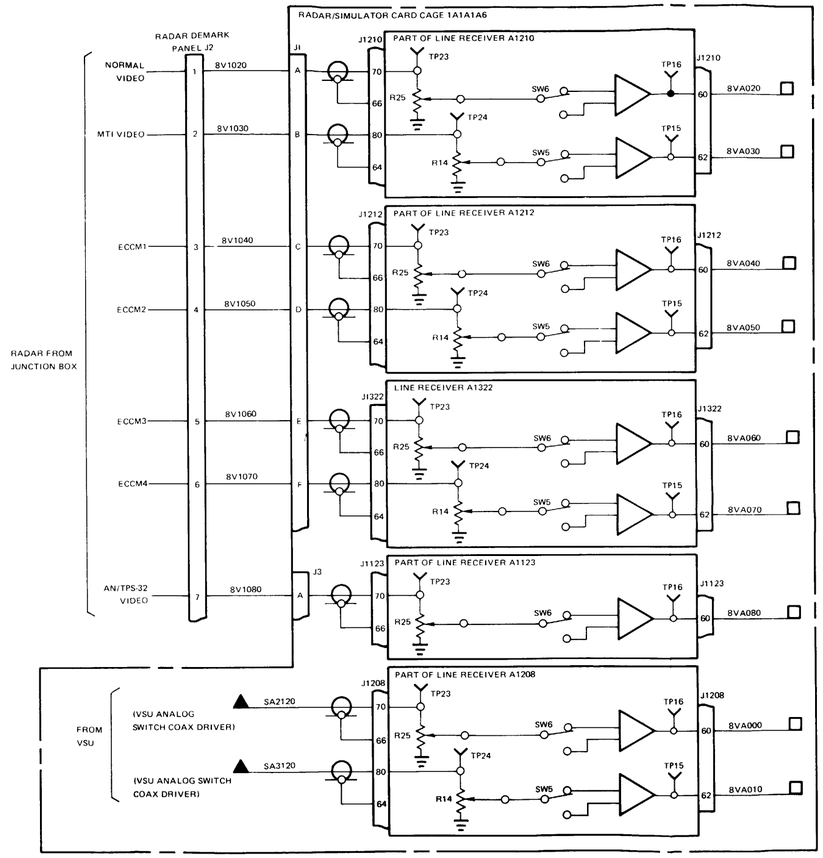
1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1. RIU BAY 1 CARD CAGE (1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1123, A1139, A1149 AND A1227

MS200746



FO-141. VDU Video Selection and Decoding Logic Diagram (Sheet 2 of 2)

INPUT	
SIGNAL	SOURCE FO-SH
SA2120	22803
SA3120	22804

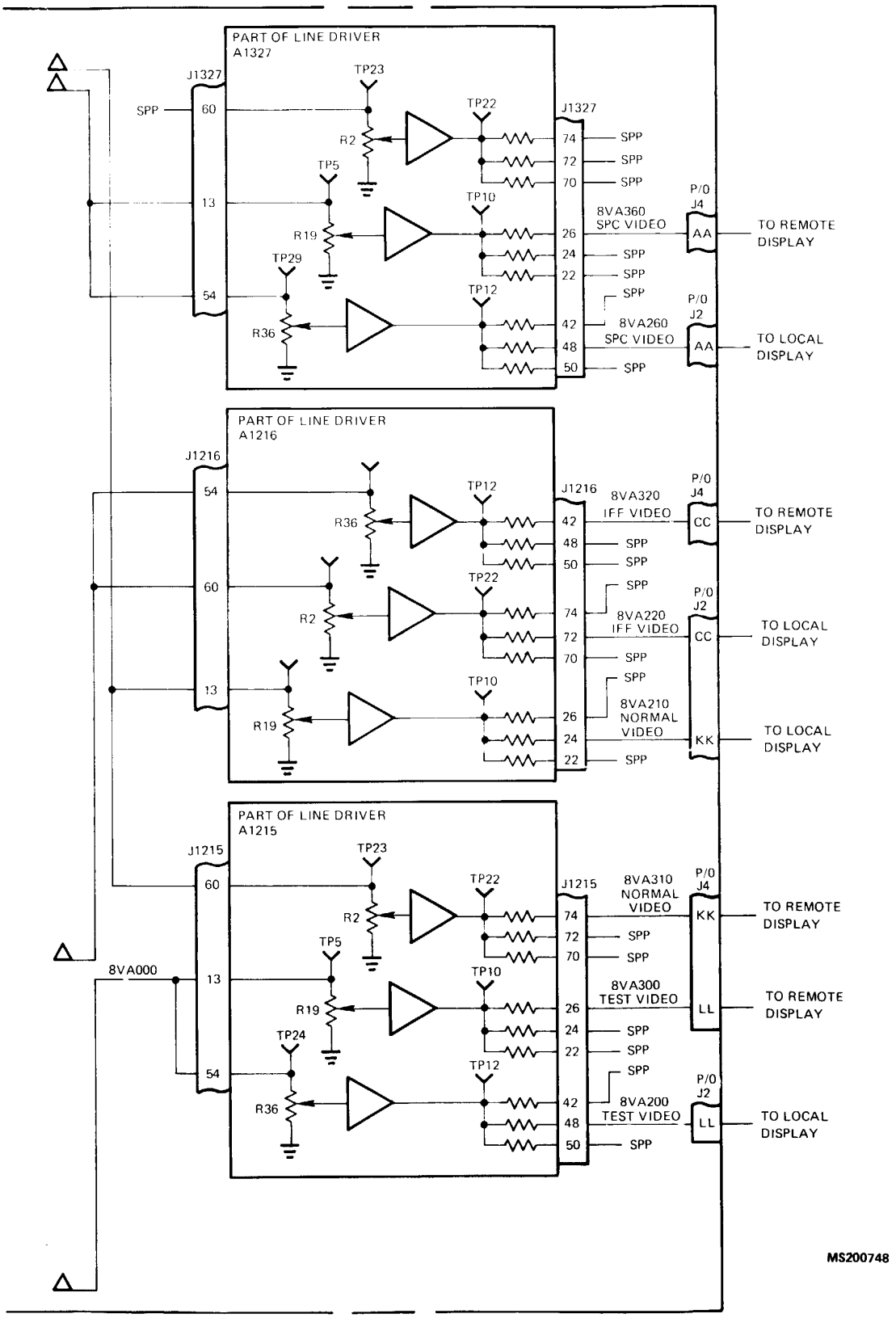


NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1. RSU (1A1A1A6.)
3. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING:
4. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING.
5. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS

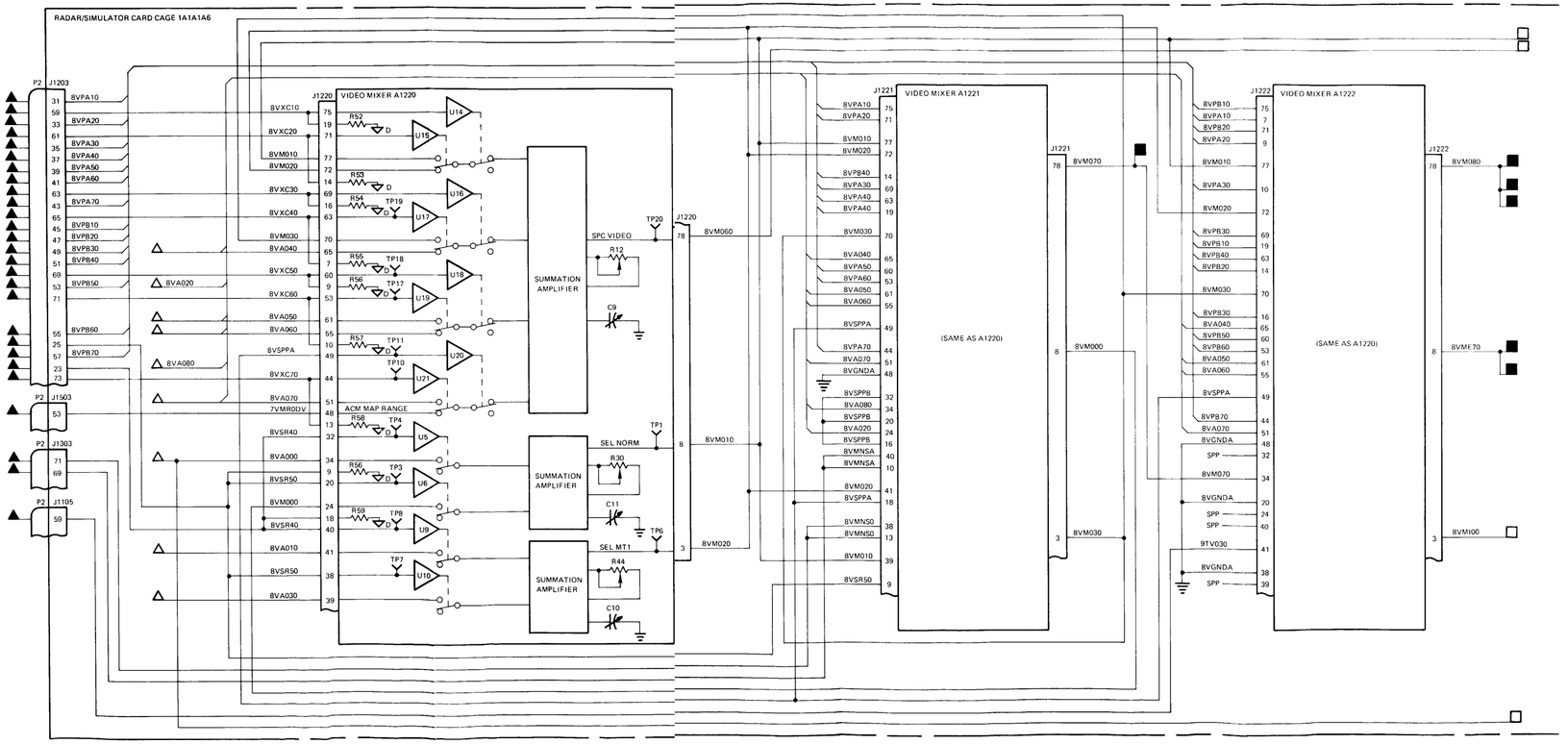
MS200747

FO-142. VDU Video Mixers Logic Diagram (Sheet 1 of 3)



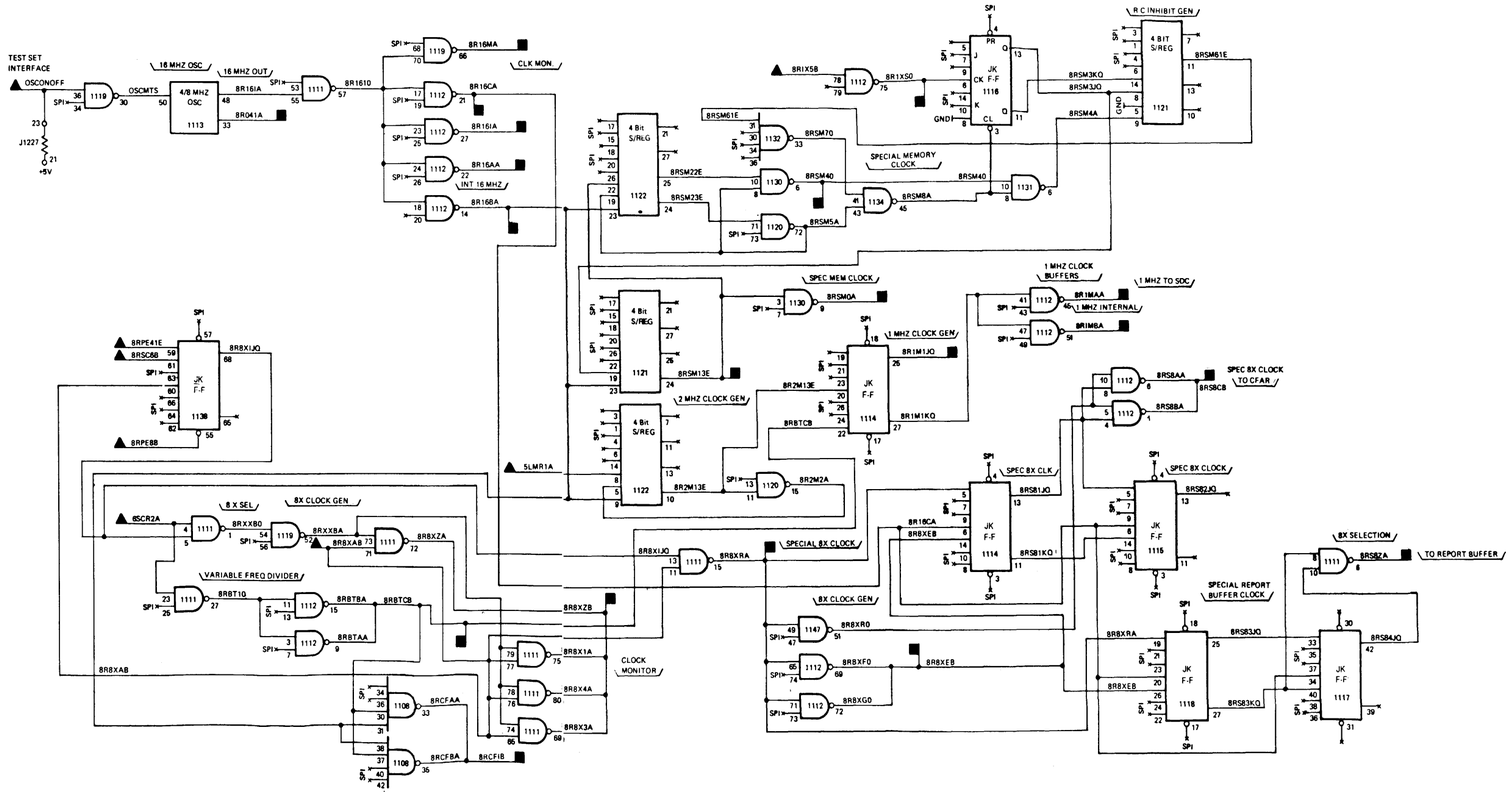
FO-142. VDU Video Mixers Logic Diagram (Sheet 3 of 3)

INPUT		INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
7VMRD0V	04900	8VXC40	14000	8VME70	03800
7VMRD0V	30402	8VXC40	26902	8VM070	00300
8VMNSA	14100	8VXC50	14000	8VM080	03800
8VMNSA	28902	8VXC50	26902		
8VMNS0	14100	8VXC60	14000		
8VMNS0	28902	8VXC60	26902		
8VPA10	14100	8VXC70	14000		
8VPA10	26901	8VXC70	26902		
8VPA20	14100	9TV030	20202		
8VPA20	26901				
8VPA30	14100				
8VPA30	26901				
8VPA40	14100				
8VPA40	26901				
8VPA50	14100				
8VPA50	26902				
8VPA60	14100				
8VPA60	26902				
8VPA70	14100				
8VPA70	26902				
8VPB10	14100				
8VPB10	26902				
8VPB20	14100				
8VPB20	26902				
8VPB30	14100				
8VPB30	26902				
8VPB40	14100				
8VPB40	26902				
8VPB50	14100				
8VPB50	26902				
8VPB60	14100				
8VPB60	26902				
8VPB70	14100				
8VPB70	26902				
8VSR40	14000				
8VSR40	26803				
8VSR40	26901				
8VSR50	14000				
8VSR50	26803				
8VSR50	26901				
8VXC10	14000				
8VXC10	26902				
8VXC20	14000				
8VXC20	26902				
8VXC30	14000				
8VXC30	26902				



FO-142. VDU Video Mixers Logic diagram (Sheet 2 of 3)

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
OSCONOFF	26110	8RBTCB	14400, 14700, 14900
5LMR1A	13400	8RBT1Q	15001
6SRC2A	05900	8RCF1B	26803
6SRC2A	27201	8RSM0A	06001, 26803, 27502
8RPE41E	14400	8RSM13E	13400
8RPE8B	14400	8RSM40	14600
8RSC6B	14900	8RS8CB	06600, 26803, 28203
8R1XSB	00300	8RS8ZA	26803
8R1XSB	14900	8R041A	14400, 14500, 15001
8R1XSB	26802	8R1MAA	16700, 26803
8R1XSB	26803	8R1MBA	14400, 15001
8R1XSB	27501	8R1M1JQ	13200
8R8XAB	14900	8R1XSO	13400, 14500, 14600
8R8XAB	28203	8R16AA	05200, 26803, 27201
		8R16BA	14400, 14500, 14700
		8R16CA	14400, 14500, 15002, 26901
		8R16IA	12800
		8R16MA	26803
		8R8XEB	14900, 15001
		8R8XRA	14400
		8R8XZB	00100, 00703, 01501, 02500, 06600, 06700, 07600, 07900, 08100, 15002, 15100, 15200, 26802, 26803, 27201



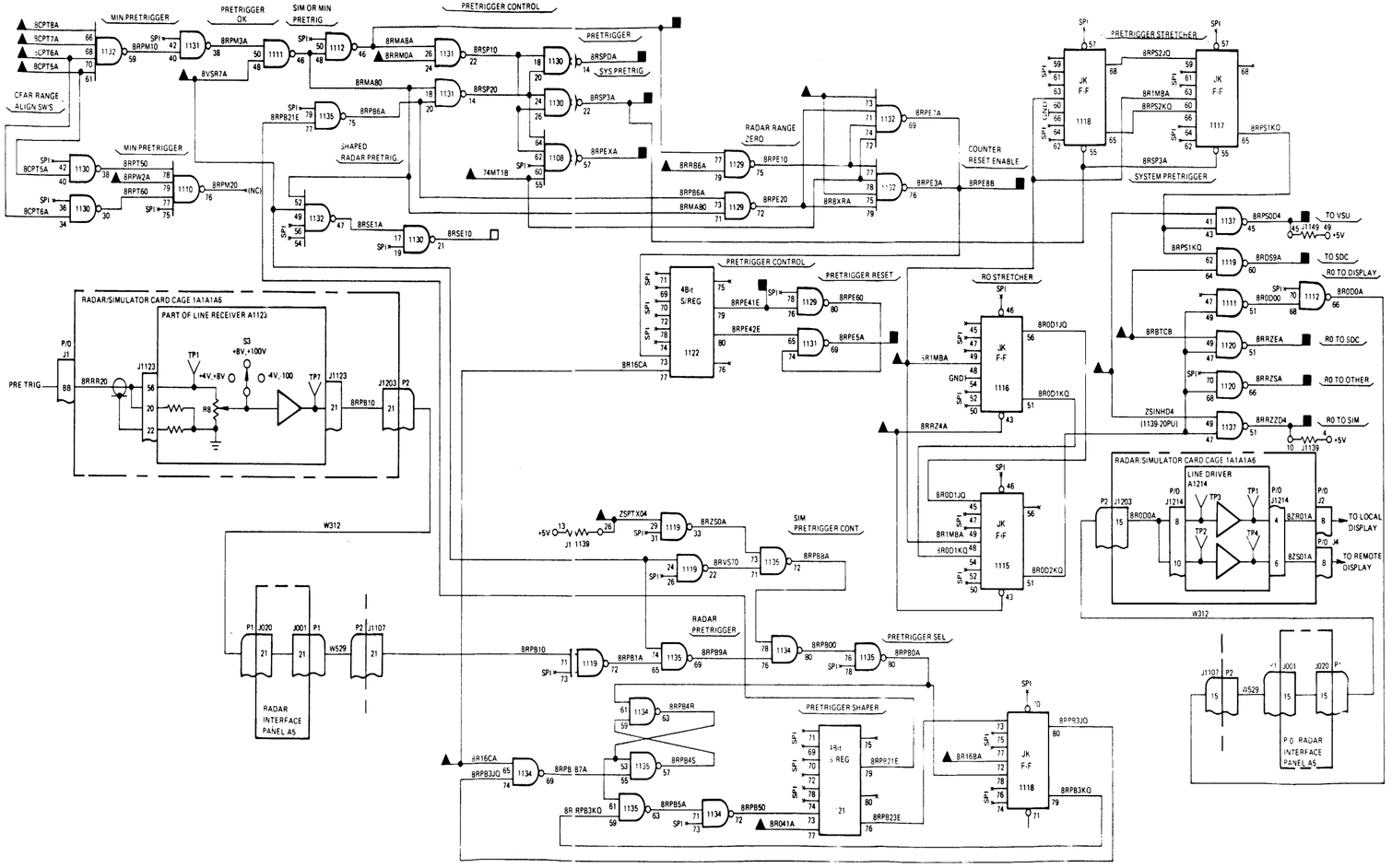
NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1. RIU BAY 1 CARD CAGE (1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1123, A1139, A1149 AND A1227

Change 3 FO-143. RSU Special Clock Generator Logic Diagram

INPUT SIGNAL	SOURCE FO-SH
ZSINHD4	26803
ZSINHD4	27002
ZSINHD4	27803
ZSPTXD4	20602
ZSPTXD4	26803
ZSPTXD4	27002
74MT1B	05101
74MT1B	28202
8CPT5A	27101
8CPT6A	27101
8CPT7A	27101
8CPT8A	27101
8RBTCB	14300
8RPW2A	14700
8RRB6A	14500
8RRM0A	14600
8RRZ4A	14600
8RD41A	14300
8R1MBA	14300
8R16BA	14300
8R15CA	14300
8R16CA	26901
8R8XRA	14300
8VSR7A	14000
8VSR7A	26803

OUTPUT SIGNAL	DESTINATION FO-SH
8RMA8A	14600
8RPEXA	26803
8RPE41E	14300
8RPE5A	14700
8RPE8B	14300, 14700, 14800
8RPSOD4	26803
8RPS9C	01501, 05900, 17300, 26802, 26803, 28202
8RRZEA	17300, 26803
8RRZSA	01501, 05900, 26802, 26803, 28202
8RRZZD4	26803
8RSE10	14500
8RSPDA	15100, 15200
8RSP3A	14500



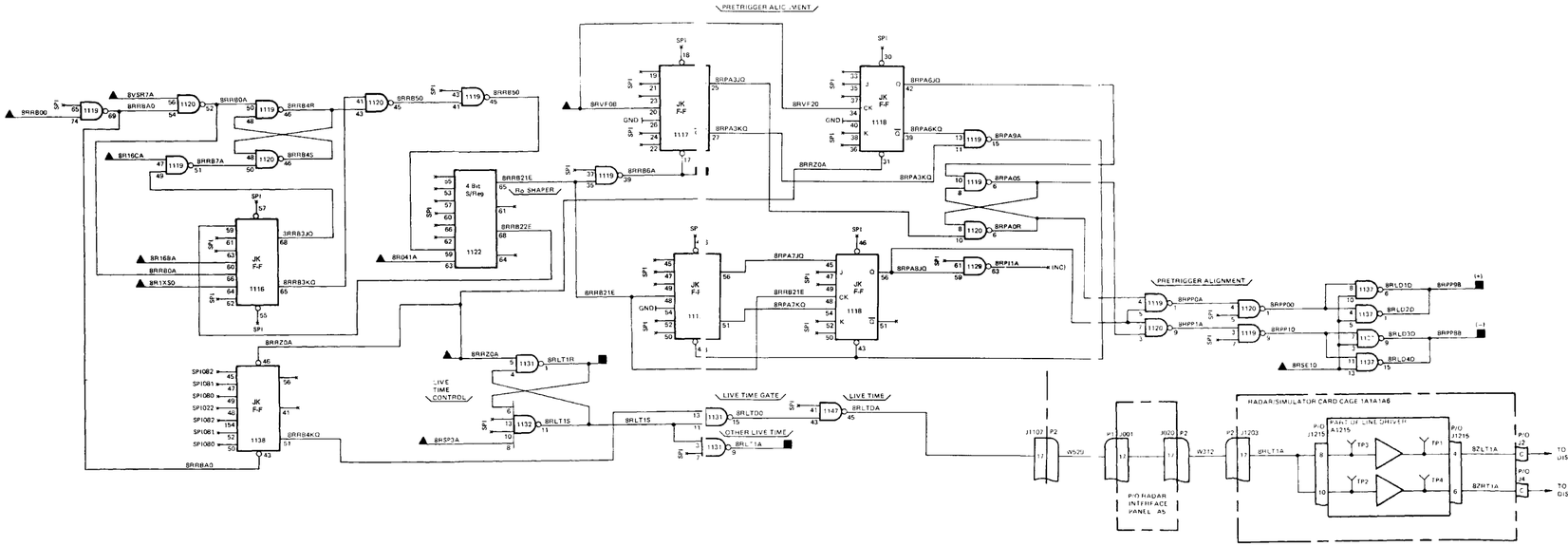
Change 3 FO-144. RSU Pretrigger/Range Zero Stretcher Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1. RIU BAY 1 CARD CAGE (1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1123, A1139, A1149 AND A1227

MS200750B

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
8RRB00	16101	8RLT1A	00100, 08502, 12200, 12400,
8RRB00	26803		14400, 26803
8RRZ0A	14600	8RLT1R	15001, 27502
8RSE10	14400	8RPP8B	26803, 30102
8RSP3A	14400	8RPP9B	26803, 30102
8RVF0B	14700	8RRB6A	14400
8RG41A	14300		
8R1XS0	14300		
8R16BA	14300		
8R16CA	14300		
8R16CA	26901		
8VSR7A	14000		
8VSR7A	26803		

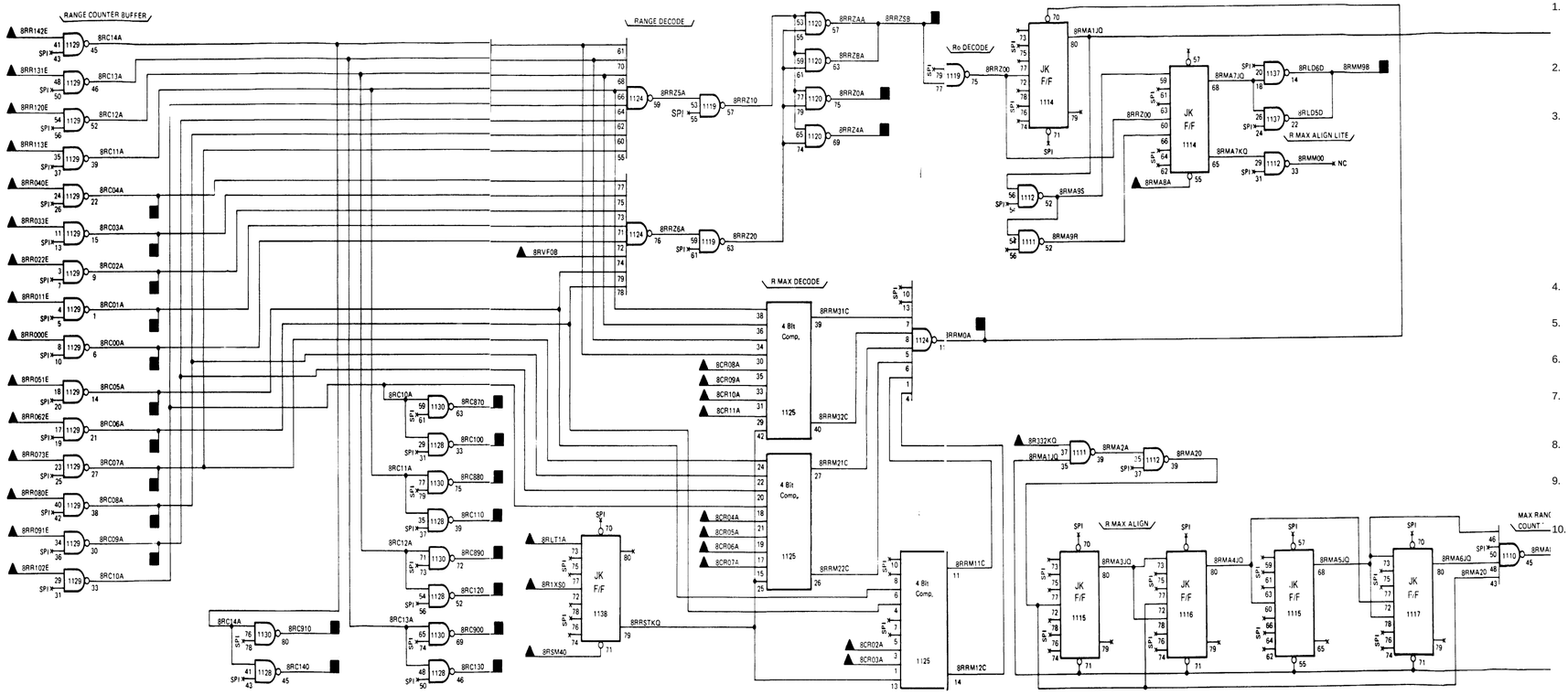


Change 3 FO-145. RSU Pretrigger Alignment Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN. FOR COMPLETE DESIGNATIONS, PREFIX WITH APPROPRIATE ASSEMBLY NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN RIU BAY 1 CARD CAGE (1A1A1A1 THRU 1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - INDICATES INPUT FROM ANOTHER FIGURE
   - INDICATES OUTPUT TO ANOTHER FIGURE
   - INDICATES INPUT FROM THE SAME FIGURE
   - INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
   - INDICATES OUTPUT TO THE SAME FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
7. REFER TO THE POWER DISTRIBUTION DIAGRAMS FOR RIU POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1123, A1139, A1149 AND A1227

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
8CR02A	27101	8RC00A	14900
8CR03A	27101	8RC01A	14900
8CR04A	27101	8RC02A	14900
8CR05A	27101	8RC03A	14900
8CR06A	27101	8RC04A	14900
8CR07A	27101	8RC05A	14900
8CR08A	27101	8RC06A	14900
8CR09A	27101	8RC07A	14900
8CR10A	27101	8RC08A	14900
8CR11A	27101	8RC09A	14900
8RLT1A	14500	8RC10D	08501, 18401, 26803, 27501
8RLT1A	26802	8RC11D	08501, 18401, 26803, 27501
8RLT1A	26901	8RC12D	07600, 08501, 18401, 26802, 26803, 27501
8RMA8A	14400	8RC13D	08501, 18401, 26803, 27501
8RR000E	14800	8RC14D	08501, 18401, 26803, 27501
8RR011E	14800	8RC87D	02500, 26803, 27202
8RR022E	14800	8RC88D	02500, 26803, 27202
8RR033E	14800	8RC89D	02500, 26803, 27202
8RR040E	14800	8RC90D	02500, 26803, 27202
8RR051E	14800	8RC91D	02500, 26803, 27202
8RR062E	14800	8RMM9B	26803, 30101
8RR073E	14800	8RRM0A	14400
8RR080E	14800	8RRZSB	07600, 07701, 07800, 12200, 18700, 26802, 26803, 27502
8RR091E	14800	8RRZ0A	14500, 15001, 15100, 15200, 15600, 15800, 15900
8RR102E	14800	8RRZ4A	14400
8RR113E	14800		
8RR120E	14800		
8RR131E	14800		
8RR142E	14800		
8RSM40	14300		
8RVF0B	14700		
8R1XS0	14300		
8R332KQ	15001		

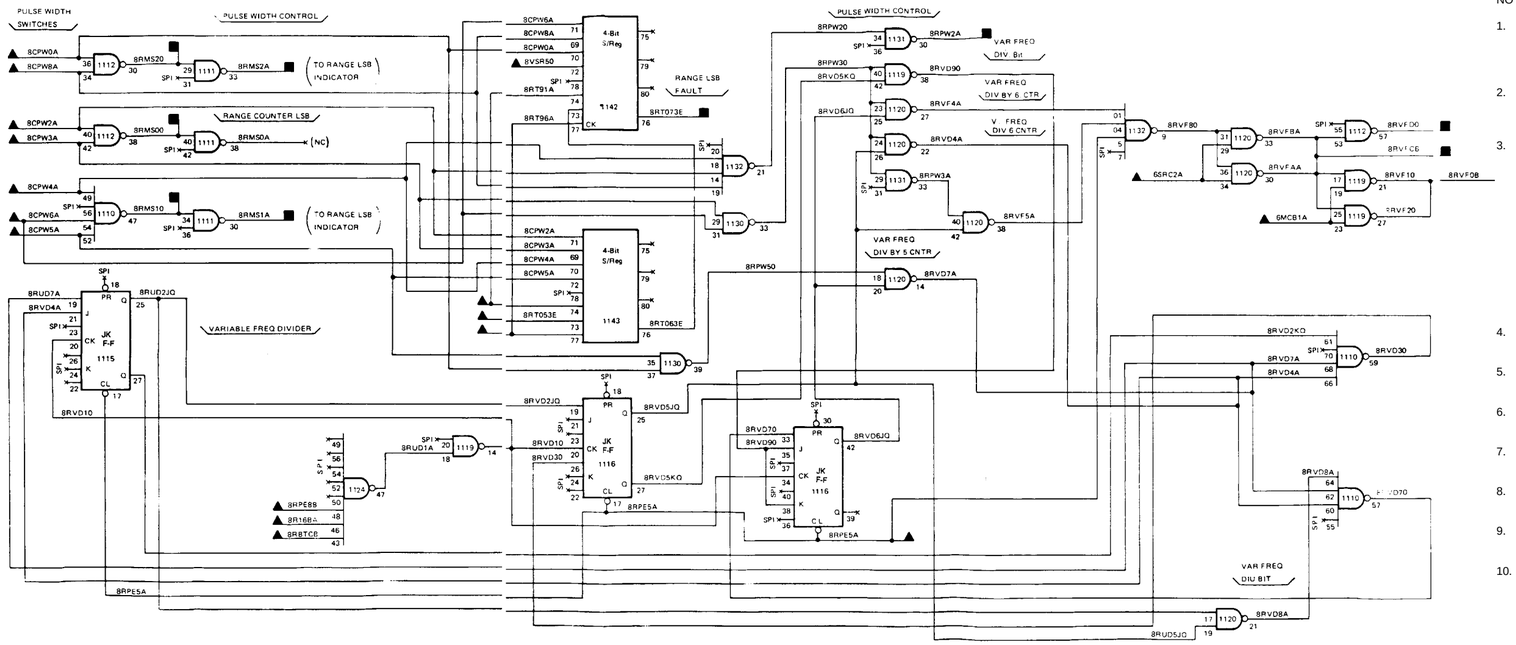


NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1. RIU BAY 1 CARD CAGE (1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURI
   - △ INDICATES INPUT FROM THE SAME FIGUR
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AN ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1123, A1139, A1149 AND A1227

Change 3 FO-146. RSU Range Zero/Range Max Decode Logic Diagram

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
6MCB1A	05600	8RMS00	14900
6MCB1A	27201	8RMS1A	12300, 26803, 27502
6SRC2A	05900	8RMS10	14900
6SRC2A	27201	8RMS2A	12300, 26803, 27502
8CPW0A	26504	8RMS20	14900
8CPW0A	26803	8RPW2A	14400
8CPW0A	27101	8RT073E	14100, 16500
8CPW0A	28901	8RVFCB	08300, 26803, 27502
8CPW2A	26802	8RVFD0	14900
8CPW2A	26803	8RVF0B	14500, 14600, 14800
8CPW2A	27101		
8CPW2A	28901		
8CPW2A	32102		
8CPW3A	26803		
8CPW3A	27101		
8CPW3A	28901		
8CPW4A	26803		
8CPW4A	27101		
8CPW4A	28901		
8CPW5A	26803		
8CPW5A	27101		
8CPW5A	28902		
8CPW6A	26803		
8CPW6A	27101		
8CPW6A	28902		
8CPW8A	26803		
8CPW8A	27101		
8CPW8A	28902		
8RBTCB	14300		
8RPE5A	14400		
8RPE8B	14400		
8RT053E	14000		
8RT91A	16500		
8RT96A	16500		
8R16BA	14300		
8VSR50	14000		
8VSR50	26803		
8VSR50	26901		

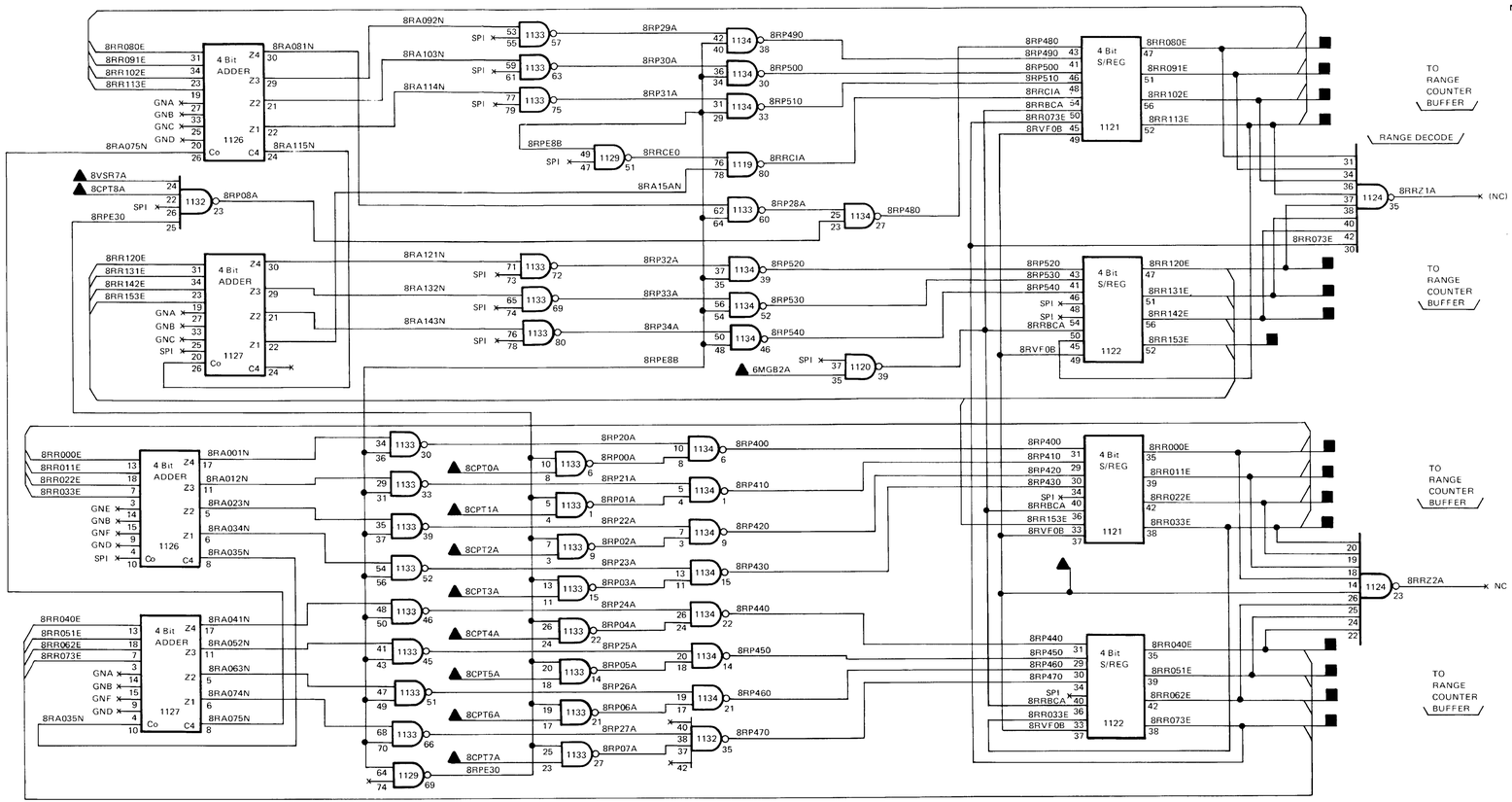


NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN; FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1. RIU BAY 1 CARD CAGE (1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1123, A1139, A1149 AND A1227

Change 3 FO-147. RSU Variable Frequency Divider Logic Diagram

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
6MGB2A	05600	8RR000E	14600
8CPT0A	27101	8RR011E	14600
8CPT1A	27101	8RR022E	14600
8CPT2A	27101	8RR033E	14600
8CPT3A	27101	8RR040E	14600
8CPT4A	27101	8RR051E	14600
8CPT5A	27101	8RR062E	14600
8CPT6A	27101	8RR073E	14600
8CPT7A	27101	8RR080E	14600
8CPT8A	27101	8RR091E	14600
8RPE8B	14400	8RR102E	14600
8RVF0B	14700	8RR113E	14600
8VSR7A	14000	8RR120E	14600
8VSR7A	26803	8RR131E	14600
		8RR142E	14600
		8RR153E	16500



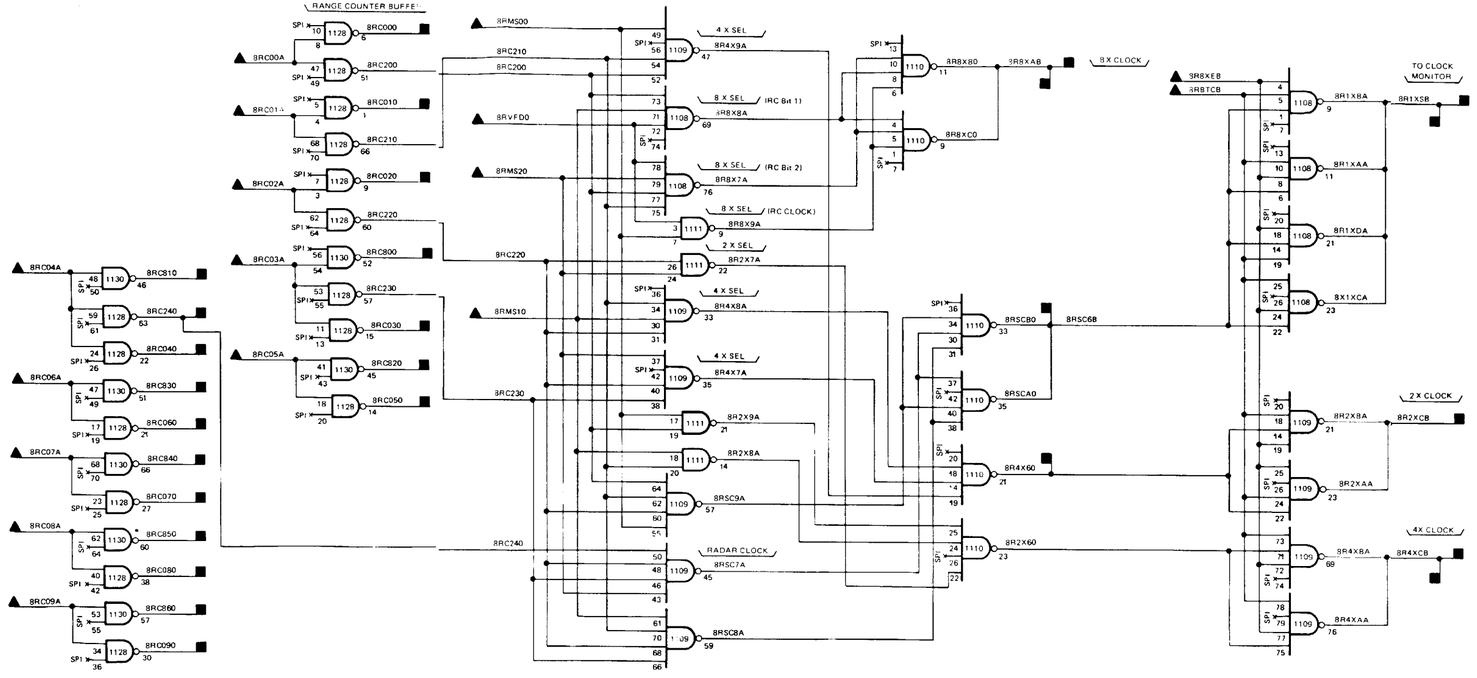
NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN; FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, RIU BAY 1 CARD CAGE (1A1A1A4).
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE.
   - △ INDICATES INPUT FROM THE SAME FIGURE.
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE.
   - □ INDICATES OUTPUT TO THE SAME FIGURE.
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE.
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1123, A1139, A1149 AND A1227

MS200754

FO-148. RSU Range Counter Logic Diagram

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
8RBTCB	14300	8RC000	08501, 12100, 26802, 26803, 27501
8RC00A	14600		
8RC01A	14600	8RC010	08501, 12100, 26802, 26803, 27501
8RC02A	14600		
8RC03A	14600	8RC020	08501, 12100, 18300, 26802, 26803, 27501
8RC04A	14600		
8RC05A	14600	8RC030	08501, 12100, 18402, 26802, 26803, 27501
8RC06A	14600		
8RC07A	14600	8RC040	08501, 12100, 18402, 26802, 26803, 27501
8RC08A	14600		
8RC09A	14600	8RC050	08501, 12100, 18401, 26802, 26803, 27501
8RMS00	14700		
8RMS10	14700	8RC060	08501, 12100, 18401, 26802, 26803, 27501
8RMS20	14700		
8RVFD0	14700	8RC070	08501, 12100, 18401, 26802, 26803, 27501
8R8XEB	14300		
		8RC080	08501, 12100, 18401, 26802, 26803, 27501
		8RC090	08501, 12100, 18401, 26802, 26803, 27501
		8RC800	02500, 26803, 27202
		8RC810	02500, 26803, 27202
		8RC820	02500, 26803, 27202
		8RC830	02500, 26803, 27202
		8RC840	02500, 26803, 27202
		8RC850	02500, 26803, 27202
		8RC860	02500, 26803, 27202
		8RSC6B	14300, 15001
		8R1XSB	00300, 01501, 03800, 06600, 07600, 08300, 08900, 11200, 11400, 11600, 11700, 11800, 14300, 15001, 15002, 15100, 15200, 16700, 26802, 26803, 27501
		8R2XCB	26802, 26803, 27201
		8R4XCB	06600, 15700, 15800, 26803, 27201
		8R4X60	15001
		8R8XAB	05800, 14300, 26802, 26803, 28203

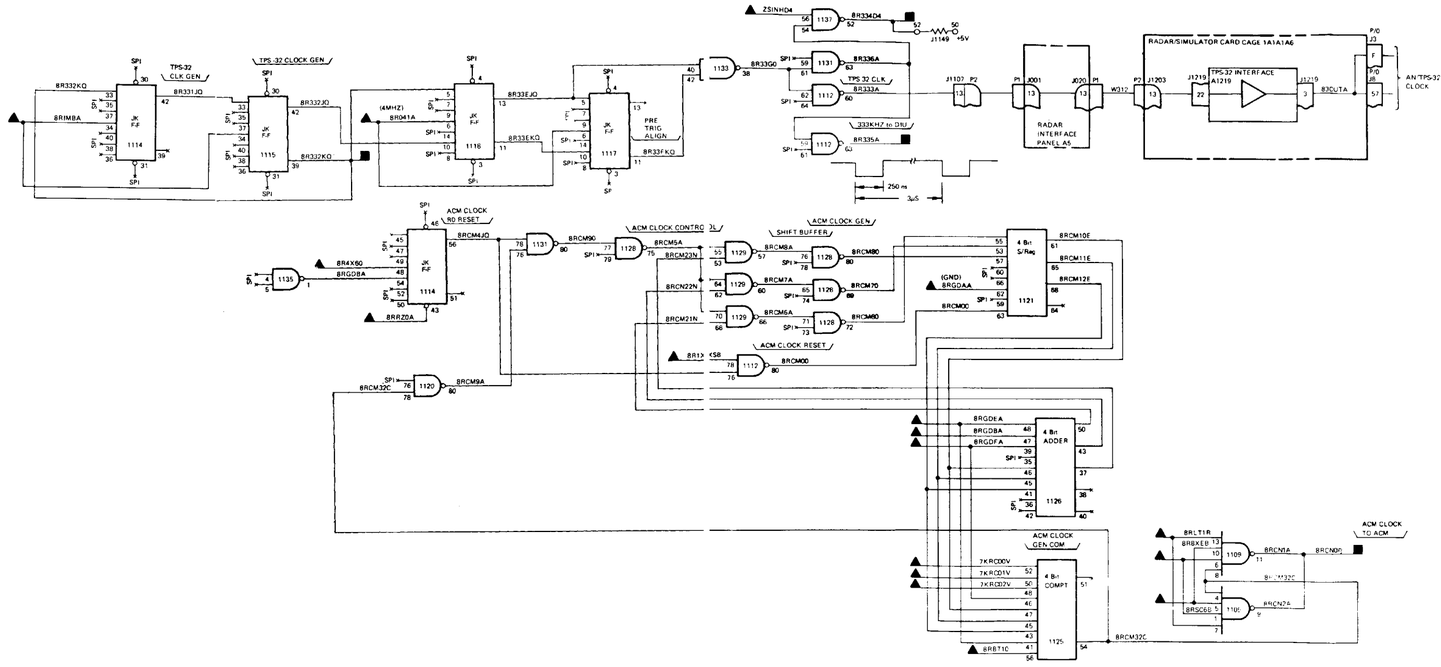


Change 3 FO-149. RSU Radar Clock Generator Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1. RIU BAY 1 CARD CAGE (1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1123, A1139, A1149 AND A1227

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
ZSINHD4	26803	8RCNDB	01501, 04300, 04400, 04500, 04700, 26802, 26803, 27201
ZSINHD4	27002	8R332KQ	14600
ZSINHD4	27803	8R334D4	26803
7KRC00V	02100	8R335A	18100, 26803
7KRC00V	26803		
7KRC01V	26803		
7KRC02V	26803		
8RBT10	14300		
8RLT1R	14500		
8RLT1R	27502		
8RRZDA	14600		
8RSC6B	14900		
8R041A	14300		
8R1MBA	14300		
8R1XSB	00300		
8R1XSB	14900		
8R1XSB	26802		
8R1XSB	26803		
8R1XSB	27501		
8R4X60	14900		
8R8XEB	14300		



NOTES: UNLESS OTHERWISE SPECIFIED

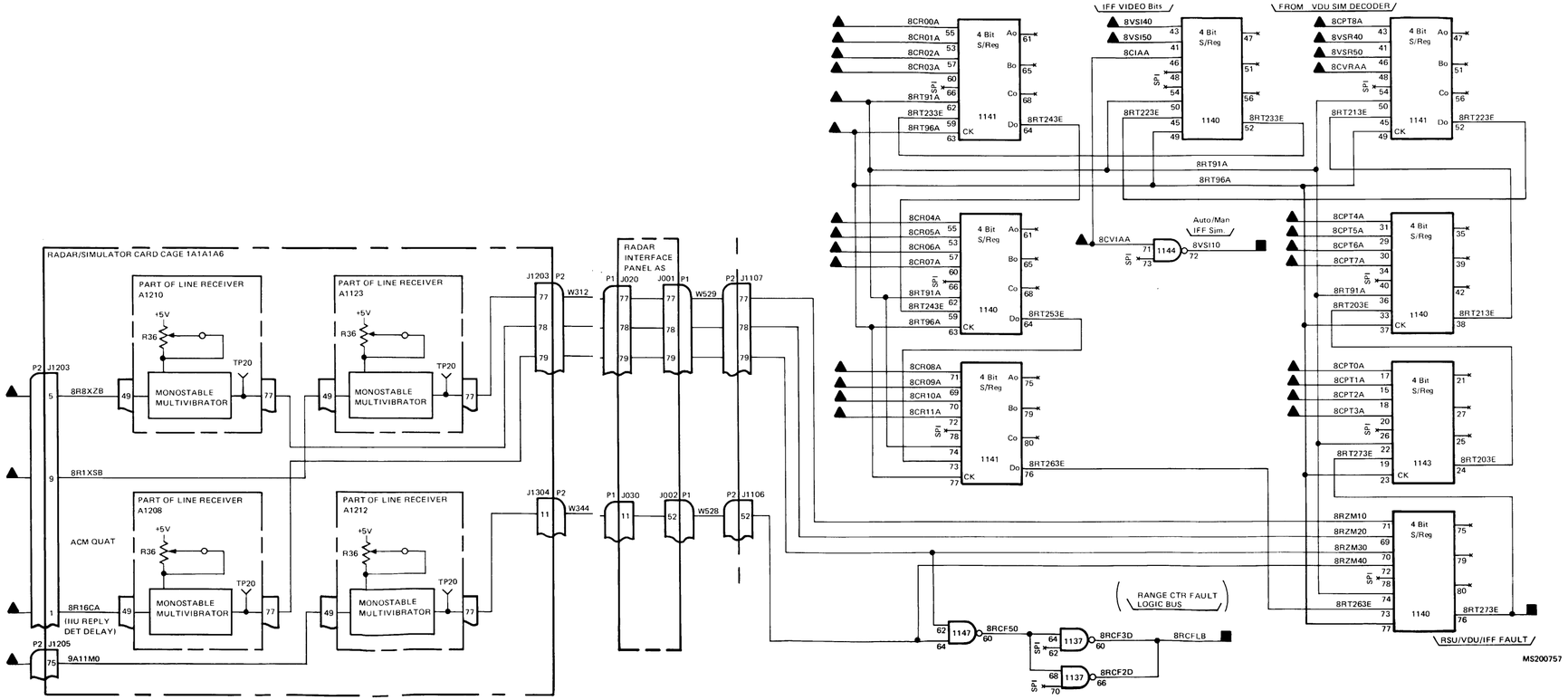
1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1. RIU BAY 1 CARD CAGE (1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1123, A1139, A1149 AND A1227

Change 3 FO-150 RSU TUP-32/ACM Clock Generator and Error Check Logic Diagram (Sheet 1 of 2)

INPUT SIGNAL	INPUT SOURCE FO-SH
8CPT0A	27101
8CPT1A	27101
8CPT2A	27101
8CPT3A	27101
8CPT4A	27101
8CPT5A	27101
8CPT6A	27101
8CPT7A	27101
8CPT8A	27101
8CR00A	26803
8CR00A	27101
8CR01A	26803
8CR01A	27101
8CR02A	27101
8CR03A	27101
8CR04A	27101
8CR05A	27101
8CR06A	27101
8CR07A	27101
8CR08A	27101
8CR09A	27101
8CR10A	27101
8CR11A	27101
8CVIAA	26803
8CVIAA	27102
8CVRAA	27102
8RT91A	16500
8RT96A	16500
8R1XSB	00300
8R1XSB	14900
8R1XSB	26802
8R1XSB	26803
8R1XSB	27501
8R16CA	14300
8R16CA	26901
8R8XZB	14300

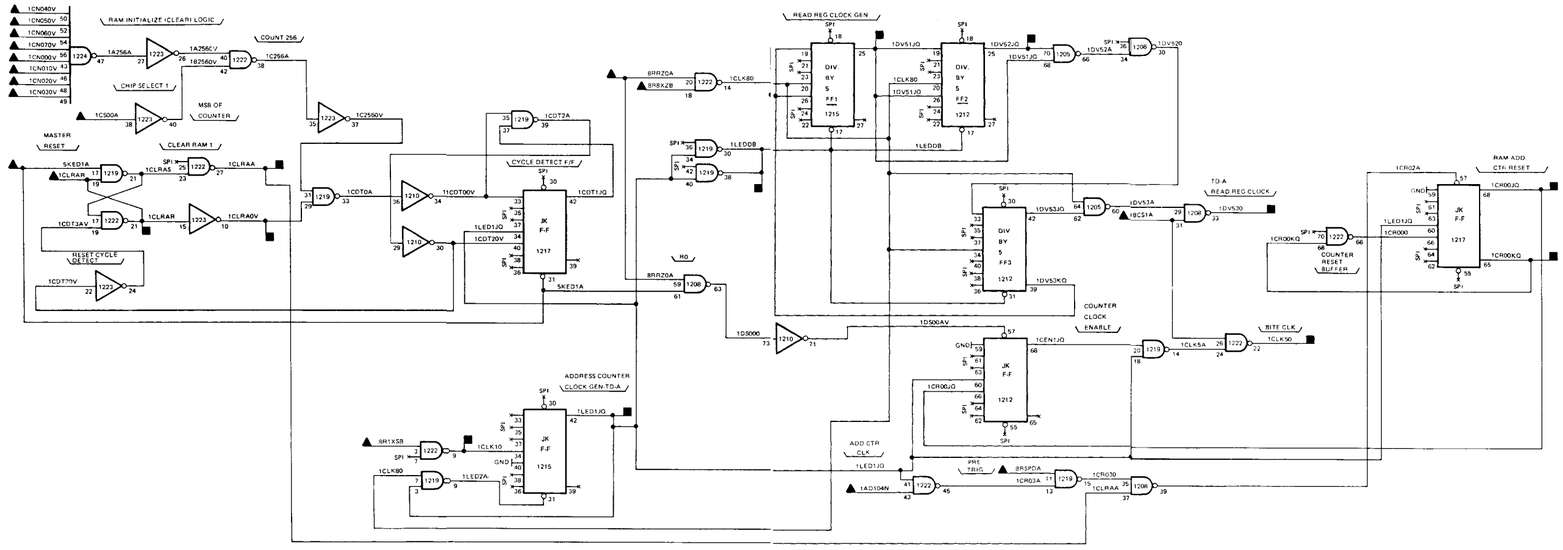
INPUT SIGNAL	INPUT SOURCE FO-SH
8R8XZB	27201
8VSI40	14000
8VSI40	26901
8VSI50	14000
8VSI50	26901
8VSR40	14000
8VSR40	26803
8VSR40	26901
8VSR50	14000
8VSR50	26803
8VSR50	26901

OUTPUT SIGNAL	OUTPUT DESTINATION FO-SH
8RCFLB	26803, 27102
8RT273E	16500
8VSI10	14000



FO-150 RSU TPS-32/ACM Clock Generator & Error Check Logic Diagram (Sheet 2 of 2)

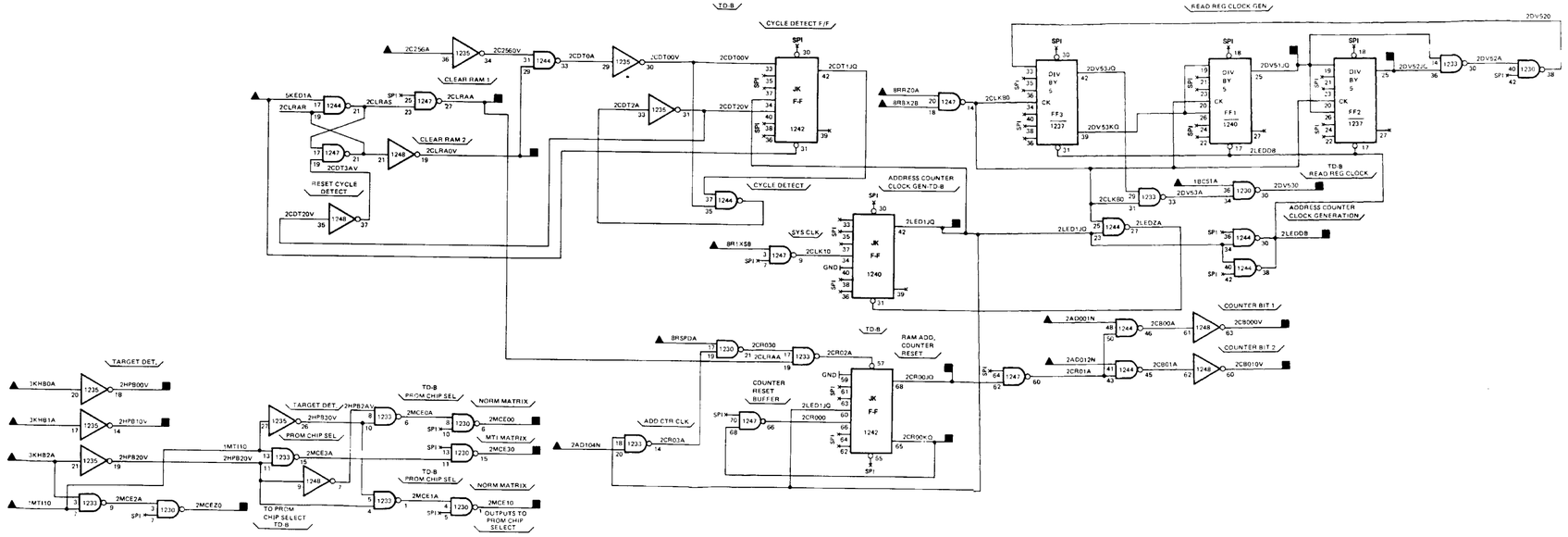
INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
1AD104N	15700	1CLK10	15300
1BCS1A	16500	2CLK50	15600, 15700, 16500
1CN000V	15600	1CLRAA	15400, 15700
1CN000V	27701	1CLRAR	15400
1CN010V	15600	1C_RA0V	16300
1CN010V	27701	1CR00JQ	15600, 16500
1CN020V	15600	1CR00KQ	15600, 15700
1CN020V	27701	1DV51JQ	16500
1CN030V	15600	1DV52JQ	16500
1CN030V	27701	1DV530	16300
1CN040V	15600	1LED0B	15400, 15700
1CN040V	27701	1LED1JQ	16500
1CN050V	15600		
1CN050V	27701		
1CN060V	15600		
1CN060V	27701		
1CN070V	15600		
1CN070V	27701		
1CS00A	15700		
1CS00A	27701		
5KED1A	13400		
5KED1A	26803		
5KED1A	31301		
BRR2OA	14600		
BRSPDA	14400		
BR1XSB	00300		
BR1XSB	14900		
BR1XSB	26802		
BR1XSB	26803		
BR1XSB	27501		
BR8XZB	14300		
BR8XZB	27201		



Change 3 FO-151. TDU-A Timing Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1. RIU BAY 1 CARD CAGE (1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1123, A1139, A1149 AND A1227

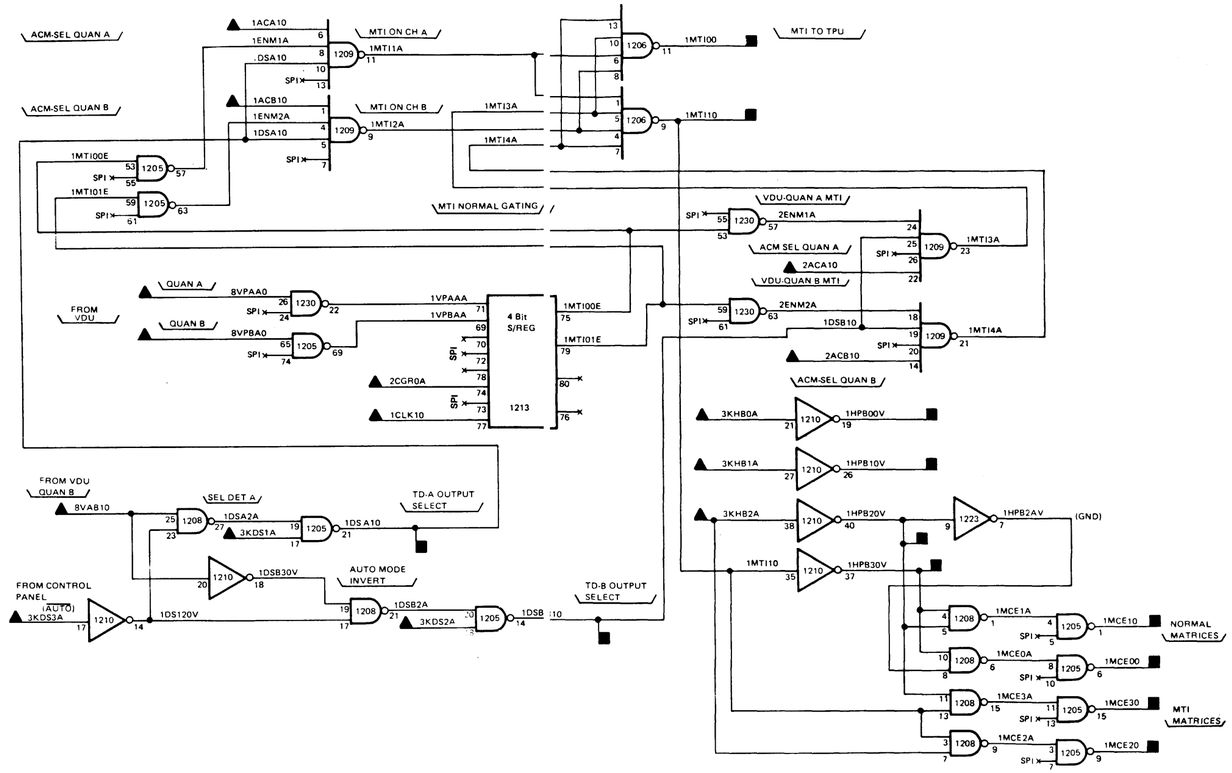


Change 3 FO-152. TDU-B Timing Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1.
   RIU BAY 1 CARD CAGE (1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1123, A1139, A1149 AND A1227

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
1ACA10	15400	1DSA10	16300
1ACB10	15400	1DSB10	16400
1CLK10	15100	1HPB00V	16000, 16500, 26803, 27602
2ACA10	15500	1HPB10V	16000, 16500, 26803, 27602
2ACB10	15500	1HPB20V	16500
2CGR0A	15500	1HPB30V	16500
3KDS1A	27102	1MCE00	16000, 26803, 27602
3KDS2A	27102	1MCE10	16000, 26803, 27602
3KDS3A	27102	1MCE20	16000, 26803, 27602
3KHB0A	27001	1MCE30	16000, 26803, 27602
3KHB0A	28901	1MTI00	08502, 16500, 26803, 27502
3KHB1A	27001	1MTI10	15200
3KHB1A	28901		
3KHB2A	27102		
8VAB10	13900		
8VPAA0	14100		
8VPBA0	14100		

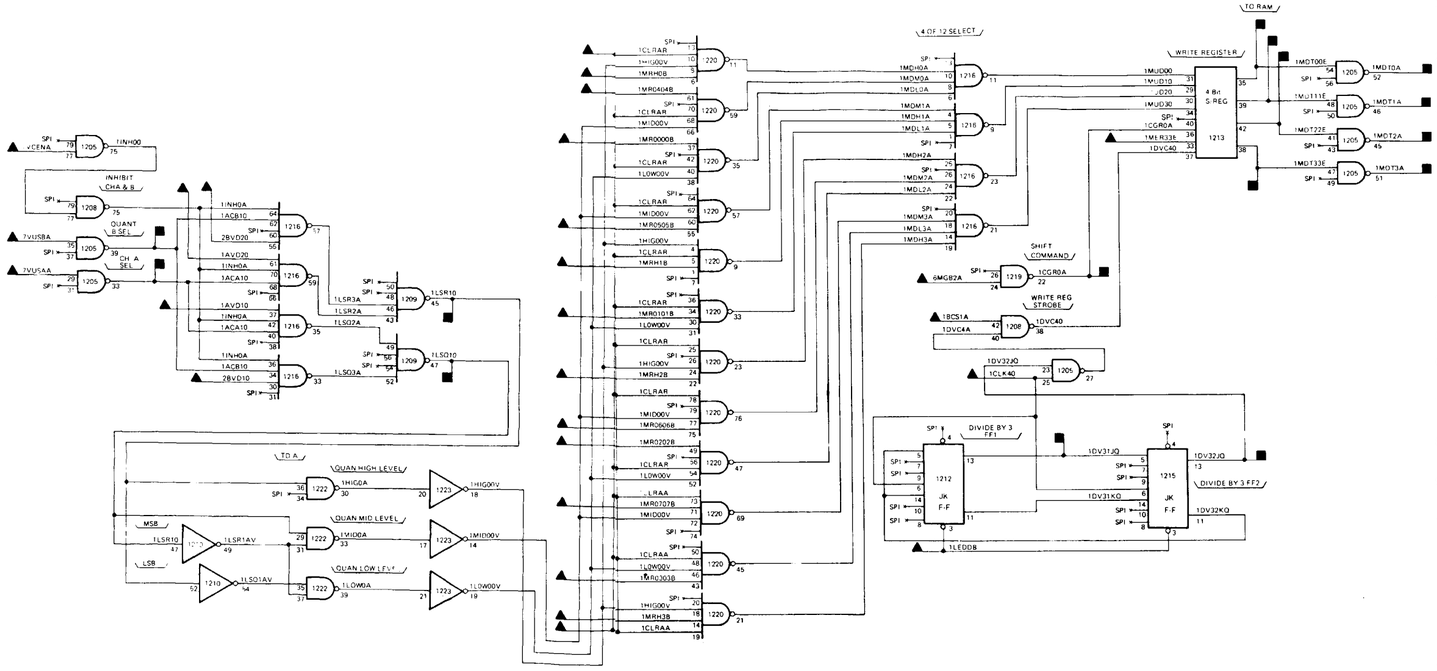


Change 3 FO-153. TDU MTI and Matrix Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1.
   RIU BAY 1 CARD CAGE (1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1123, A1139, A1149 AND A1227

INPUT		INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
1AVD10	26803	6MGB2A	05600	1ACA10	15300
1AVD20	26803	7VCENA	03900	1ACB10	15300
1BCS1A	16500	7VCENA	27202	1CGR0A	15600, 15700, 16300, 16500
1CLK40	15700	7VUSAA	03900	1DV31JQ	16500
1CLRAA	15100	7VUSAA	27202	1DV32JQ	16500
1CLRAR	15100	7VUSBA	03900	1LSR10	16500
1LEDDB	15100	7VUSBA	27202	1MDT0A	16300
1MER33E	16300			1MDT00E	16101, 26803, 27701
1MRH0B	16000			1MDT1A	16300
1MRH09	26803			1MDT11E	16101, 26803, 27701
1MRH0B	27601			1MDT2A	16300
1MRH1B	16000			1MDT22E	16101, 26803, 27701
1MRH1B	26803			1MDT3A	16300
1MRH1B	27601			1MDT33E	16500
1MRH2B	16000				
1MRH2B	26803				
1MRH2B	27601				
1MRH3B	16000				
1MRH3B	26803				
1MRH3B	27601				
1MR0000B	16000				
1MR0000B	26803				
1MR0000B	27601				
1MR0101B	16000				
1MR0101B	26803				
1MR0101B	27601				
1MR0202B	16000				
1MR0202B	26803				
1MR0202B	27601				
1MR0303B	16000				
1MR0303B	26803				
1MR0303B	27601				
1MR0404B	16000				
1MR0404B	26803				
1MR0404B	27601				
1MR0505B	16000				
1MR0505B	26803				
1MR0505B	27601				
1MR0606B	16000				
1MR0606B	26803				
1MR0606B	27601				
1MR0707B	16000				
1MR0707B	26803				
1MR0707B	27601				
2BVD10	26803				
2BVD20	26803				



Change 3 FO-154. TDU-A Write Register Logic Diagram

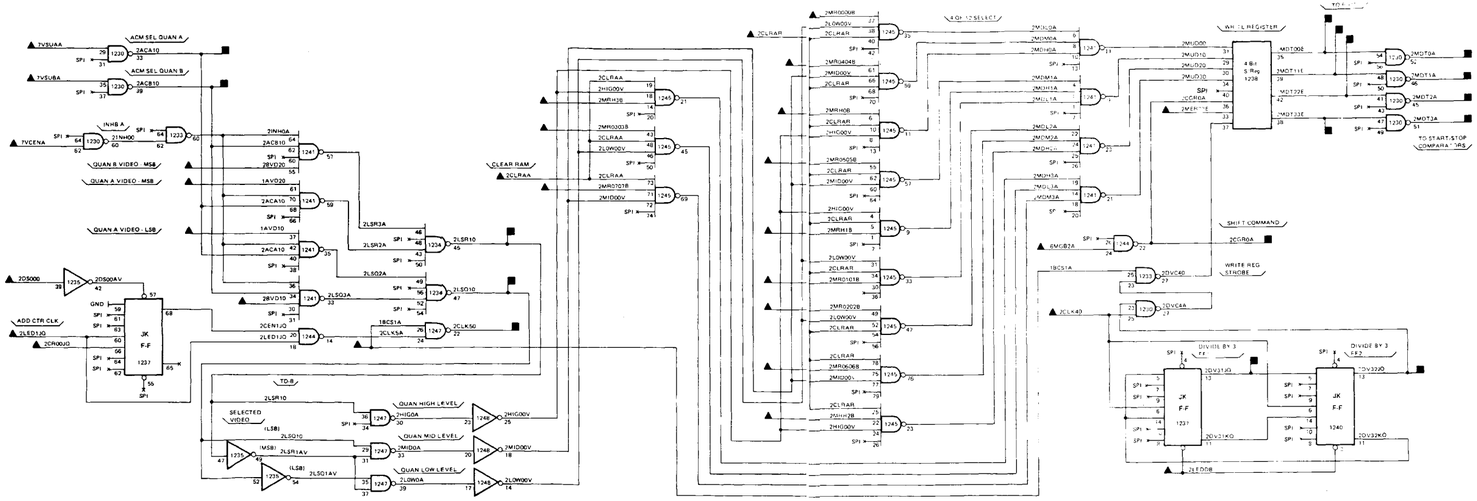
NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1. RIU BAY 1 CARD CAGE (1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1123, A1139, A1149 AND A1227

INPUT SIGNAL	INPUT SOURCE FO-SH
1AVD10	26803
1AVD20	26803
1BCS1A	16500
2BVD10	26803
2BVD20	26803
2CLKA0	15800
2CLRAA	15200
2CLRAR	15200
2CR00JG	15200
2DS000	15800
2LEDDB	15200
2LED1JG	15200
2MER33E	16400
2MRH0B	16000
2MRH0B	26803
2MRH0B	27602
2MRH1B	16000
2MRH1B	26803
2MRH1B	27602
2MRH2B	16000
2MRH2B	26803
2MRH2B	27602
2MRH3B	16000
2MRH3B	26803
2MRH3B	27602
2MR0000B	16000
2MR0000B	26803
2MR0000B	27601
2MRC 01B	16000
2MRC 01B	26803
2MR01C 9	27602
2MR0202B	16000
2MR0202B	26803
2MR0202B	27602
2MR0303B	16000
2MR0303B	26803
2MR0303B	27602
2MR0404B	16000
2MR0404B	26803
2MR0404B	27602
2MR0505B	16000
2MR0505B	26803
2MR0505B	27602
2MR0606B	16000
2MR0606B	26803
2MR0606B	27602

INPUT SIGNAL	SOURCE FO-SH
2MR07078	16000
2MR07078	26803
2MR07078	27602
6MGB2A	05600
7VCENA	03900
7VCENA	27202
7VUSAA	03900
7VUSAA	27202
7VUSBA	03900
7VUSBA	27202

OUTPUT SIGNAL	DESTINATION FO-SH
2ACA10	15300
2ACB10	15300
2CGR0A	15300, 15800, 15900, 16400, 16500
2CLK50	15800, 15900, 16500
2DV32JG	16500
2LSQ10	16500
2LSR10	16500
2MDT0A	16400
2MDT00E	16201, 26803, 27701
2MDT1A	16400
2MDT22E	16201, 26803, 27702
2MDT3A	16400
2MDT33E	16500

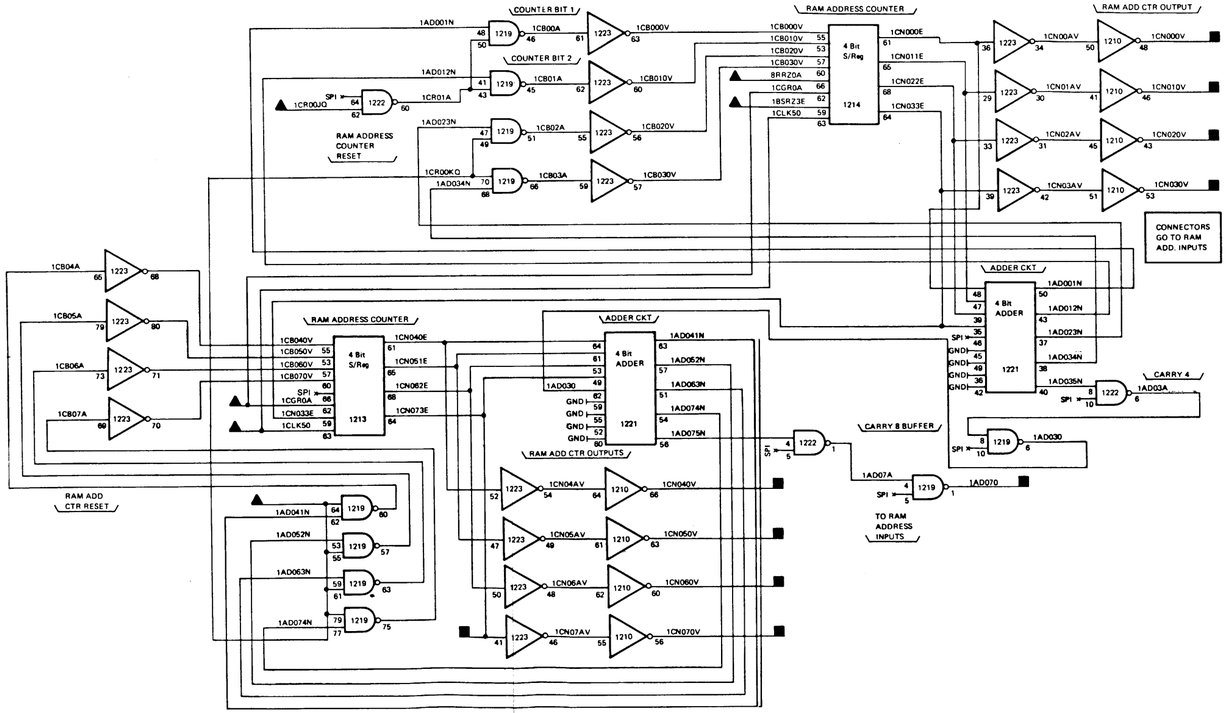


Change 3 FO-155. TDU-B Write Register Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOI COMPLETE DESIGNATIONS, PREFIX WITH APPLICABL UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINEI IN EQUIPMENT RACK 1, RIU BAY 1 CARD CAGE (1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTIOI DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUI CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATIOI AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOI CARDS A1123, A1139, A1149 AND A1227

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
1BSR23E	16500	1AD070	15700
1CGR0A	15400	1CN000V	15100, 16101, 26803, 27701
1CLK50	15100	1CN010V	15100, 16101, 26803, 27701
1CR00JQ	15100	1CN020V	15100, 16101, 26803, 27701
1CR00KQ	15100	1CN030V	15100, 16101, 26803, 27701
8RRZ0A	14600	1CN040V	15100, 16101, 26803, 27701
		1CN050V	15100, 16101, 26803, 27701
		1CN060V	15100, 16101, 26803, 27701
		1CN070V	15100, 16101, 26803, 27701
		1CN073E	15700



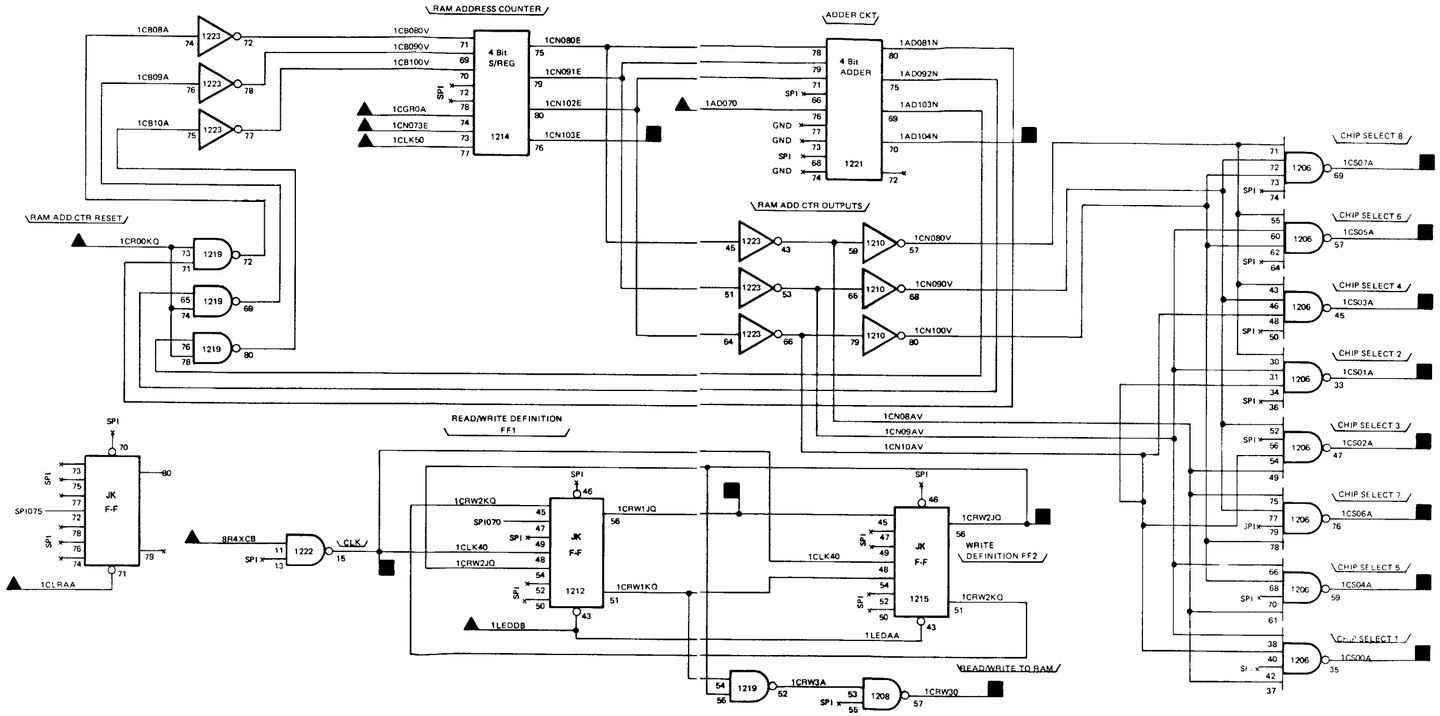
Change 3 FO-156. TDU RAM Address Counter Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1. RIU BAY 1 CARD CAGE (1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1123, A1139, A1149 AND A1227

INPUT SIGNAL	SOURCE FO-SH
1AD070	15600
1CGR0A	15400
1CLK50	15100
1CLRAA	15100
1CN073E	15600
1CR00KQ	15100
1LEDDB	15100
8R4XCB	14900
8R4XCB	26802
8R4XCB	27201

OUTPUT SIGNAL	DESTINATION FO-SH
1AD104N	15100
1CLK40	15400
1CN103E	16300
1CRW1JQ	16500
1CRW2JQ	16500
1CRW30	16101, 26803, 27602
1CS00A	15100, 16104, 26803, 27701
1CS01A	16104, 26803, 27701
1CS02A	16103, 26803, 27701
1CS03A	16103, 26803, 27701
1CS04A	16102, 26803, 27701
1CS05A	16102, 26803, 27701
1CS06A	16101, 26803, 27701
1CS07A	16101, 26803, 27701

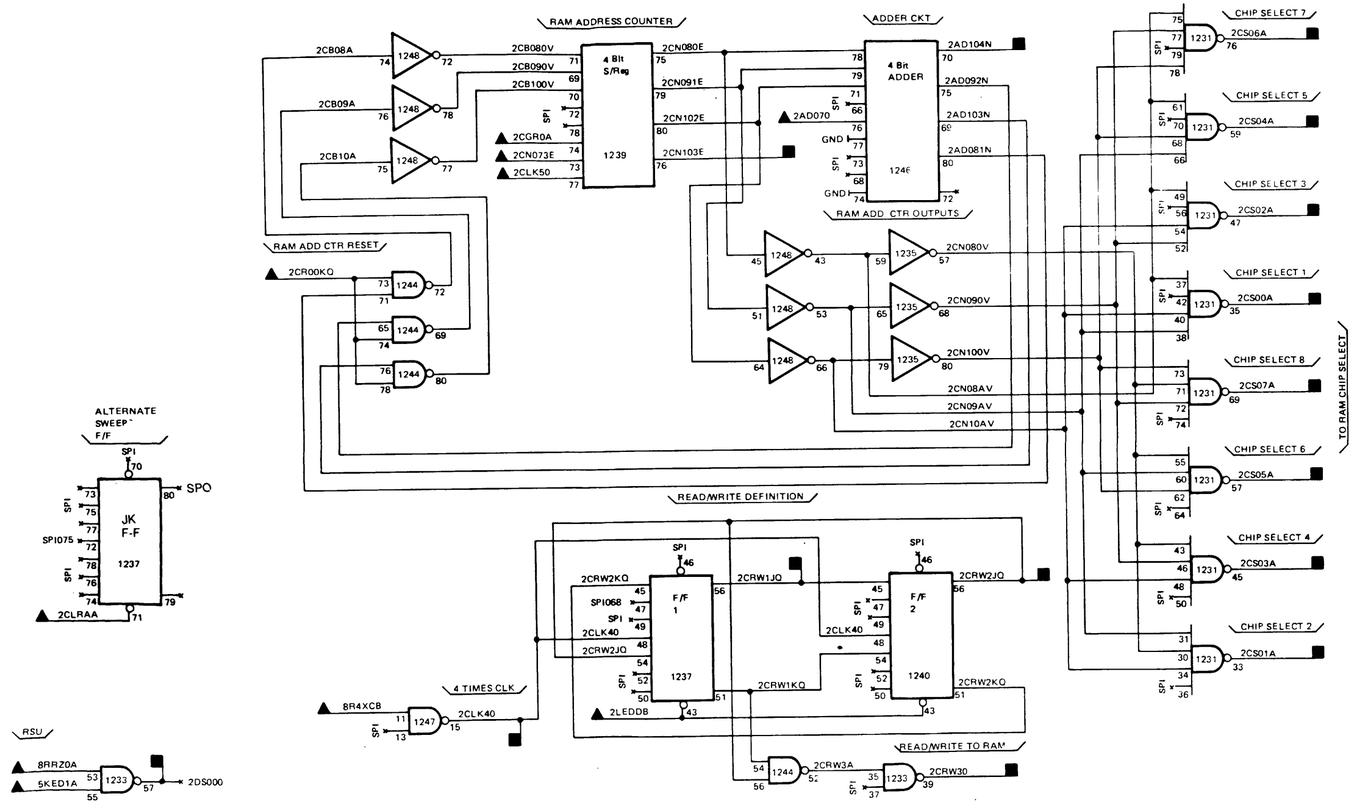


NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1. RIU BAY 1 CARD CAGE (1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1123, A1139, A1149 AND A1227

Change 3 FO-157. TDU-A RAM Chip Selection Logic Diagram

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
2AD070	15900	2AD104N	15200
2CGR0A	15500	2CLK40	15500
2CLK50	15500	2CN103E	16400
2CLRAA	15200	2CRW1JQ	16500
2CN073E	15900	2CRW2JQ	16500
2CR00KQ	15200	2CRW3D	16201, 26803, 27602
2LEDDB	15200	2CS00A	15900, 16204, 26803, 27702
5KED1A	13400	2CS01A	16204, 26803, 27702
5KED1A	26803	2CS02A	16203, 26803, 27702
5KED1A	31301	2CS03A	16203, 26803, 27702
8RRZ0A	14600	2CS04A	16202, 26803, 27702
8R4XCB	14900	2CS05A	16202, 26803, 27702
8R4XCB	26802	2CS06A	16201, 26803, 27702
8R4XCB	27201	2CS07A	16201, 26803, 27702
		2DS000	15500



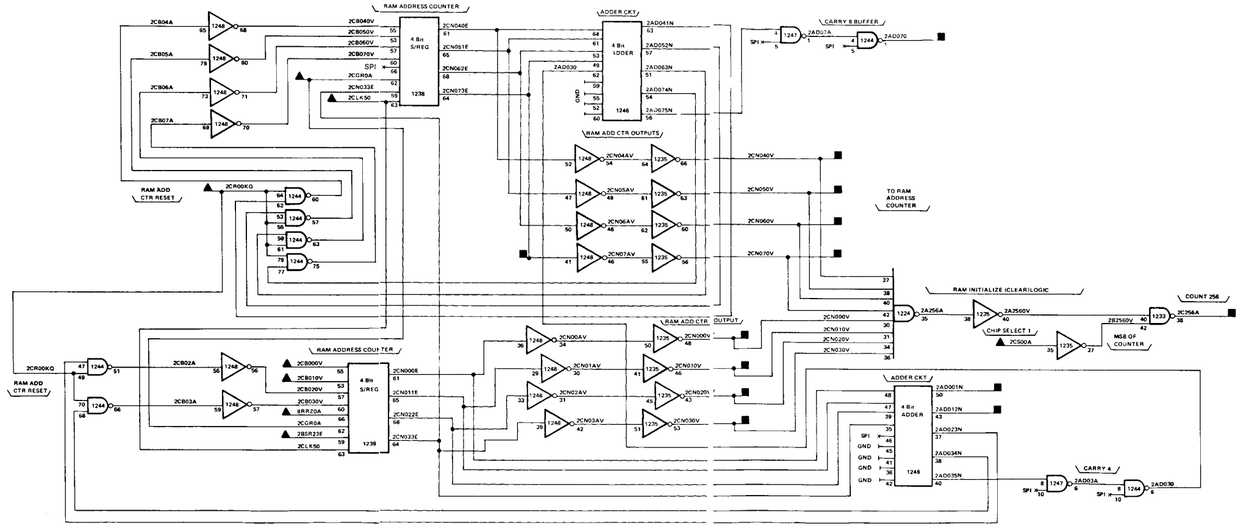
Change 3 FO-158. TDU-B RAM Chip Selection Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1. RIU BAY 1 CARD CAGE (1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1123, A1139, A1149 AND A1227

MS 200765B

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
2BSR23E	16500	2AD001N	15200
2CB000V	15200	2AD012N	15200
2CB010V	15200	2AD070	15800
2CGR0A	15500	2CN000V	16201, 26803, 27702
2CLK50	15500	2CN010V	16201, 26803, 27702
2CR00KG	15200	2CN020V	16201, 26803, 27702
2CS00A	15800	2CN030V	16201, 26803, 27702
8RR20A	14600	2CN040V	16201, 26803, 27702
		2CN050V	16201, 26803, 27702
		2CN060V	16201, 26803, 27702
		2CN070V	16201, 26803, 27702
		2CN073E	15800
		2C256A	15200

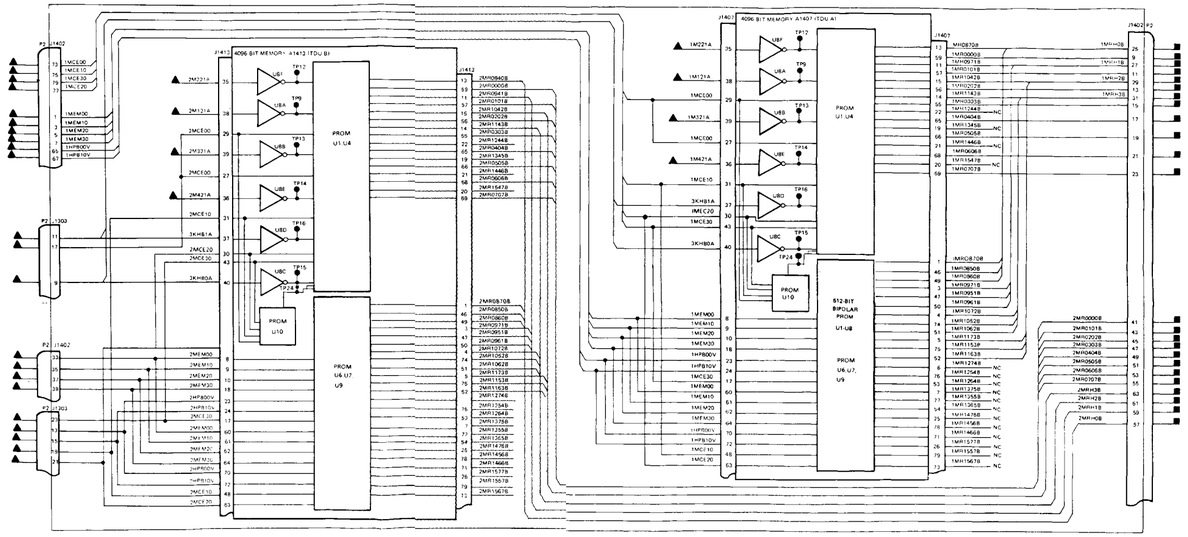


Change 3 FO-159. TDU-B RAM Addressing and Clear Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1. RIU BAY 1 CARD CAGE (1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1123, A1139, A1149 AND A1227

INPUT		INPUT		OUTPUT	
SIGNAL	SOURCE FO-SP	SIGNAL	SOURCE FO-SII	SIGNAL	DESTINATION FO-SH
1HPB00V	15300	2M321A	16202	1MRH0B	15400, 27601
1HPB00V	27602	2M-21A	16201	1MRH1B	15400, 27601
1HPB10V	15300	3KHB0A	27001	1MRH2B	15400, 27601
1HPB10V	27602	3KHB0A	28901	1MRH3B	15400, 27601
1MCE00	15300	3KHB1A	27001	1MR0000B	15400, 27601
1MCE00	27602	3KHB1A	28901	1MR0101B	15400, 27601
1MCE10	15300			1MR0202B	15400, 27601
1MCE10	27602			1MR0303B	15400, 27601
1MCE20	15300			1MR0404B	15400, 27601
1MCE20	27602			1MR0505B	15400, 27601
1MCE30	15300			1MR0606B	15400, 27601
1MCE30	27602			1MR0707B	15400, 27601
1MEM00	26803			2MRH0B	15500, 27602
1MEM00	27601			2MRH1B	15500, 27602
1MEM10	26803			2MRH2B	15500, 27602
1MEM10	27601			2MRH3B	15500, 27602
1MEM20	26803			2MR0000B	15500, 27601
1MEM20	27601			2MR0101B	15500, 27602
1MEM30	26803			2MR0202B	15500, 27602
1MEM30	27601			2MR0303B	15500, 27602
1M121A	16105			2MR0404B	15500, 27602
1M221A	16104			2MR0505B	15500, 27602
1M321A	16103			2MR0606B	15500, 27602
1M421A	16102			2MR0707B	15500, 27602
2HPB00V	15200				
2HPB00V	28901				
2HPB10V	15200				
2HPB10V	28901				
2MCE00	15200				
2MCE00	28901				
2MCE10	15200				
2MCE10	28901				
2MCE20	15200				
2MCE20	28901				
2MCE30	15200				
2MCE30	28901				
2MEM00	26803				
2MEM00	27601				
2MEM10	26803				
2MEM10	27601				
2MEM20	26803				
2MEM20	27601				
2MEM30	26803				
2MEM30	27601				
2M121A	16204				
2M221A	16203				



NOTES: UNLESS OTHERWISE SPECIFIED

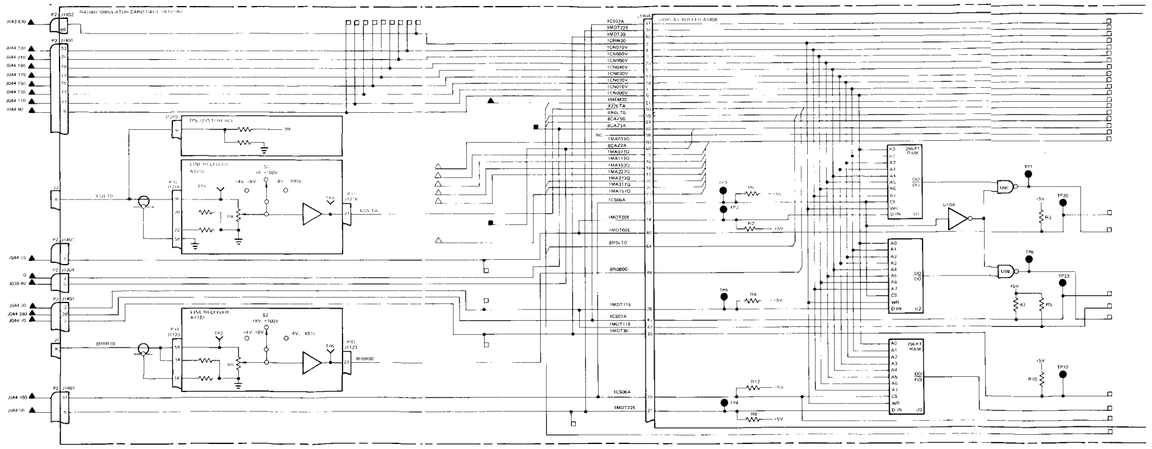
1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1. RSU BAY 1 CARD CAGE (1A1A1A4.)
3. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
4. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
5. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.

MS200767B

Change 3 FO-160. TDU PROM Logic Diagram

INPUT	
SIGNAL	SOURCE F0-5H
1CND00V	15600
1CND00V	27701
1CND10V	15600
1CND10V	27701
1CND20V	15600
1CND20V	27701
1CND30V	15600
1CND30V	27701
1CND40V	15600
1CND40V	27701
1CND50V	15600
1CND50V	27701
1CND60V	15600
1CND60V	27701
1CND70V	15600
1CND70V	27701
1CRW3C	15700
1CRW3C	27602
1CSD6A	15700
1CSD6A	27701
1CSD7A	15700
1CSD7A	27701
1MDT00E	15400
1MDT00E	27701
1MDT11E	15400
1MDT11E	27701
1MDT22E	15400
1MDT22E	27701
1MDT30	16300
1MDT30	27701
1MEM30	26803
1PEM30	27603
8CA12A	27802
8CA23A	27802

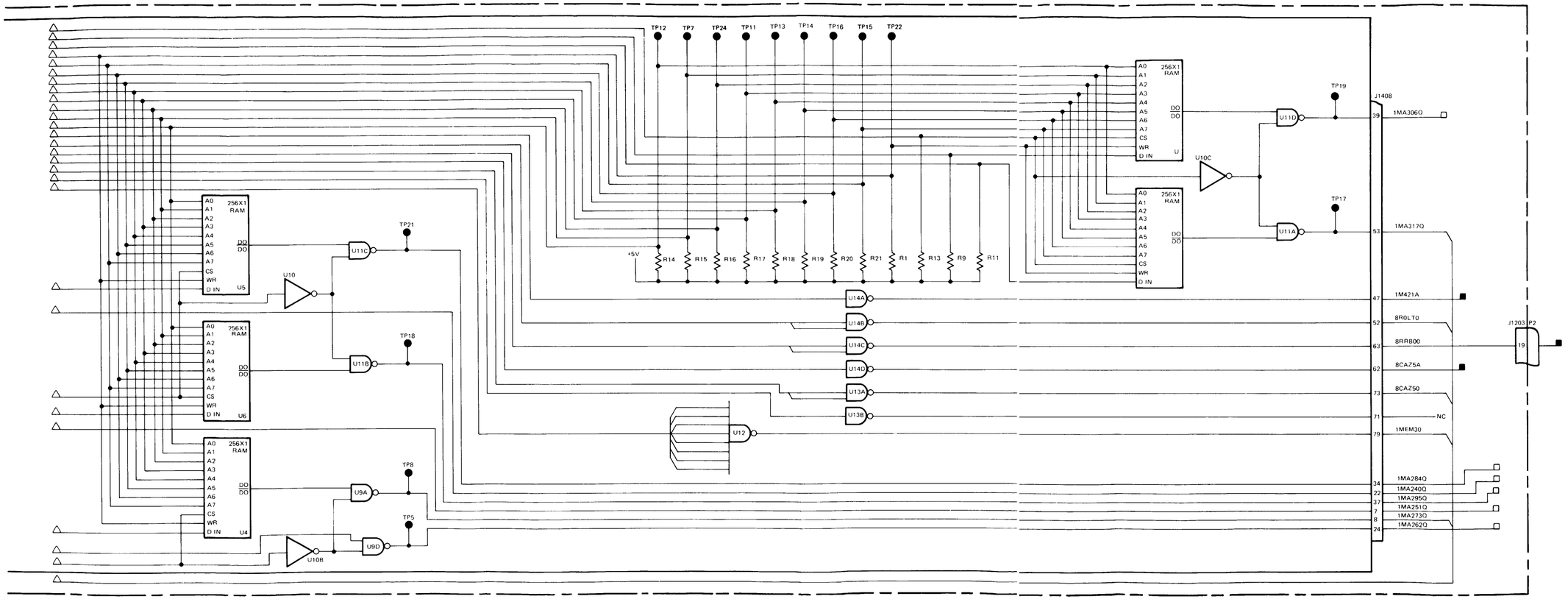
OUTPUT	
SIGNAL	DESTINATION F0-5M
1MA21A	16000
8CA25A	16600, 27802, 30102
BRRB00	14500



NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1. RIU BAY 1 CARD CAGE (1A1A1A6.)
3. REFER to table 5-2 FOR KEY SIGNAL LOOK UP LISTING
4. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
5. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.

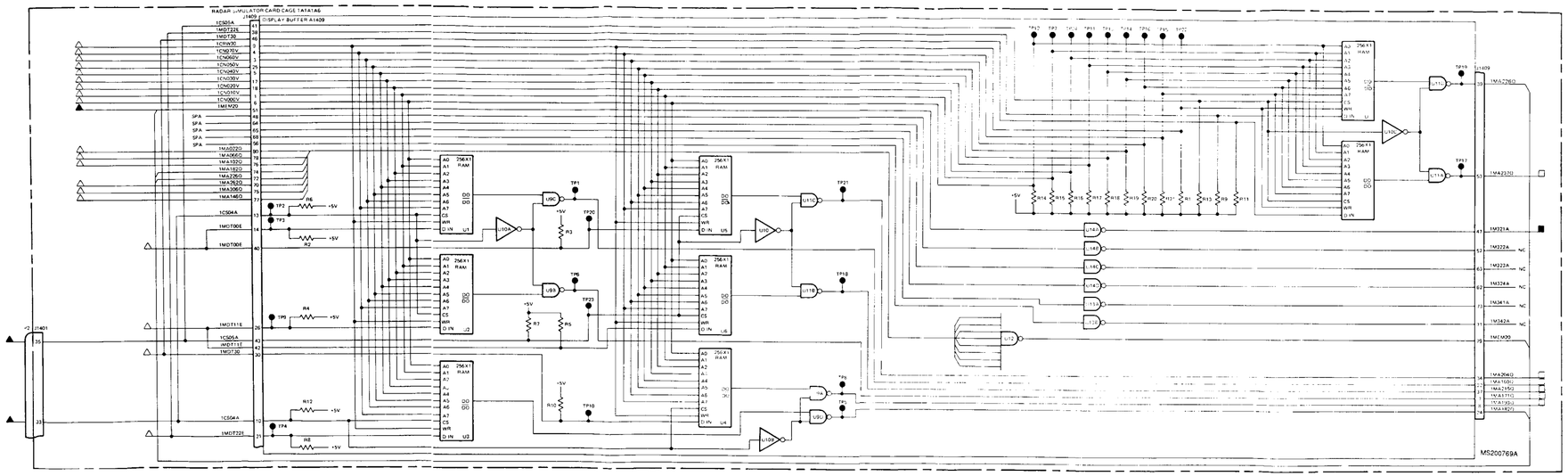
F0-161. TDU Channel A RAM Logic Diagram (Sheet 1 of 5)



MS200768

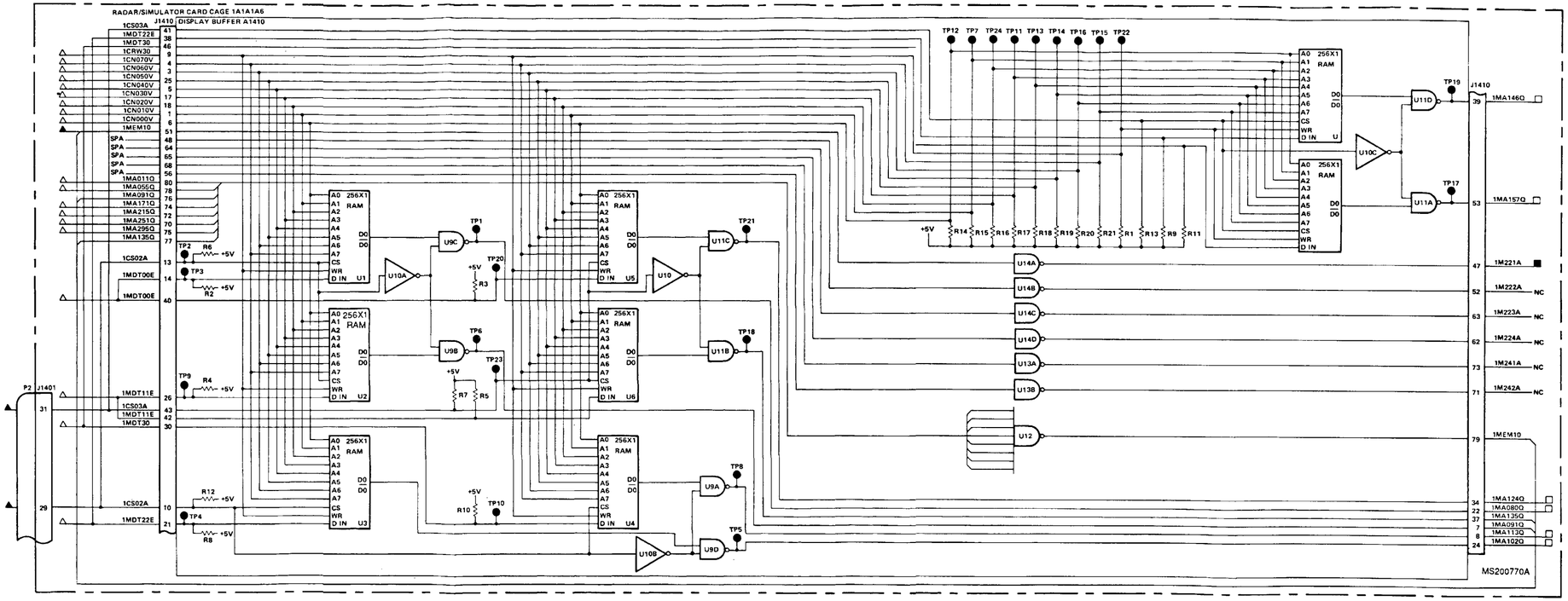
FO-161. TDU Channel A RAM Logic Diagram (Sheet 2 of 5)

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-S-
1CS04A	15700	1M321A	16000
1CS04A	27701		
1CS05A	15700		
1CS05A	27701		
1MEM20	26803		
1MEM20	27601		



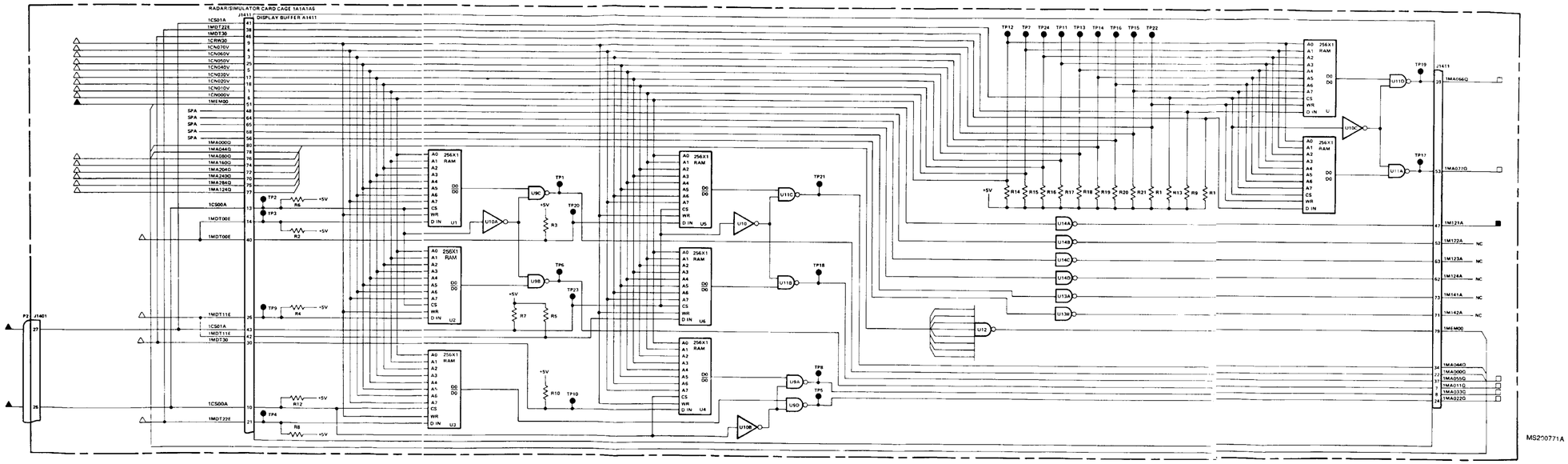
Change 3 FO-161. TDU Channel A RAM Logic Diagram (Sheet 3 of 5).

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
1CS02A	15700	1M221A	16000
1CS02A	27701		
1CS03A	15700		
1CS03A	27701		
1MEM10	26803		
1MEM10	27601		



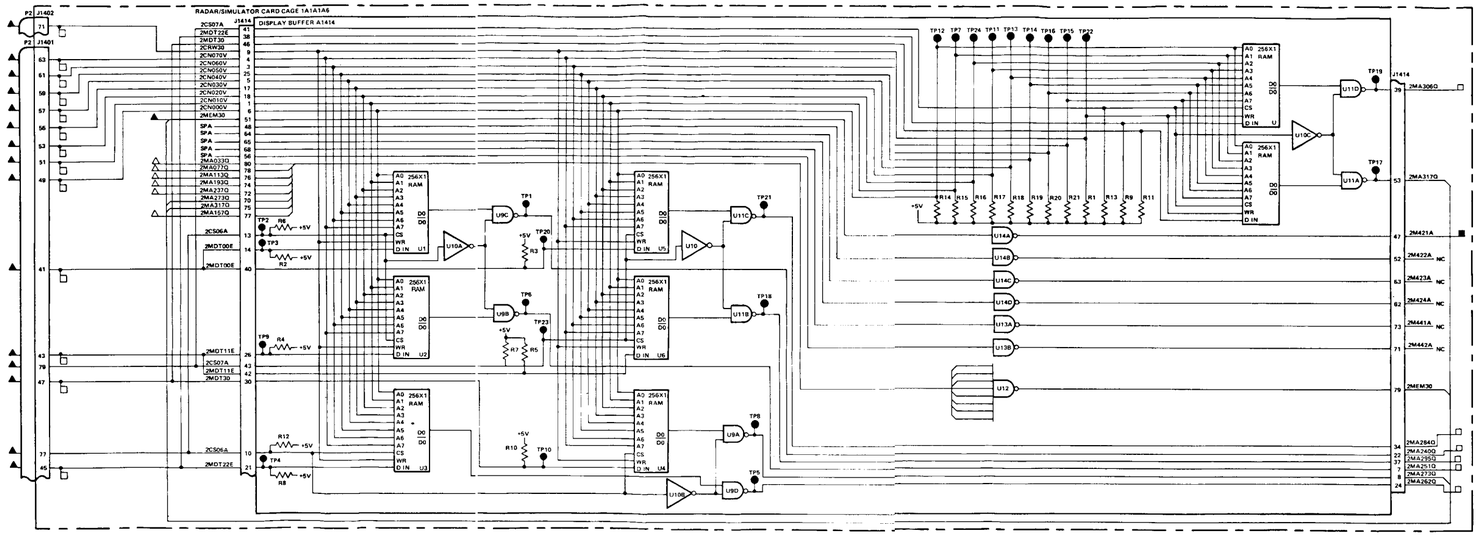
Change 3 FO-161. TDU Channel A RAM Logic Diagram (Sheet 4 of 5).

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
1CS00A	15700	1M121A	16000
1CS00A	27701		
1CS01A	15700		
1CS01A	27701		
1MEM00	26803		
1MEM00	27601		



Change 3 FO-161. TDU Channel A RAM Logic Diagram (Sheet 5 of 5).

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
2CN000V	15900	2M421A	16000
2CN000V	27702		
2CN010V	15900		
2CN010V	27702		
2CN020V	15900		
2CN020V	27702		
2CN030V	15900		
2CN030V	27702		
2CN040V	15900		
2CN040V	27702		
2CN050V	15900		
2CN050V	27702		
2CN060V	15900		
2CN060V	27702		
2CN070V	15900		
2CN070V	27702		
2CRW30	15800		
2CRW30	27602		
2CS06A	15800		
2CS06A	27702		
2CS07A	15800		
2CS07A	27702		
2MDT00E	15500		
2MDT00E	27701		
2MDT11E	15500		
2MDT11E	27702		
2MDT22E	15500		
2MDT22E	27702		
2MDT30	16400		
2MDT30	27702		
2MEM30	26803		
2MEM30	27601		



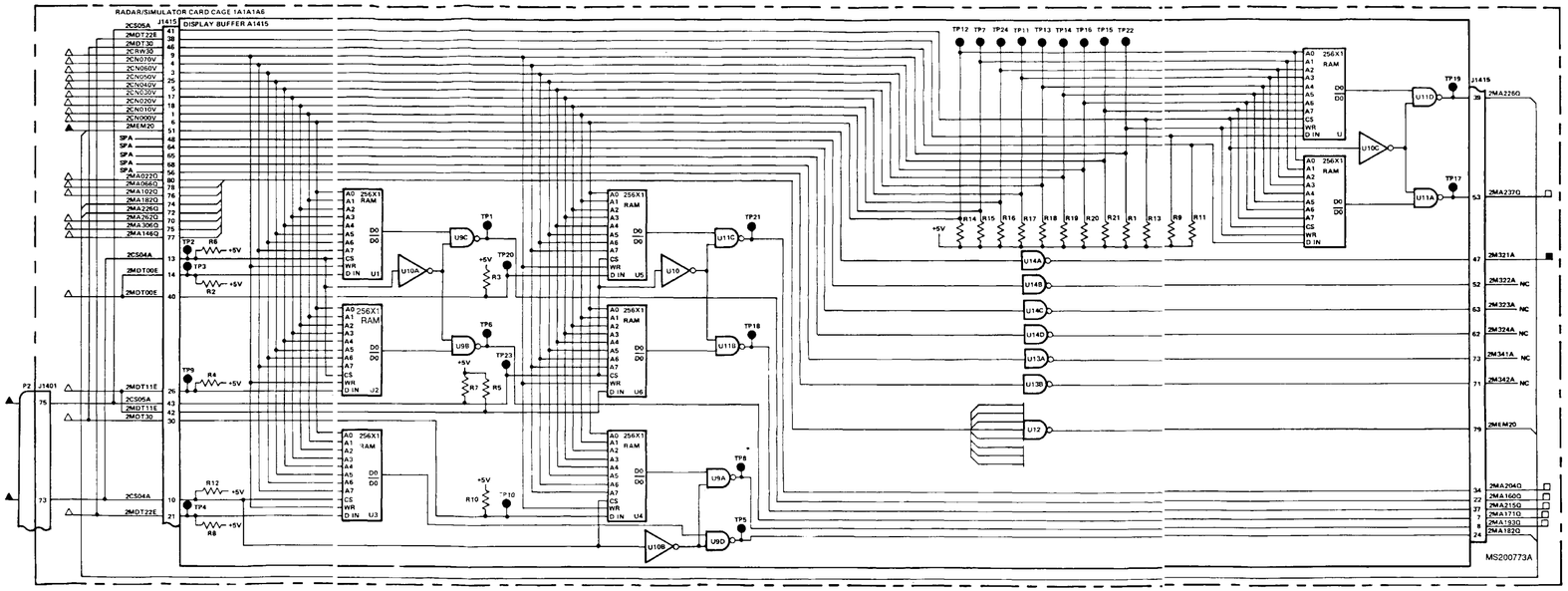
Change 3 FO-162. TDU Channel B RAM Logic Diagram (Sheet 1 of 4).

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1. RSU (1A1A1A6.)
3. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING:
4. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING.
5. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS

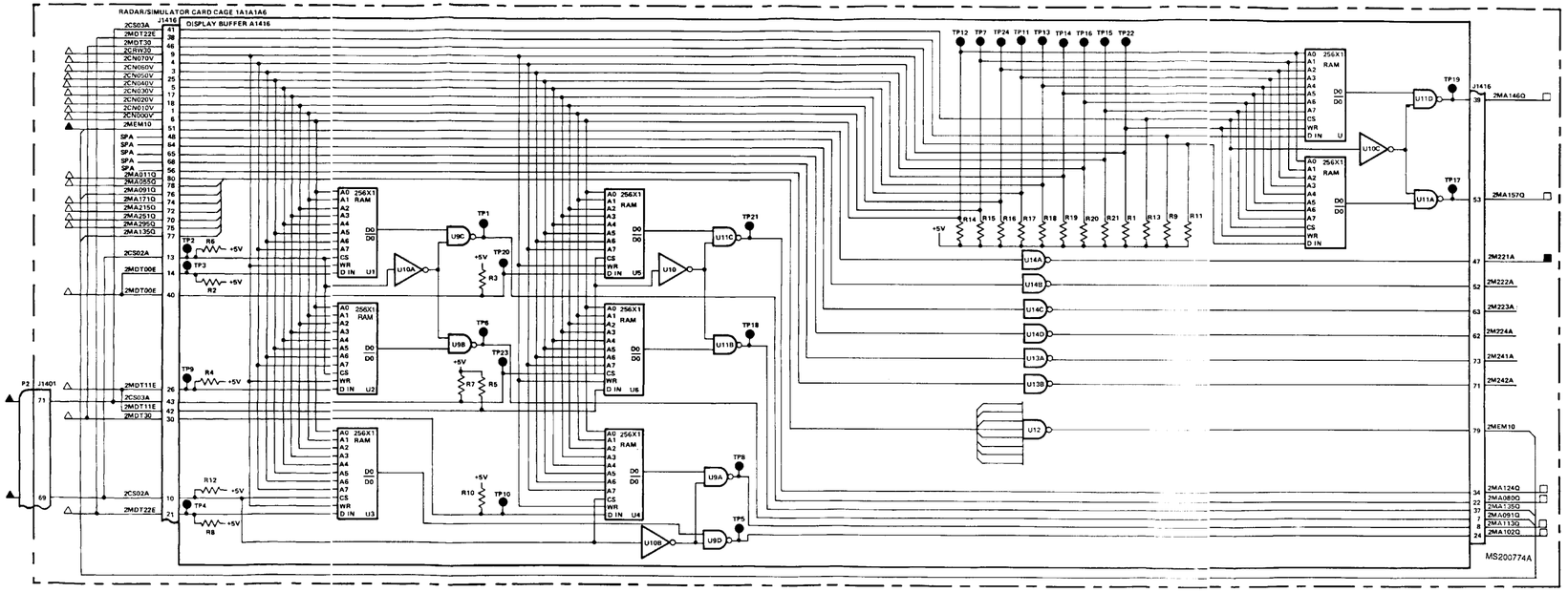
MS200772A

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
2CS04A	15800	2M321A	16000
2CS04A	27702		
2CS05A	15800		
2CS05A	27702		
2MEM20	26803		
2MEM20	27601		



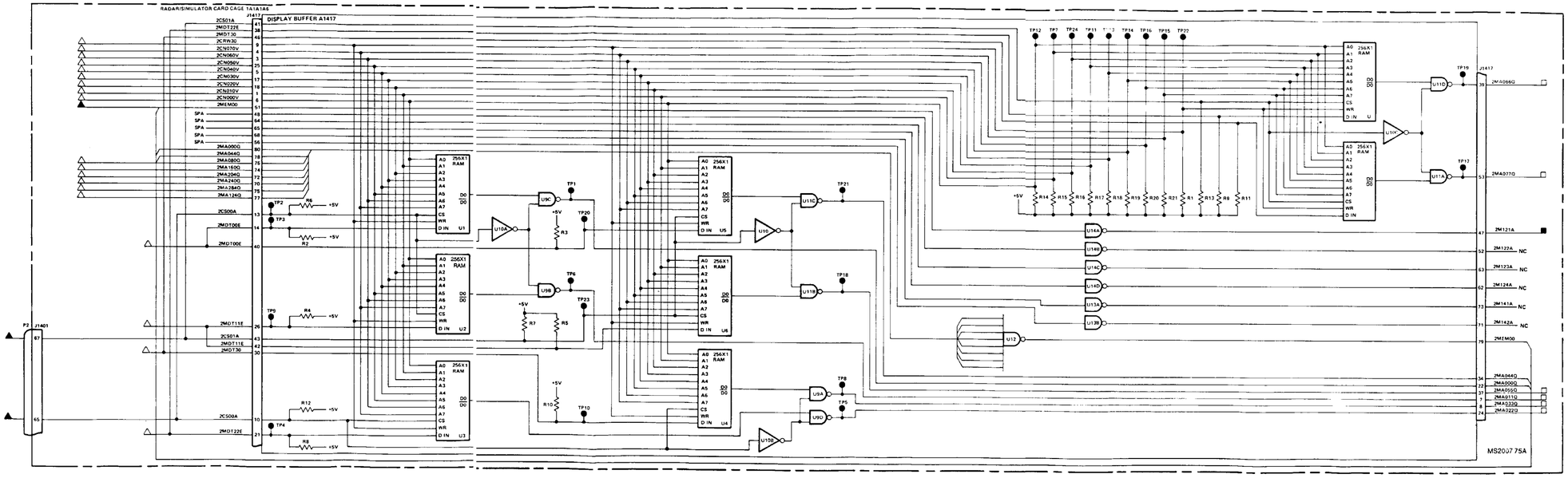
Change 3 FO-162. TDU Channel B RAM Logic Diagram (Sheet 2 of 4).

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
2CS02A	15800	2M221A	16000
2CS02A	27702		
2CS03A	15800		
2CS03A	27702		
2MEM10	26803		
2MEM10	27601		



Change 3 FO-162. TDU Channel B RAM Logic Diagram (Sheet 3 of 4).

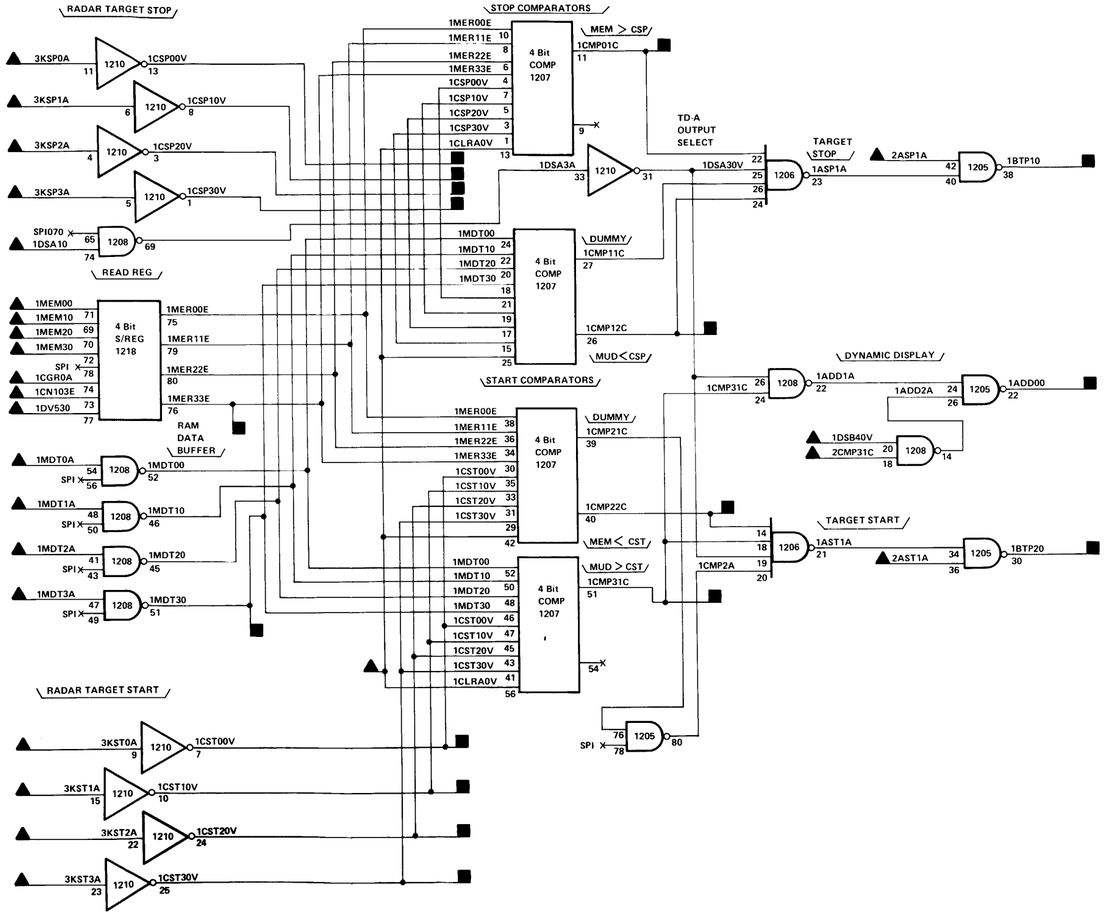
INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
2CS00A	15800	2M121A	16000
2CS00A	27702		
2CS01A	15800		
2CS01A	27702		
2MEM00	26803		
2MEM00	27601		



Change 3 FO-162. TDU Channel B RAM Logic Diagram (Sheet 4 of 4)

INPUT SIGNAL	SOURCE FO-SH
1CGR0A	15400
1CLRA0V	15100
1CN103E	15700
1DSA10	15300
1DSB40V	16400
1DV530	15100
1MDT0A	15400
1MDT1A	15400
1MDT2A	15400
1MDT3A	15400
1MEM00	26803
1MEM00	27601
1MEM10	26803
1MEM10	27601
1MEM20	26803
1MEM20	27601
1MEM30	26803
1MEM30	27601
2ASP1A	16400
2AST1A	16400
2CMP31C	16400
3KSP0A	27002
3KSP1A	27002
3KSP2A	27002
3KSP3A	27002
3KST0A	27002
3KST1A	27002
3KST2A	27002
3KST3A	27002

OUTPUT SIGNAL	DESTINATION FO-SH
1ADD00	08502, 11200, 16500, 26802, 26803, 27502
1BTP10	08502, 11200, 16500, 26802, 26803, 27502
1BTP20	11200, 16500, 26803, 27502
1CMP01C	16500
1CMP12C	16500
1CMP22C	16500
1CMP31C	16500
1CSP00V	16500
1CSP10V	16500
1CSP20V	16500
1CSP30V	16500
1CST00V	16500
1CST10V	16500
1CST20V	16500
1CST30V	16500
1MDT30	16101, 26803, 27701
1MER33E	15400



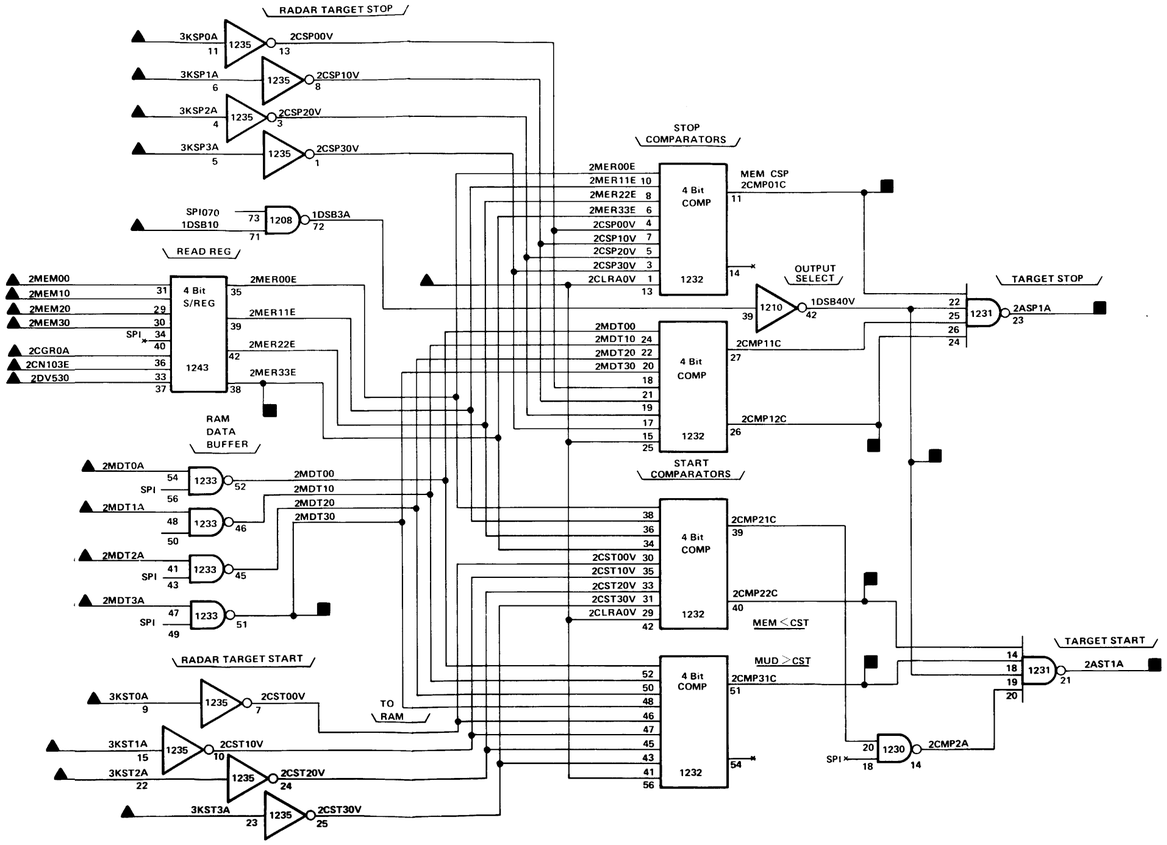
NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1. RIU BAY 1 CARD CAGE (1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1123, A1139, A1149 AND A1227

MS200776A

FO-163. TDU-A Start/Stop Comparators Logic Diagram

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
1DSB10	15300	1DSB40V	16300
2CGR0A	15500	2ASP1A	16300
2CLRA0V	15200	2AST1A	16300
2CN103E	15800	2CMP01C	16500
2DV530	15200	2CMP12C	16500
2MDT0A	15500	2CMP22C	16500
2MDT1A	15500	2CMP31C	16300, 16500
2MDT2A	15500	2MDT30	16201, 26803, 27702
2MDT3A	15500	2MER33E	15500
2MEM00	26803		
2MEM00	27601		
2MEM10	26803		
2MEM10	27601		
2MEM20	26803		
2MEM20	27601		
2MEM30	26803		
2MEM30	27601		
3KSP0A	27002		
3KSP1A	27002		
3KSP2A	27002		
3KSP3A	27002		
3KST0A	27002		
3KST1A	27002		
3KST2A	27002		
3KST3A	27002		

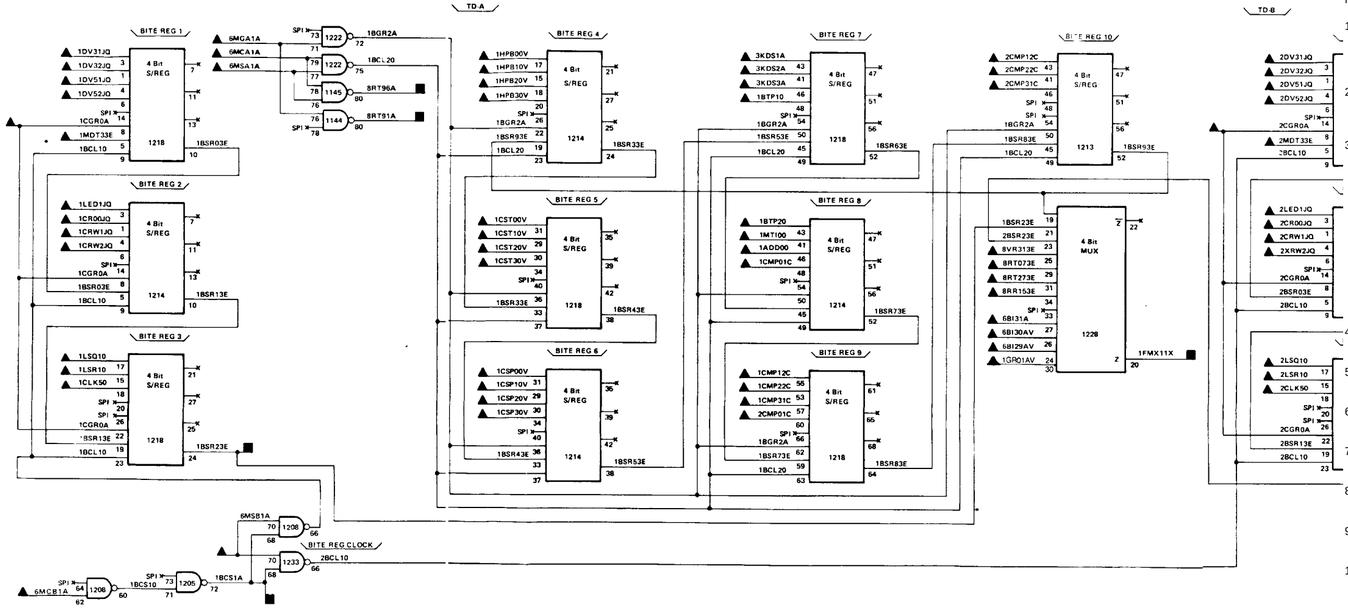


FO-164. TDU B Start/Stop Comparators Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1. RIU BAY 1 CARD CAGE (1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1227 AND A2332.

INPUT SIGNAL	INPUT SOURCE FO-SH	INPUT SIGNAL	INPUT SOURCE FO-SH	OUTPUT SIGNAL	OUTPUT DESTINATION FO-SH
1ADD0C	16300	2CRW2JC	15800	1BI31A	15100, 15200, 15400, 15500
1BTP10	16300	2CR00JC	15200	1BSR23E	15600
1BTP1C	27502	2DV31JC	15500	1FMX11X	05400, 26803, 27201
1BTP20	16300	2DV32JC	15500	2BSR23E	15900
1BTP20	26802	2DV51JC	15200	8RT91A	14000, 14100, 14700, 15002
1BTP2C	27502	2DV52JC	15200	8RT96A	14000, 14100, 14700, 15002
1CGR0A	15400	2LED1JC	15200		
1CLK50	15100	2LSQ10	15500		
1CMP01C	16300	2LSR10	15500		
1CMP12C	16300	2MDT33E	15500		
1CMP22C	16300	3KDS1A	27102		
1CMP31C	16300	3KDS2A	27102		
1CRW1JC	15700	3KDS3A	27102		
1CRW2JC	15700	6BI29AV	05300		
1CR00JC	15100	6BI29AV	26803		
1CSP00V	16300	6BI29AV	27201		
1CSP10V	16300	6BI30AV	05300		
1CSP20V	16300	6BI30AV	26803		
1CSP30V	16300	6BI30AV	27201		
1CST00V	16300	6BI31A	05300		
1CST10V	16300	6BI31A	26802		
1CST20V	16300	6BI31A	26803		
1CST30V	16300	6BI31A	27901		
1DV31JC	15400	6MCA1A	05600		
1DV32JC	15400	6MCA1A	26803		
1DV51JC	15100	6MCA1A	27901		
1DV52JC	15100	6MCB1A	05600		
1HPB00V	15300	6MCB1A	27201		
1HPB00V	27602	6MGA1A	05600		
1HPB10V	15300	6MGA1A	26803		
1HPB10V	27602	6MGA1A	27201		
1HPB20V	15300	6MSA1A	05600		
1HPB30V	15300	6MSA1A	26803		
1LED1JC	15100	6MSA1A	27201		
1LSQ10	15400	6MSB1A	05600		
1LSR10	15400	6MSB1A	26802		
1MDT33E	15400	6MSB1A	26803		
1MT100	15300	6MSB1A	27201		
1MT100	26802	8RT913E	14800		
2CGR0A	15502	8RT073E	14700		
2CLK50	15500	8RT273E	15002		
2CMP01C	16400	8VR313E	13900		
2CMP12C	16400				
2CMP22C	16400				
2CMP31C	16400				
2CRW1JC	15800				



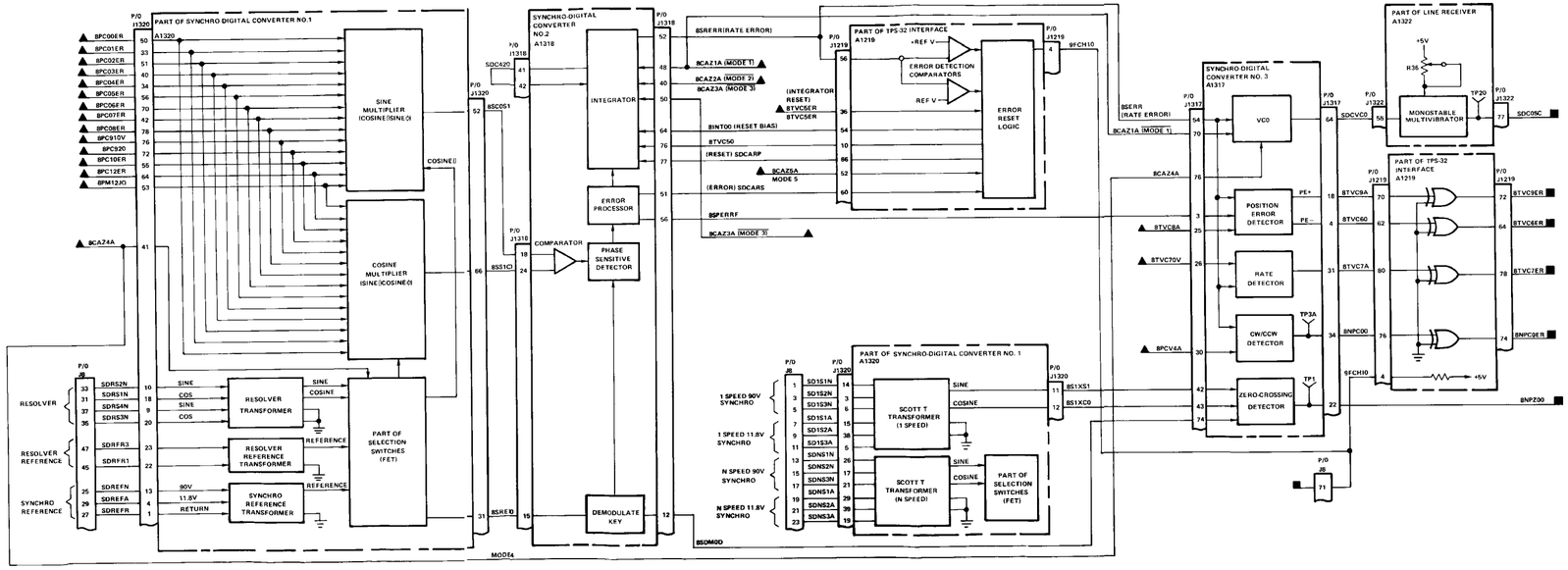
NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1. RIU BAY 1 CARD CAGE (1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1123, A1139, A1149 AND A1227

MS 200778A

Change 3 FO-165. TDU BITE Register Logic Diagram

INPUT		INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
SDNS1N	28001	8PC10ER	16800	SDC05C	26803
SDNS2N	28001	8PC10ER	26803	8NPC0ER	26803
SDNS3N	28001	8PC10ER	28802	8NPZ00	16800, 26803, 28801
SDREFN	28001	8PC12ER	16800	8TVC6ER	26803
SDRFR1	28001	8PC12ER	26803	8TVC7ER	26803
SDRFR3	28001	8PC12ER	28802	8TVC9ER	26803
SDRS1N	28001	8PC910V	16800	9FCH10	33100
SDRS2N	28001	8PC910V	26803		
SDRS3N	28001	8PC910V	28802		
SDRS4N	28001	8PC920	16800		
SD151N	28001	8PC920	26803		
SD152N	28001	8PC920	28802		
SD153N	28001	8PM12JG	16800		
8CAZ1A	26803	8PM12JG	26803		
8CAZ2A	27802	8PM12JG	28802		
8CAZ3A	27802	8TVC5ER	16700		
8CAZ4A	26803	8TVC5ER	26803		
8CAZ5A	16101	8TVC5ER	28802		
8PCV4A	26803	8TVC70V	16700		
8PC00ER	16800	8TVC70V	26803		
8PC00ER	26803	8TVC70V	28802		
8PC00ER	28801	8TVC8A	16700		
8PC01ER	16800	8TVC8A	26803		
8PC01ER	26803	8TVC8A	28802		
8PC01ER	28801				
8PC02ER	16800				
8PC02ER	26803				
8PC02ER	28801				
8PC03ER	16800				
8PC03ER	26803				
8PC03ER	28801				
8PC04ER	16800				
8PC04ER	26803				
8PC04ER	28801				
8PC05ER	16800				
8PC05ER	26803				
8PC05ER	28801				
8PC06ER	16800				
8PC06ER	26803				
8PC06ER	28801				
8PC07ER	16800				
8PC07ER	26803				
8PC07ER	28801				
8PC08ER	16800				
8PC08ER	26803				
8PC08ER	28802				

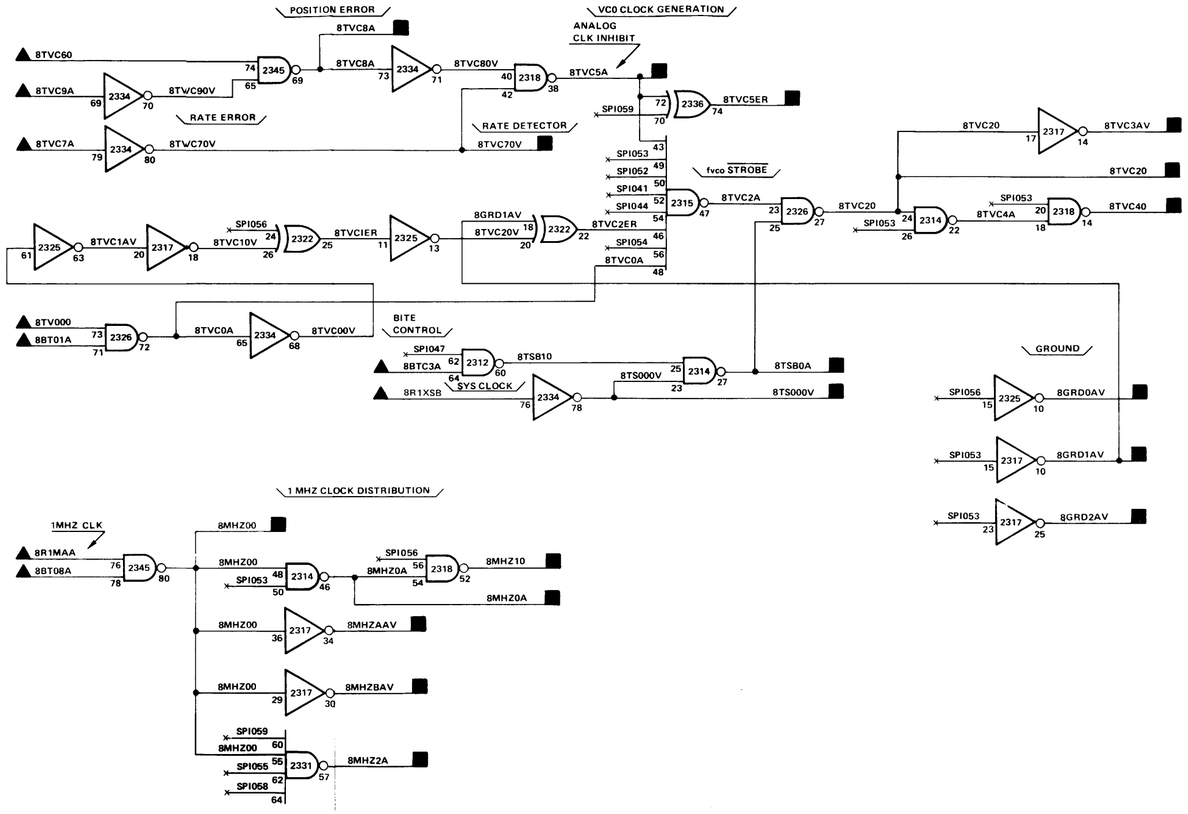


FO-166. SDC Interface Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1. RIU BAY 1 CARD CAGE (1A1A1A4.)
3. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX
4. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
5. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
6. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
7. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.

INPUT		OUTPUT	
	SOURCE		DESTINATION
SIGNAL	FO-SH	SIGNAL	FO-SH
8BTC3A	26803	8GRD0AV	16800, 17101, 17200, 17400, 17700
8BTC3A	28401		
8BT01A	26803	8GRD1AV	17000, 17101, 17200
8BT01A	28401	8GRD2AV	17300, 17400, 17600, 17700, 17800, 17900
8BT08A	26803		
8BT08A	28401	8MHZAAV	17000, 17900
8R1MAA	14300	8MHZBAV	17000
8R1MAA	26803	8MHZ0A	17400
8R1XSB	00300	8MHZ00	17300, 18000
8R1XSB	14900	8MHZ10	16900, 17000, 17300, 17700, 17800, 17900
8R1XSB	26802		
8R1XSB	26803	8MHZ2A	17000
8R1XSB	27501	8TSB0A	18000
8TVC60	26803	8TS000V	16900
8TVC60	28702	8TVC20	17200
8TVC7A	26803	8TVC3AV	17200
8TVC7A	28702	8TVC40	16800
8TVC9A	26803	8TVC5A	18000
8TVC9A	28702	8TVC5ER	16600, 26803, 28702
8TV000	26803	8TVC70V	16600, 26803, 28702
		8TVC8A	16600, 26803, 28702



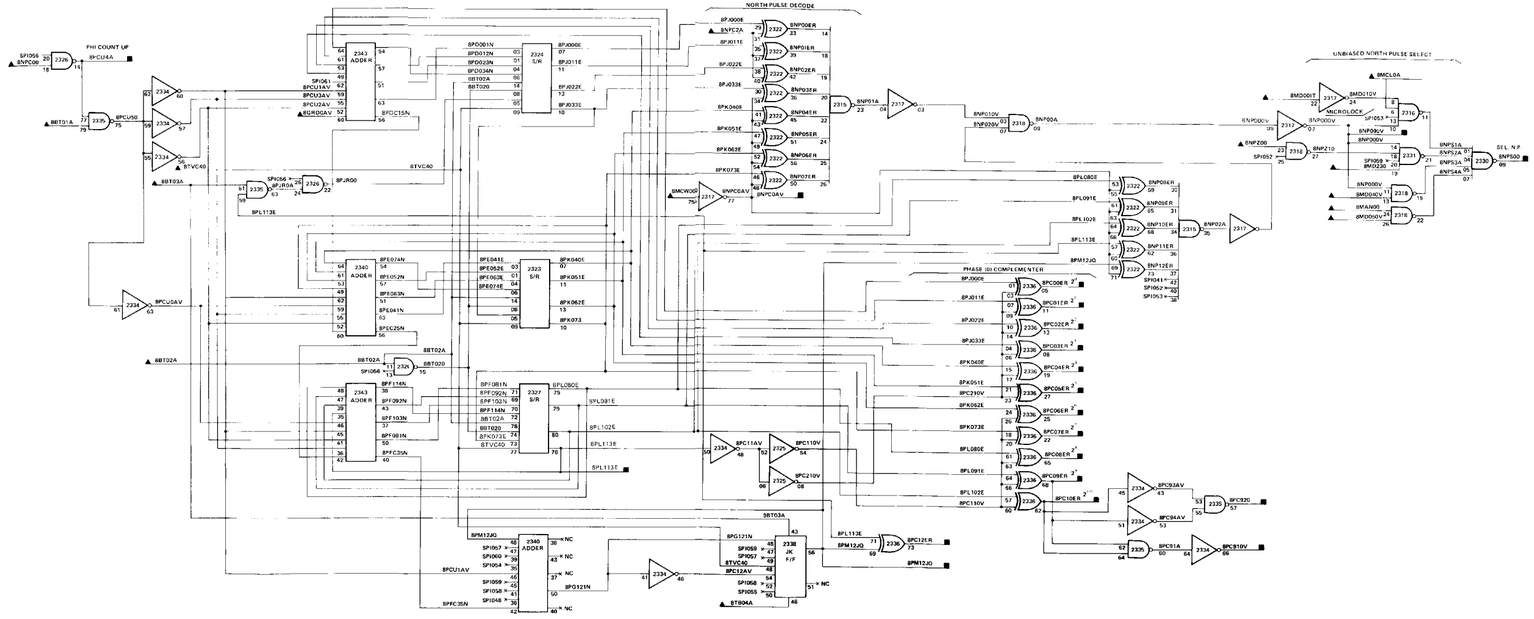
FO-167. SDC Clock Generator Circuit Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1. RIU BAY 1 CARD CAGE (1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1227AND A2332.

INPUT	
SIGNAL	SOURCE FO-SH
8BT01A	26803
8BT01A	28401
8BT02A	26803
8BT02A	28401
8BT03A	26803
8BT03A	28401
8BT04A	26803
8BT04A	28401
8GRD0AV	16700
8MAN00	16900
8MCL0A	16900
8MCL0A	28701
8MCW00	16900
8MD021T	17200
8MD040V	17200
8MD050V	17200
8MD230	17200
8NPC00	26803
8NPC00	28701
8NPC2A	17200
8NPZ00	16600
8NPZ00	26803
8NPZ00	28701
8TVC40	16700

OUTPUT	
SIGNAL	DESTINATION FO-SH
8NPC0AV	17300
8NPS00	17101, 17200
8NP000V	18000
8PCU4A	26803
8PC00ER	16600, 26803, 28701
8PC01ER	16600, 26803, 28701
8PC02ER	16600, 26803, 28701
8PC03ER	16600, 26803, 28701
8PC04ER	16600, 26803, 28701
8PC05ER	16600, 26803, 28701
8PC06ER	16600, 26803, 28701
8PC07ER	16600, 26803, 28702
8PC08ER	16600, 26803, 28702
8PC09ER	26803, 28702
8PC10ER	16600, 26803, 28702
8PC12ER	16600, 26803, 28702
8PC910V	16600, 26803, 28702
8PC920	16600, 26803, 28702
8PL113E	18000
8PM12JQ	16600, 26803, 28702



FO-168. SDC Phase Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1. RIU BAY 1 CARD CAGE (1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A2217 AND A2332.

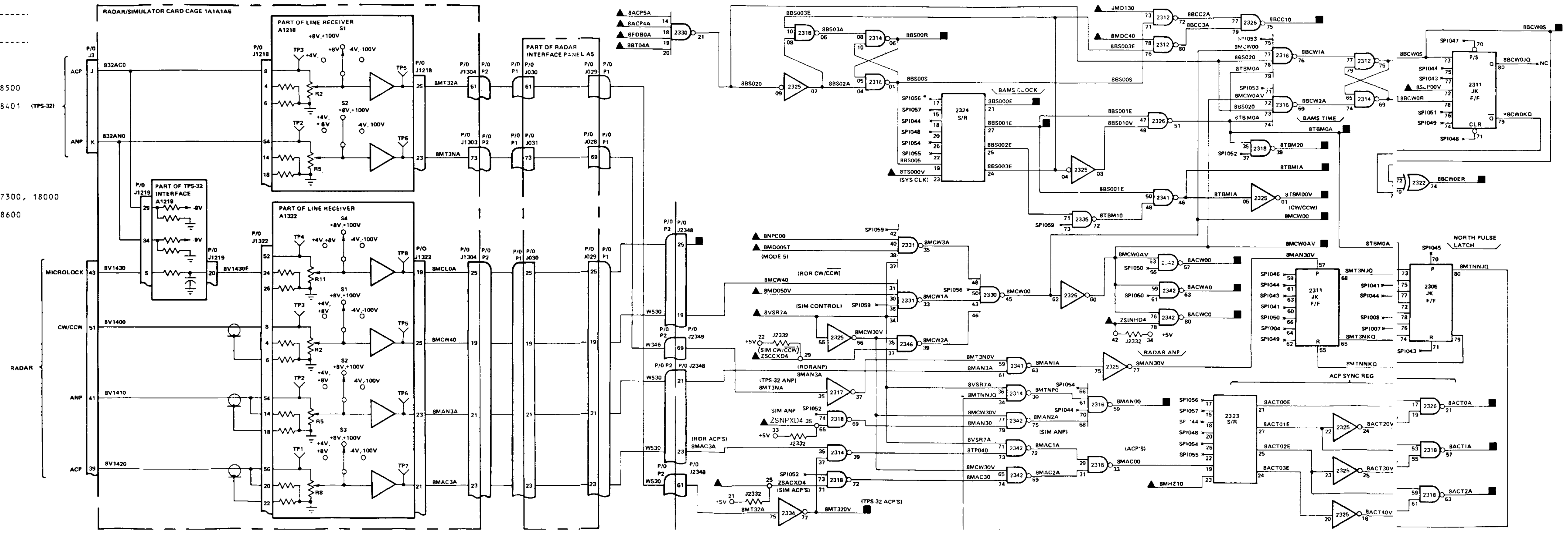
MS200781

INPUT

SIGNAL	SOURCE FO-SH
ZSACXD4	20702
ZSACXD4	26803
ZSACXD4	27802
ZSCCXD4	20702
ZSCCXD4	26803
ZSCCXD4	27802
ZSINHD4	26803
ZSINHD4	27002
ZSINHD4	27803
ZSNPXD4	20702
ZSNPXD4	26803
ZSNPXD4	27802
8ACP4A	17200
8ACP5A	17200
8BT04A	26803
8BT04A	28401
8FDB0A	17000
8MDC40	17200
8MD005T	17200
8MD050V	17200
8MD130	17200
8MHZ10	16700
8NPC00	26803
8NPC00	28701
8SLP00V	17900
8TS000V	16700
8VI400	28001
8VI410	28001
8VI420	28001
8VI430	28001
8VSR7A	14000
8VSR7A	26803

OUTPUT

SIGNAL	DESTINATION FO-SH
8ACT0A	17000
8ACT1A	17000, 17200
8ACT2A	17000
8ACWA0	11301, 26803, 28500
8ACWC0	26803
8ACW00	03300, 26803, 28401
8BCC10	17101
8BCW0ER	17101
8BCW0S	17101
8BS00R	17400
8BS000E	17400
8BS001E	17400
8MAN00	16800
8MCL0A	16800, 28801
8MCW0AV	17600
8MCW00	16800, 17200, 17300, 18000
8MT320V	17000
8TBM0A	17900, 18300, 18600
8TBM00V	17101, 17600
8TBM1A	17101
8TBM20	17200



Change 3 FO-169. SDC NAP/ACP Select Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.

2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1. RIU BAY 1 CARD CAGE (1A1A1A4.)

3. REFERENCES ARE AS FOLLOWS:

   ▲ INDICATES INPUT FROM ANOTHER FIGURE

   △ INDICATES INPUT FROM THE SAME FIGURE

   ■ INDICATES OUTPUT TO ANOTHER FIGURE

   □ INDICATES OUTPUT TO THE SAME FIGURE

   ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .

5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.

6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .

7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.

8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.

9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.

10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER

    B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.

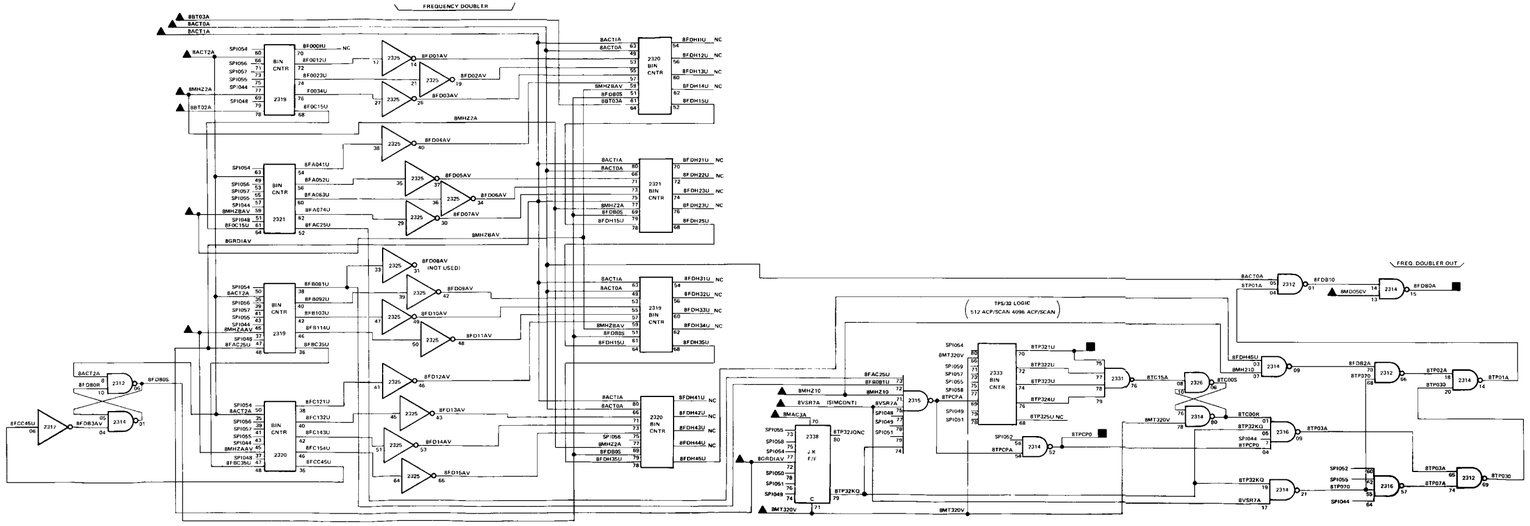
    C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A2217 AND A2332.

MS200783A

INPUT	
SIGNAL	SOURCE FO-SH
8ACT0A	16900
8ACT1A	16900
8ACT2A	16900
8BT02A	26803
8BT02A	28401
8BT03A	26803
8BT03A	28401
8GRD1AV	16700
8MAC3A	28701
8MD050V	17200
8MHZ14V	16700
8MHZ8AV	16700
8MHZ10	16700
8MHZ2A	16700
8MT320V	16900
8VSR7A	14000
8VSR7A	26803

OUTPUT	
SIGNAL	DESTINATION FO-SH
8FDB0A	16900
8TPCP0	17200
8TP321U	17200



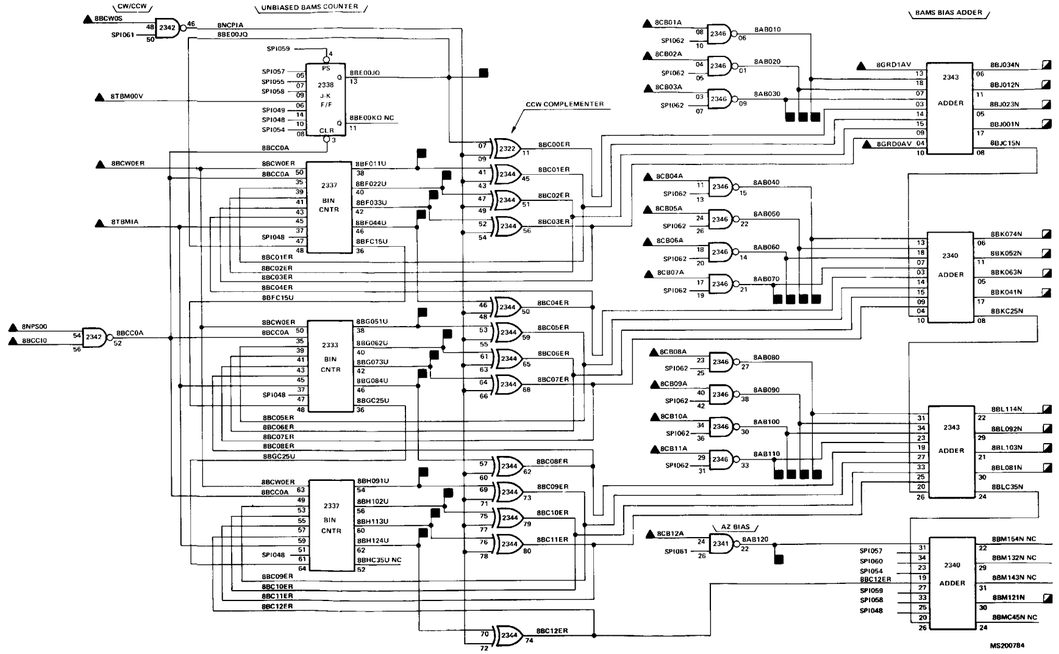
NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1. RIU BAY 1 CARD CAGE (1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A2217 AND A2332.

MS200783

FO-170. SDC Frequency Doubler Logic Diagram

INPUT SIGNAL	SOURCE FO-SH	OUTPUT SIGNAL	DESTINATION FO-SH
8BCC10	16900	8AB010	18000
8BCW0ER	16900	8AB020	18000
8BCW0S	16900	8AB030	18000
8CB01A	26803	8AB040	18000
8CB02A	26803	8AB050	18000
8CB03A	26803	8AB060	18000
8CB04A	26803	8AB070	18000
8CB05A	26803	8AB080	18000
8CB06A	26803	8AB090	18000
8CB07A	26803	8AB100	18000
8CB07A	27802	8AB110	18000
8CB08A	26803	8AB120	18000
8CB08A	27802	8BE00JQ	18000
8CB09A	26803	8BF011U	18000
8CB09A	27802	8BF022U	18000
8CB10A	26803	8BF033U	18000
8CB10A	27802	8BF044U	18000
8CB11A	26803	8BG051U	18000
8CB11A	27802	8BG062U	18000
8CB12A	26803	8BG073U	18000
8CB12A	27802	8BG084U	18000
8GRD0AV	16700	8BH091U	18000
8GRD1AV	16700	8BH102U	18000
8NPS00	16800	8BH113U	18000
8TBM00V	16900	8BH124U	18000
8TBM1A	16900	8BJ001N	17200, 17300, 17400, 18402
		8BJ012N	17200, 17400
		8BJ023N	17200, 17300, 17400
		8BJ034N	17200, 17300, 17400
		8BK041N	17200, 17300, 17400
		8BK052N	17200, 17300, 17400
		8BK063N	17200, 17300, 17400
		8BK074N	17200, 17300, 17400
		8BL081N	17200, 17300, 17400
		8BL092N	17200, 17300, 17400
		8BL103N	17200, 17400, 17600
		8BL114N	17200, 17400, 17600, 17900
		8BM121N	17200, 17400, 17600, 17900



FO-171. SDC BAMS Counter and Adder Logic Diagram (Sheet 1 of 2)

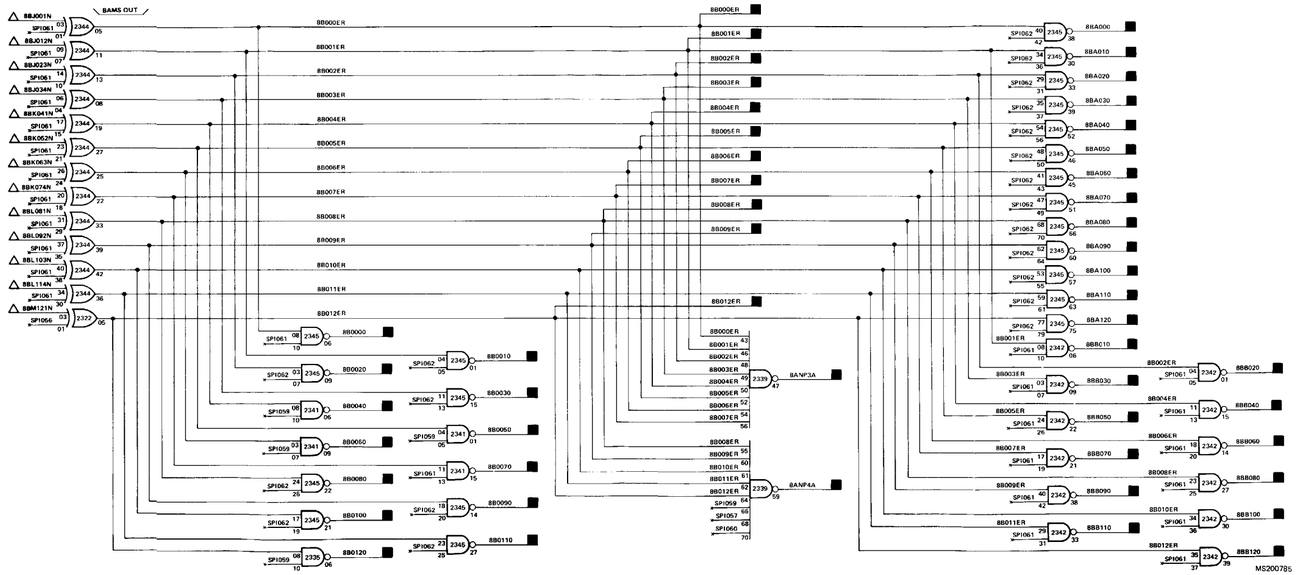
NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1.
   RIU BAY 1 CARD CAGE (1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A2217 AND A2332.

MS200784

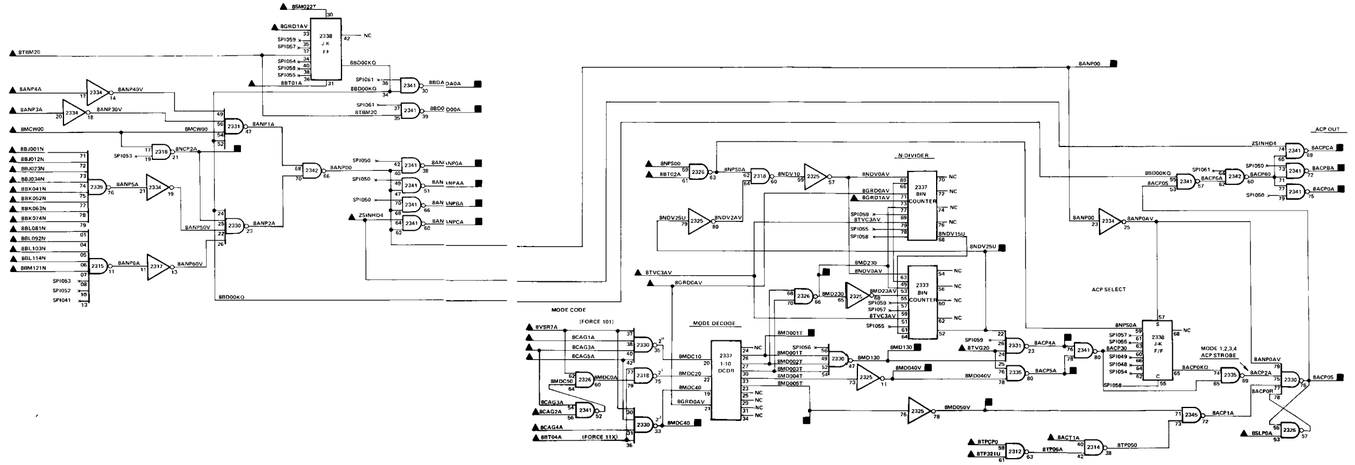
OUTPUT SIGNAL	DESTINATION FO-SH
8ANP3A	17200
8ANP4A	17200
8BA000	07900, 26803, 28500
8BA010	07900, 26803, 28500
8BA020	07900, 26803, 28500
8BA030	07900, 26803, 28500
8BA040	07900, 26803, 28500
8BA050	07900, 26803, 28500
8BA060	07900, 26803, 28500
8BA070	07900, 26803, 28500
8BA080	07900, 26803, 28500
8BA090	07900, 26803, 28500
8BA100	07900, 26803, 28500
8BA110	07900, 26803, 28500
8BA120	07900, 26803, 28500
8BB010	18401, 18600
8BB020	18401, 18600
8BB030	18401, 18600
8BB040	18401, 18600
8BB050	18402, 18600
8BB060	18402, 18600
8BB070	18402, 18600
8BB080	18402, 18600
8BB090	18402, 18600
8BB100	18402, 18600
8BB110	18402, 18600
8BB120	18402, 18600
8B000ER	18600
8B0000	02400, 26803, 28401
8B001ER	17300
8B0010	02400, 03300, 26802, 26803, 28402
8B002ER	17300
8B0020	02400, 03300, 26802, 26803, 28402
8B003ER	17300
8B0030	02400, 03300, 26802, 26803, 28402
8B004ER	17300
8B0040	02400, 03300, 26802, 26803, 28402
8B005ER	17300
8B0050	02400, 03300, 26802, 26803, 28402
8B006ER	17300
8B0060	02400, 03300, 26802, 26803, 28402

OUTPUT SIGNAL	DESTINATION FO-SH
	28402
8B007ER	17300
8B0070	02400, 03300, 26802, 26803, 28402
8B008ER	17300
8B0080	02400, 03300, 26802, 26803, 28402
8B009ER	17300
8B0090	02400, 03300, 26802, 26803, 28402
8B0100	02400, 03300, 26802, 26803, 28402
8B0110	02400, 03300, 26802, 26803, 28402
8B012ER	17900
8B0120	02400, 03300, 26802, 26803, 28402



FO-171. SDC BAMS Counter and Adder Logic Diagram (Sheet 2 of 2).

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
ZSINHD4	26803	BACPDA	13500, 26803
ZSINHD4	27002	BACPCA	26803, 27802
ZSINHD4	27803	BACPDA	01501, 03300, 05900, 26802, 26803, 28401
BACT1A	16900		
BANP3A	17102	BACPDS	18000
BANP4A	17102	BACP4A	16900
BBJ001N	17101	BACP5A	16900
BBJ012N	17101	BANPAA	11900, 26803, 28500
BBJ023N	17101	BANPBA	13102, 13400, 26803
BBJ034N	17101	BANPCA	26803, 27802, 31802
BBK041N	17101	BANPDA	01501, 05900, 26802, 26803, 28401
BBK052N	17101		
BBK063N	17101	BANPDD	17700, 18000
BBK074N	17101	BBDA0A	07600, 26803, 28500
BBL081N	17101	BBB00A	02400, 17900, 26803, 28401
BBL092N	17101	BMDC40	16900
BBL103N	17101	BMD001T	16800
BBL114N	17101	BMD005T	16900
BBM121N	17101	BMD040V	16800
BBTQ1A	26803	BMD050V	16800, 16900, 17000, 18300
BBTQ1A	28401	BMD130	16900
BBTQ2A	26803	BMD230	16800
BBTQ2A	28401	BNDV25U	18000
BBT04A	26803	BNPC2A	16800
BBT04A	28401		
BCAG1A	26803		
BCAG2A	26803		
BCAG3A	26803		
BCAG4A	26803		
BCAG5A	26803		
BGRD0AV	16700		
BGRD1AV	16700		
BMCW00	16900		
BNP500	16800		
BSLP0A	17900		
BSM022T	17900		
BTBM20	16900		
BTPCP0	17000		
BTP321U	17000		
BTVC20	16700		
BTVC34V	16700		
BVSR7A	14000		
BVSR7A	26803		



FO-172. SDC ANP/ACP Logic Diagram

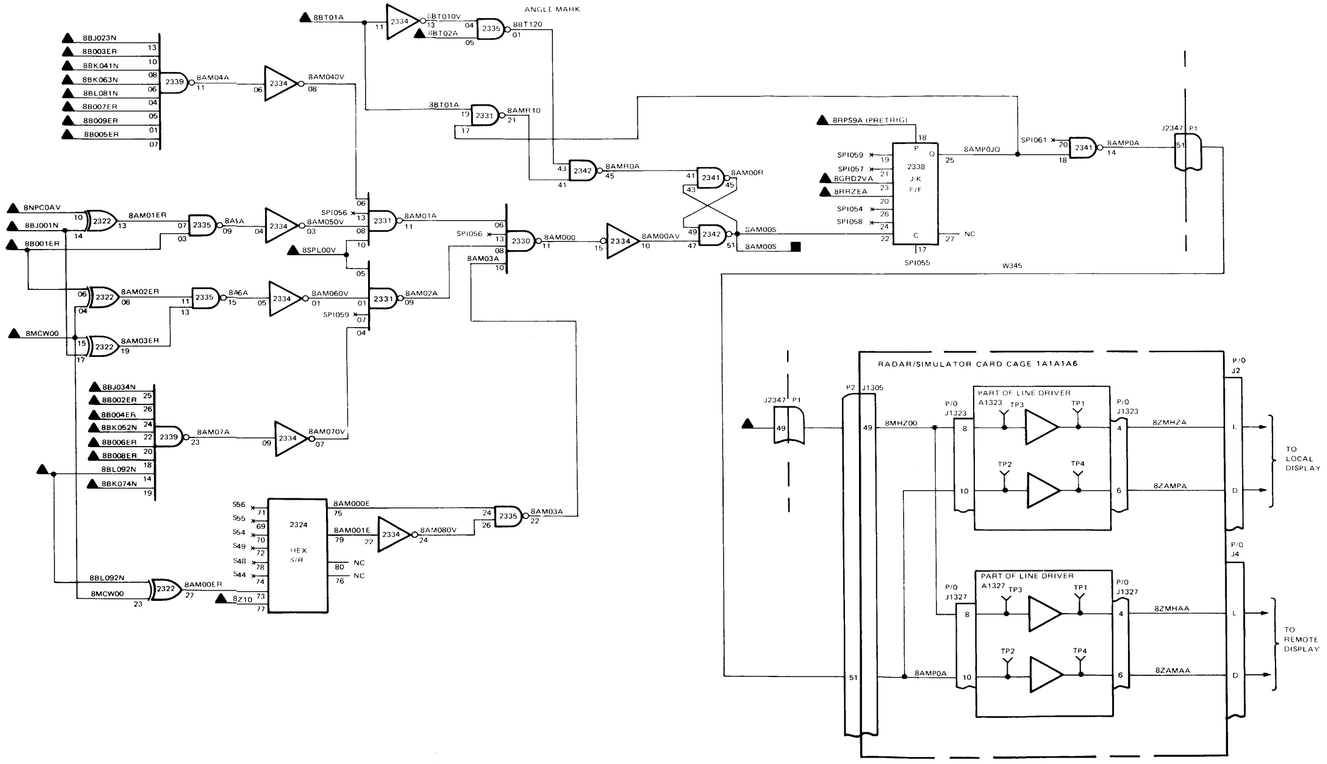
NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1. RIU BAY 1 CARD CAGE (1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A2217 AND A2332.

MS200786

INPUT	
SIGNAL	SOURCE FO-SH
8BJ001N	17101
8BJ023N	17101
8BJ034N	17101
8BK041N	17101
8BK052N	17101
8BK063N	17101
8BK074N	17101
8BL081N	17101
8BL092N	17101
8BT01A	26803
8BT01A	28401
8BT02A	26803
8BT02A	28401
8BD01ER	17102
8BD02ER	17102
8BD03ER	17102
8BD04ER	17102
8BD05ER	17102
8BD06ER	17102
8BD07ER	17102
8BD08ER	17102
8BD09ER	17102
8GRD2AV	16700
8MCW00	16900
8MHZ00	16700
8MHZ10	16700
8NPC0AV	16800
8RPS9A	14400
8RPS9A	26803
8RPS9A	28202
8RRZEA	14400
8RRZEA	26803
8SPL00V	17900

OUTPUT	
SIGNAL	DESTINATION FO-SH
8AM00S	18000



FO-173. SDC Angle Mark Generator Logic Diagram

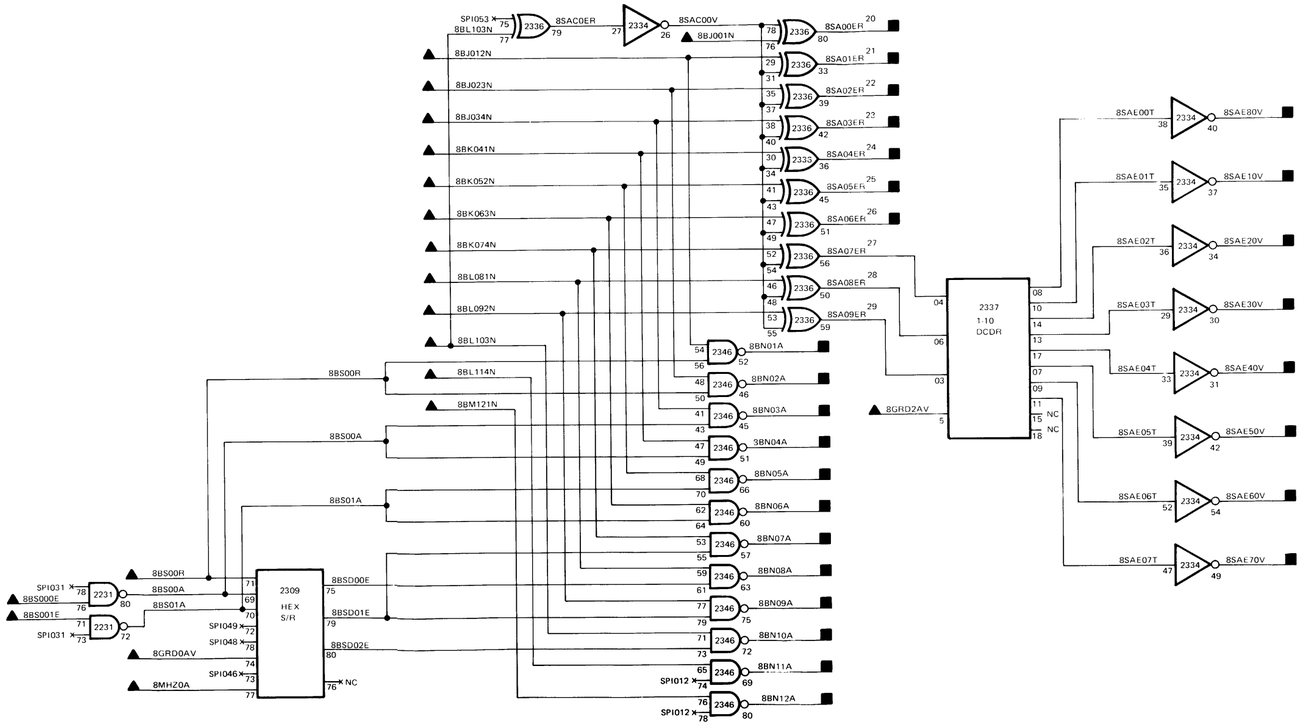
NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1. RIU BAY 1 CARD CAGE (1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1123, A1139, A1149 AND A1227

MS200787

INPUT SIGNAL	INPUT SOURCE FO-SH
8BJ001N	17101
8BJ012N	17101
8BJ023N	17101
8BJ034N	17101
8BK041N	17101
8BK052N	17101
8BK063N	17101
8BK074N	17101
8BL081N	17101
8BL092N	17101
8BL103N	17101
8BL114N	17101
8BM121N	17101
8BS00R	16900
8BS000E	16900
8BS001E	16900
8GRD0AV	16700
8GRD2AV	16700
8MHZ0A	16700

OUTPUT SIGNAL	OUTPUT DESTINATION FO-SH
8BN01A	26803, 27801
8BN02A	26803, 27801
8BN03A	26803, 27801
8BN04A	26803, 27801
8BN05A	26803, 27802
8BN06A	26803, 27802
8BN07A	26803, 27802
8BN08A	26803, 27802
8BN09A	26803, 27802
8BN10A	26803, 27802
8BN11A	26803, 27802
8BN12A	26803, 27802
8SAE10V	17500, 18000, 26803
8SAE20V	17500, 18000, 26803
8SAE30V	17500, 18000, 26803
8SAE40V	17500, 18000, 26803
8SAE50V	17500, 18000, 26803
8SAE60V	17500, 18000, 26803
8SAE70V	17500, 18000, 26803
8SAE80V	17500, 18000, 26803
8SA00ER	17600
8SA01ER	17500, 18000, 26803
8SA02ER	17500, 18000, 26803
8SA03ER	17500, 18000, 26803
8SA04ER	17500, 18000, 26803
8SA05ER	17500, 18000, 26803
8SA06ER	17500, 18000, 26803



FO-174. SDC Memory Address and Enable Logic Diagram

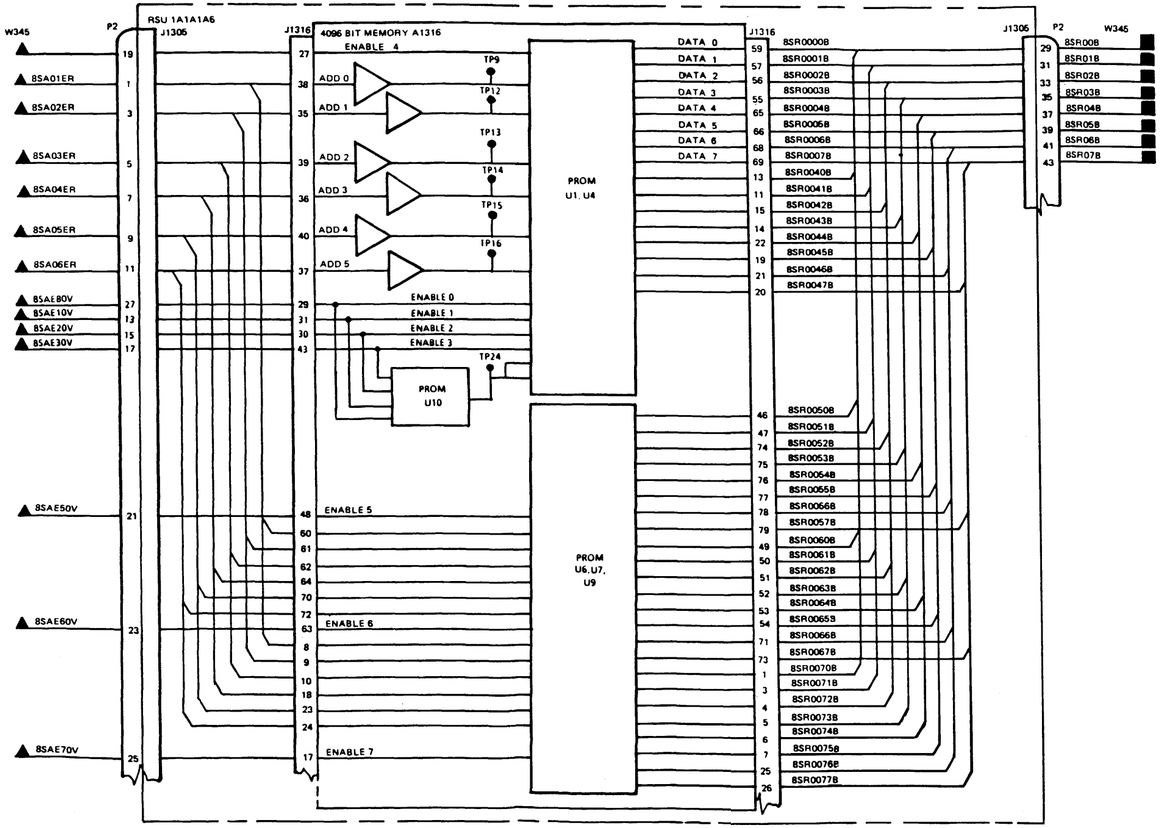
NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1.
   RIU BAY 1 CARD CAGE (1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1123, A1139, A1149 AND A1227

MS200788

INPUT	
SIGNAL	SOURCE FO-SH
8SAE10V	17400
8SAE20V	17400
8SAE30V	17400
8SAE40V	17400
8SAE50V	17400
8SAE60V	17400
8SAE70V	17400
8SAE80V	17400
8SA01ER	17400
8SA02ER	17400
8SA03ER	17400
8SA04ER	17400
8SA05ER	17400
8SA06ER	17400

OUTPUT	
SIGNAL	DESTINATION FO-SH
8SR00B	17600, 18000, 26803
8SR01B	17600, 18000, 26803
8SR02B	17600, 18000, 26803
8SR03B	17600, 18000, 26803
8SR04B	17600, 18000, 26803
8SR05B	17600, 18000, 26803
8SR06B	17600, 18000, 26803
8SR07B	17600, 18000, 26803



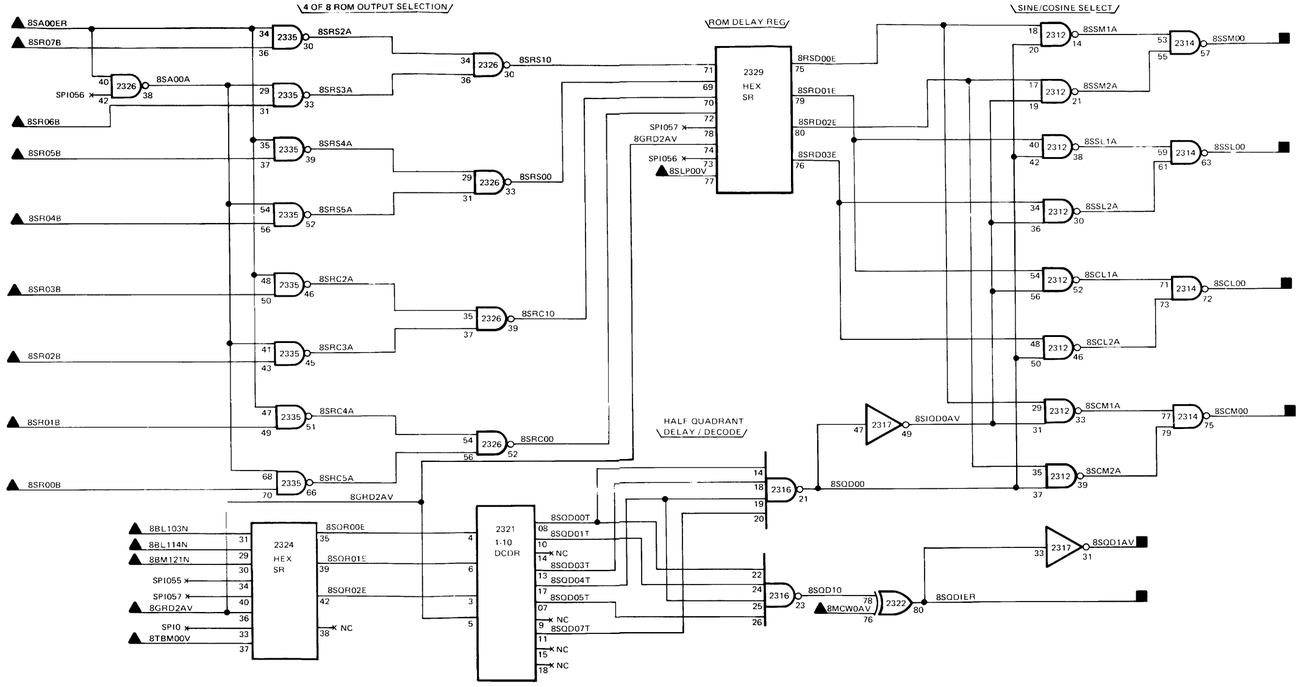
NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1. RSU (1A1A1A6.)
3. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
4. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
5. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
6. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
7. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.

MS200789A

Change 3 FO-175. SDC BAMS Memory Logic Diagram

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
8BL103N	17101	8SCL00	17800
8BL114N	17101	8SCM00	17800
8BM121N	17101	8SQD1AV	17700
8GRD2AV	16700	8SQD1ER	17800
8MCW0AV	16900	8SSL00	17700
8SA00ER	17400	8SSM00	17700
8SLP00V	17900		
8SR00B	17500		
8SR01B	17500		
8SR02B	17500		
8SR03B	17500		
8SR04B	17500		
8SR05B	17500		
8SR06B	17500		
8SR07B	17500		
8TBM00V	16900		



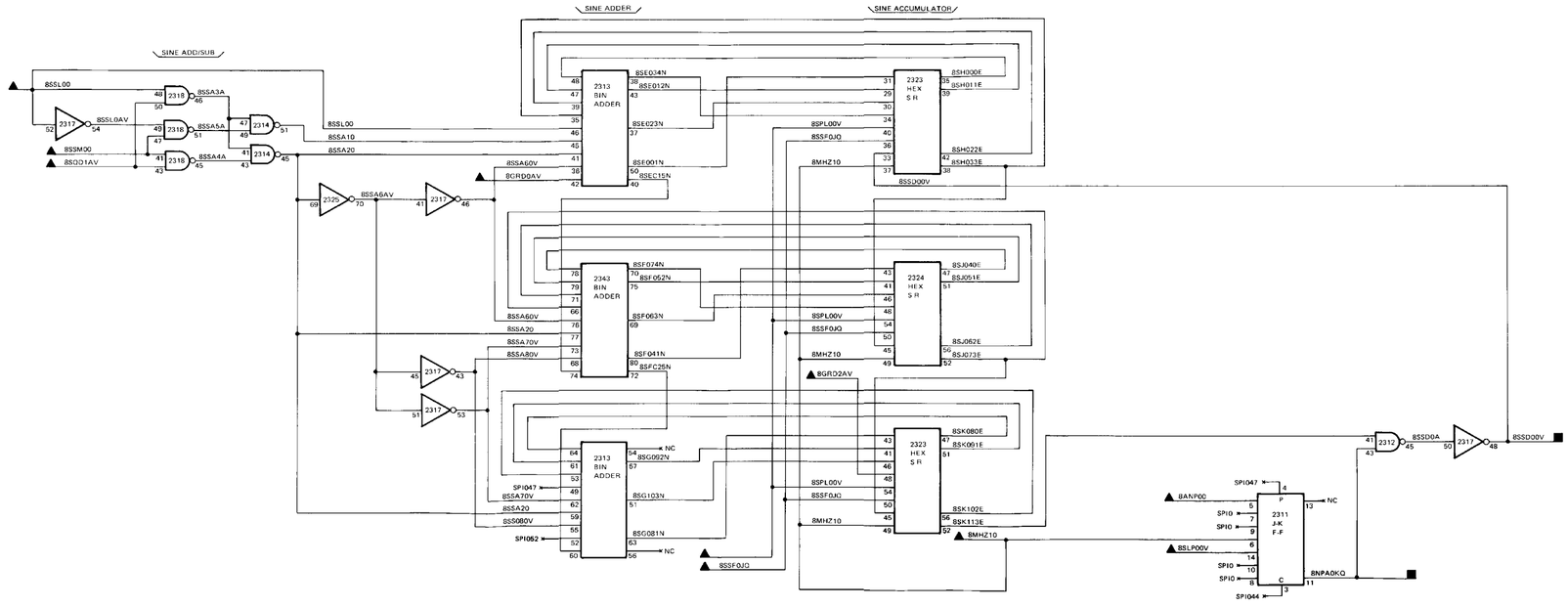
FO-176. SDC Sine/Cosine Select Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1.
   RIU BAY 1 CARD CAGE (1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1123, A1139, A1149 AND A1227

MS200790

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
8ANP00	17200	8NPA0KQ	17800
8GRD0AV	16700	8SSD00V	17900
8GRD2AV	16700		
8MHZ10	16700		
8SLP00V	17900		
8SPL00V	17900		
8SQD1AV	17600		
8SSF0JQ	17900		
8SSL00	17600		
8SSM00	17600		



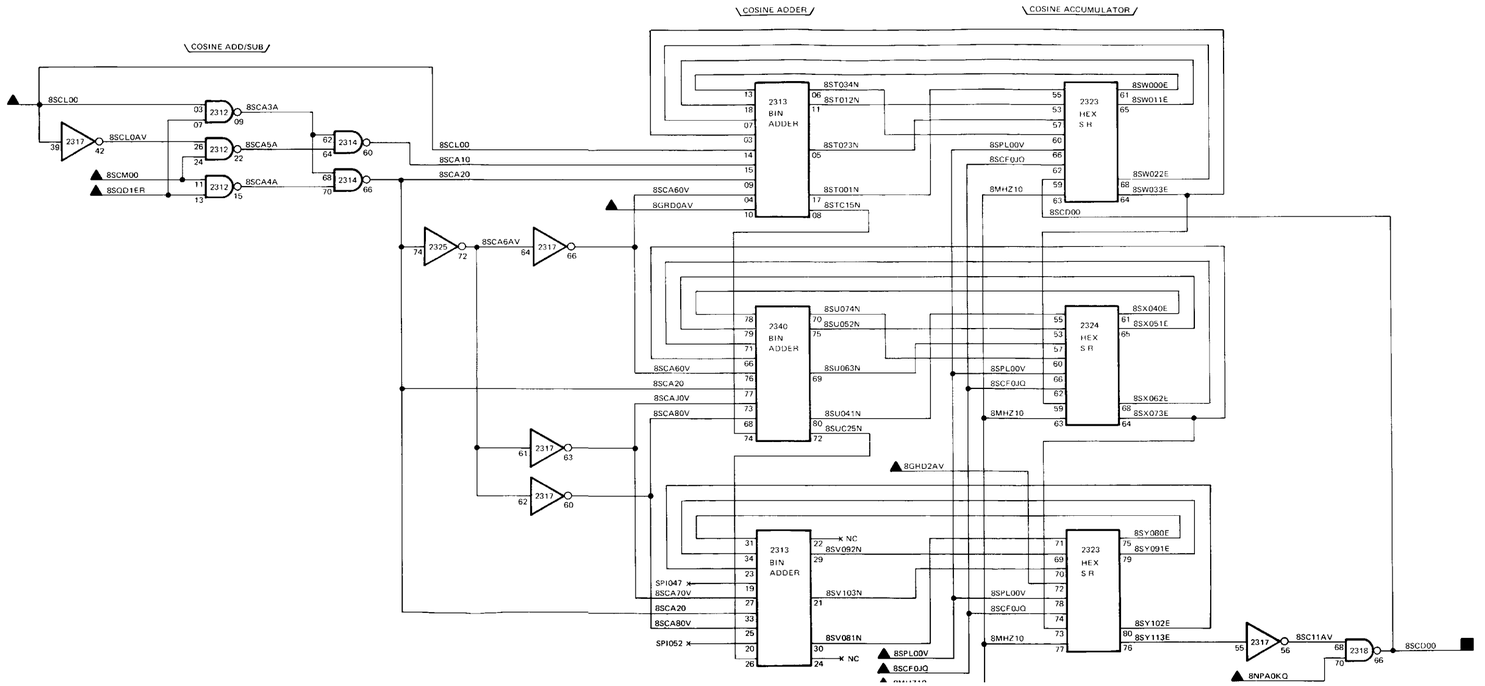
FO-177. SDC Sine Add/Subtract Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1. RIU BAY 1 CARD CAGE (1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1123, A1139, A1149 AND A1227

MS200791

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
8GRD2AV	16700	8SCD00	17900
8MHZ10	16700		
8NPA0KQ	17700		
8SCF0JQ	17900		
8SCL00	17600		
8SCM00	17600		
8SPL00V	17900		
8SQD1ER	17600		



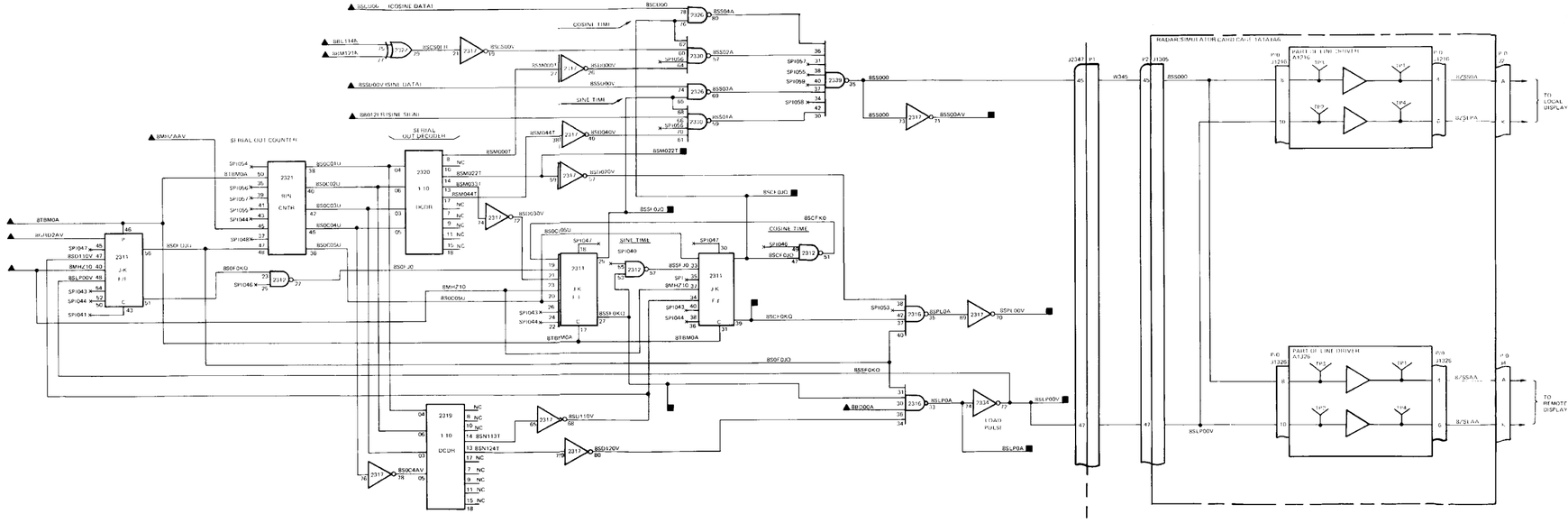
FO-178. SDC Cosine Add/Subtract Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1.
   RIU BAY 1 CARD CAGE (1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1123, A1139, A1149 AND A1227

MS200792

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
8BD00A	17200	8SCFOJQ	17800
8BD00A	26802	8SCFOKQ	18000
8BD00A	28401	8SLP0A	17200
8BL114N	17101	8SLP00V	16900, 17600, 17700
8BM121N	17101	8SM022T	17200
8BQ12ER	17102	8SPL00V	17300, 17700, 17800
8GRD2AV	16700	8SSFOJQ	17700
8MHZAAV	16700	8SSFOKQ	18000
8MHZ10	16700	8SS00AV	18000
8SC000	17800		
8SSD00V	17700		
8TBM0A	16900		



FO-179. SDC Sine/Cosine Serial Output Logic Diagram

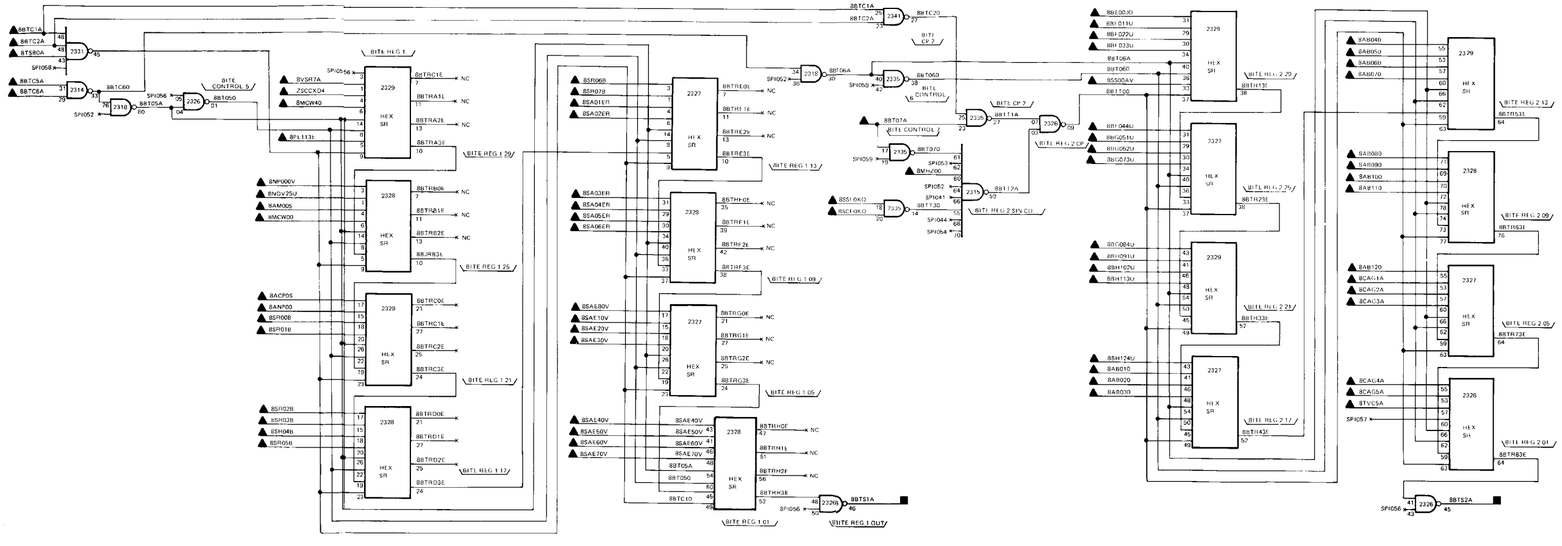
NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1. RIU BAY 1 CARD CAGE (1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1123, A1139, A1149 AND A1227

INPUT SIGNAL	INPUT SOURCE FO-SH
ZSCCXD4	20702
ZSCCXD4	26803
ZSCCXD4	27802
8ABD10	17101
8ABD20	17101
8ABD30	17101
8ABD40	17101
8ABD50	17101
8ABD60	17101
8ABD70	17101
8ABD80	17101
8ABD90	17101
8AB100	17101
8AB110	17101
8AB120	17101
8ACP05	17200
8AM005	17300
8ANP00	17200
8BE00JG	17101
8BF011U	17101
8BF022U	17101
8BF033U	17101
8BF044U	17101
8BG051U	17101
8BG062U	17101
8BG073U	17101
8BG084U	17101
8BH091U	17101
8BH102U	17101
8BH113U	17101
8BH124U	17101
8BTC1A	26803
8BTC1A	28401
8BTC2A	26803
8BTC2A	28401
8BTC5A	26803
8BTC6A	26803
8BT07A	26803
8BT07A	28401
8CAG1A	26803
8CAG2A	26803
8CAG3A	26803
8CAG4A	26803
8CAG5A	26803
8MCW00	16900
8MCW40	28701

INPUT SIGNAL	INPUT SOURCE FO-SH
8MH200	16700
8ND925U	17200
8NP000V	16800
8PL113E	16800
8SAE10V	17400
8SAE20V	17400
8SAE30V	17400
8SAE40V	17400
8SAE50V	17400
8SAE60V	17400
8SAE70V	17400
8SAE80V	17400
8SA01ER	17400
8SA02ER	17400
8SA03ER	17400
8SA04ER	17400
8SA05ER	17400
8SA06ER	17400
8SCF0KQ	17900
8SR00B	17500
8SR01B	17500
8SR02B	17500
8SR03B	17500
8SR04B	17500
8SR05B	17500
8SR06B	17500
8SR07B	17500
8SSF0KQ	17900
8SS00AV	17900
8TS80A	16700
8TVC5A	16700
8VSR7A	14000
8VSR7A	26803

OUTPUT SIGNAL	DESTINATION FO-SH
8BTS1A	05400, 26803, 28401
8BTS2A	05400, 26803, 28401



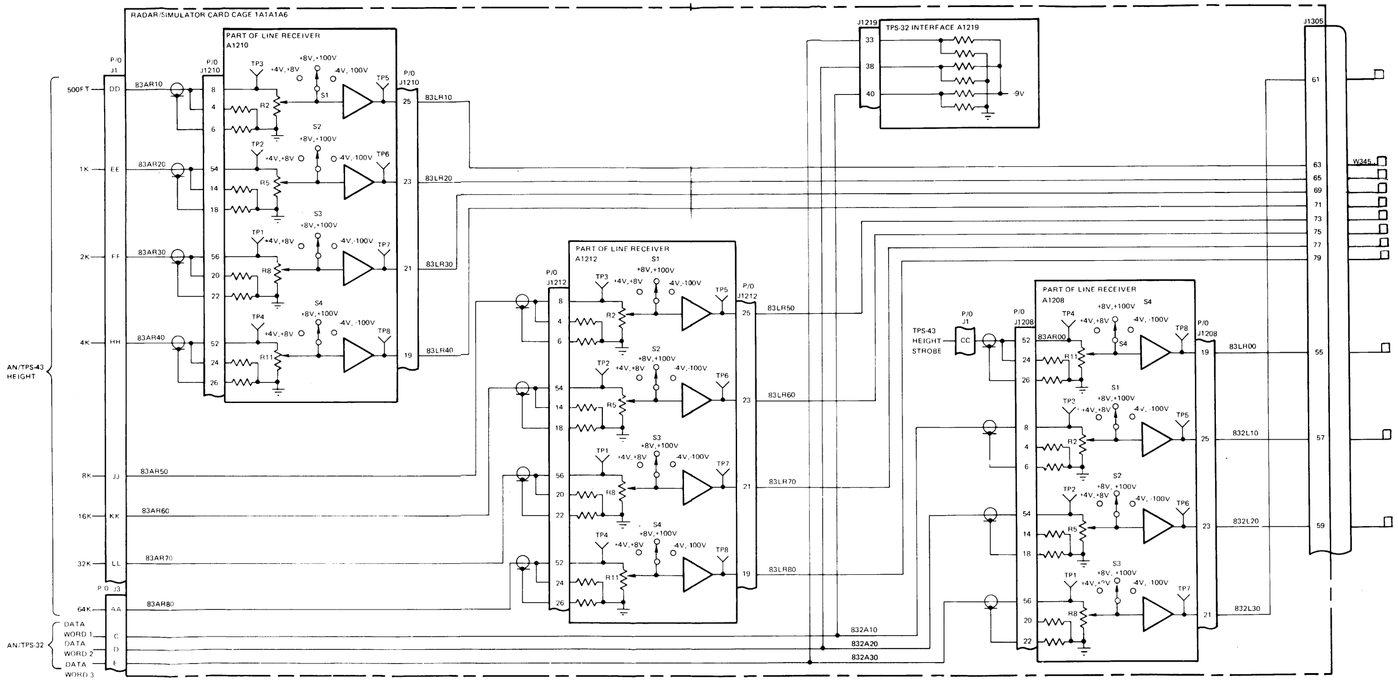
FO-180. SDC BITE Register Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1.
   RIU BAY 1 CARD CAGE (1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1123, A1139, A1149 AND A1227

MS200794

INPUT		INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
S2827D4	26803	8VSR50	26901	83A99JQ	26803
S3821D4	26803	83A263E	18402	83PC20	18401, 18402
S3821D4	27902	83A263E	28401	83PC30	18401, 18402
S3822D4	26803	83C263E	18403	83PC50	18403, 18404
S3822D4	27902	83C263E	28401	83RC4B	18401, 18402, 18403, 18404
S3823D4	26803	83D25A	18200	83RC50	18401, 18402
S3823D4	27902	83D263E	18404	83RS10	18402
S4819D4	21408	83D263E	28401	83RS30	18403
S4819D4	27902	831N2JQ	18300	83RS40	18403
S4820D4	21408	831N3A	18300	833330	18200, 18300
S4820D4	26803	83RC6KQ	18300	8343CA	18300
S4820D4	27902			83431R	18300
S4821D4	21408			83431S	18300, 18403
S4821D4	26803			83431A	18404
S4821D4	27902			83432A	18404
S4822D4	21408			83433A	18404
S4822D4	26803			83434A	18403
S4822D4	27902			83435A	18403
S4823D4	21408			83436A	18403
S4823D4	26803			83437A	18403
S4823D4	27902			83438A	18403
S4824D4	21408				
S4824D4	26803				
S4824D4	27902				
S4825D4	21408				
S4825D4	26803				
S4825D4	27902				
S4826D4	21408				
S4826D4	26803				
S4826D4	27902				
5RCL4A	06600				
5RCL4A	26803				
5RCL4A	28602				
6MCC1A	05600				
6MCC1A	26803				
6MCC1A	27901				
6MGC1A	05600				
6MGC1A	26803				
6MGC1A	27901				
8R335A	15001				
8R335A	26803				
8VSR40	14000				
8VSR40	26803				
8VSR40	26901				
8VSR50	14000				
8VSR50	26803				

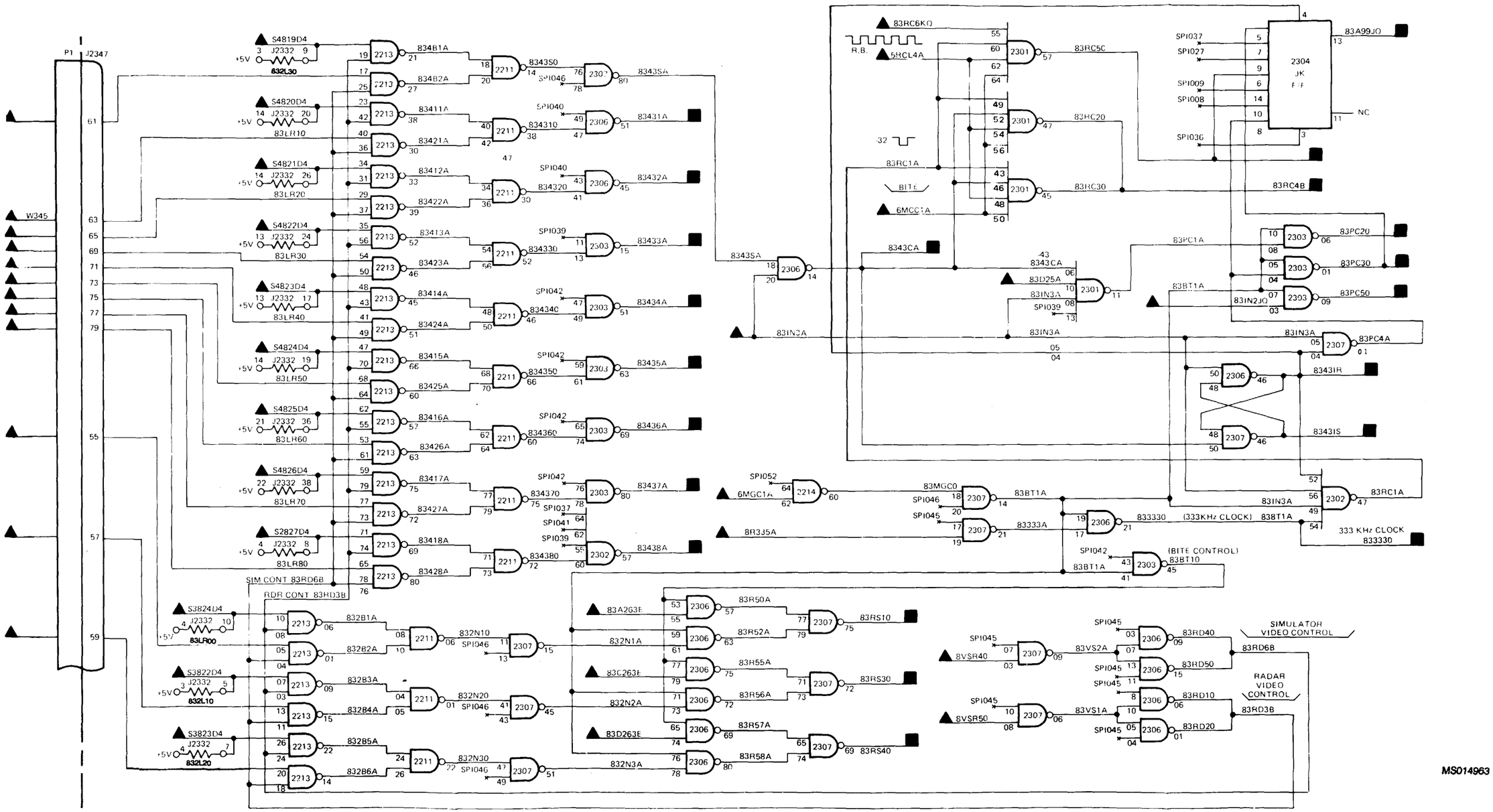


NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1. RIU BAY 1 CARD CAGE (1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1123, A1139, A1149 AND A1227

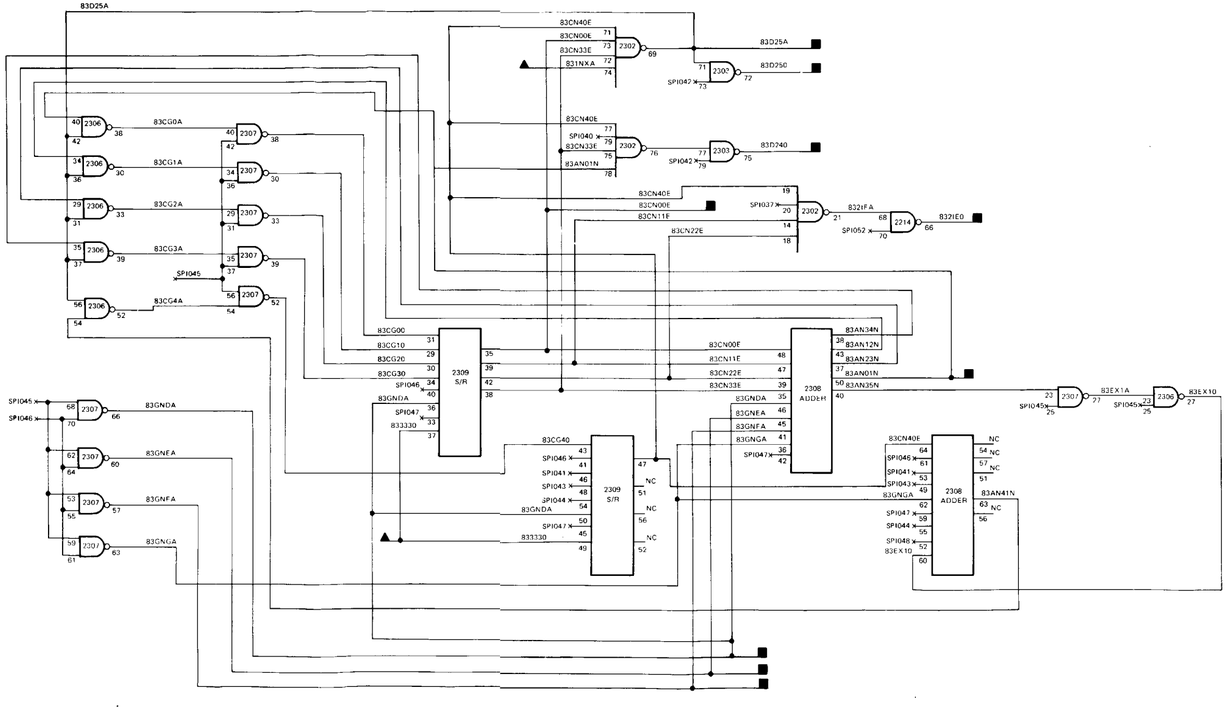
MS200795A

Change 2 FO-181. DIU Radar Simulator Data Selection and Control Logic Diagram (Sheet 1 of 2)



Change 2 FO-181. DIU Radar Simulator Data Selection and Control Logic Diagram (Sheet 2 of 2)

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
831NXA	18300	83AN01N	18300
833330	18102	83CN00E	18300
		83D240	18300
		83D25A	18102, 18300
		83D250	18300, 18401
		83GNDA	18300, 18401, 18402
		83GNEA	18300
		83GNFA	18401
		832IE0	18300



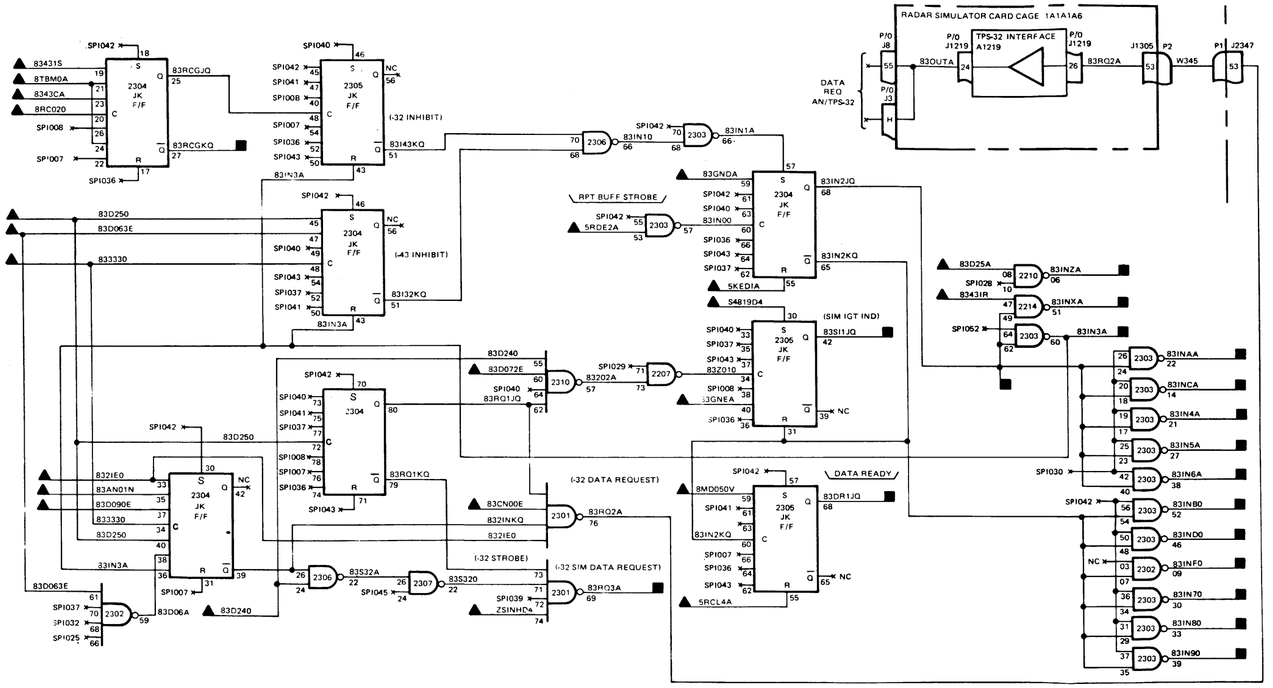
Change 2 FO-182. DIU Module-26 Counter Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1.
   RIU BAY 1 CARD CAGE (1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1123, A1139, A1149 AND A1227

MS200796A

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
S4819D4	21408	83DR1JQ	05900, 06600, 26803
S4819D4	27902	831NAA	18403, 18404
ZS1NHD4	26803	831NB0	18403, 18404
ZS1NHD4	27002	831NCA	18404
ZS1NHD4	27803	831ND0	18403, 18404
5KED1A	13400	831NF0	18404
5KED1A	26803	831NXA	18200
5KED1A	31301	831NZA	18403, 18404
5RCL4A	06600	831N2JQ	18100
5RCL4A	26803	831N3A	18100
5RCL4A	28602	831N4A	18401
5RDE2A	06600	831N5A	18402
5RDE2A	26803	831N6A	18401, 18402, 18403, 18404
5RDE2A	28602	831N70	18401
8MD050V	17200	831N80	18402
8RC020	14900	831N90	18401, 18402, 18403, 18404
8RC020	26803	83RC6KQ	18100
8RC020	27501	83RQ3A	31802
8TBM0A	16900	83S11JQ	18401
83AN01N	18200		
83CN00E	18200		
83D063E	18404		
83D072E	18404		
83D090E	18404		
83D240	18200		
83D25A	18200		
83D250	18200		
83GNDA	18200		
83GNEA	18200		
8321E0	18200		
833330	18100		
8343CA	18100		
8343IR	18100		
8343IS	18100		



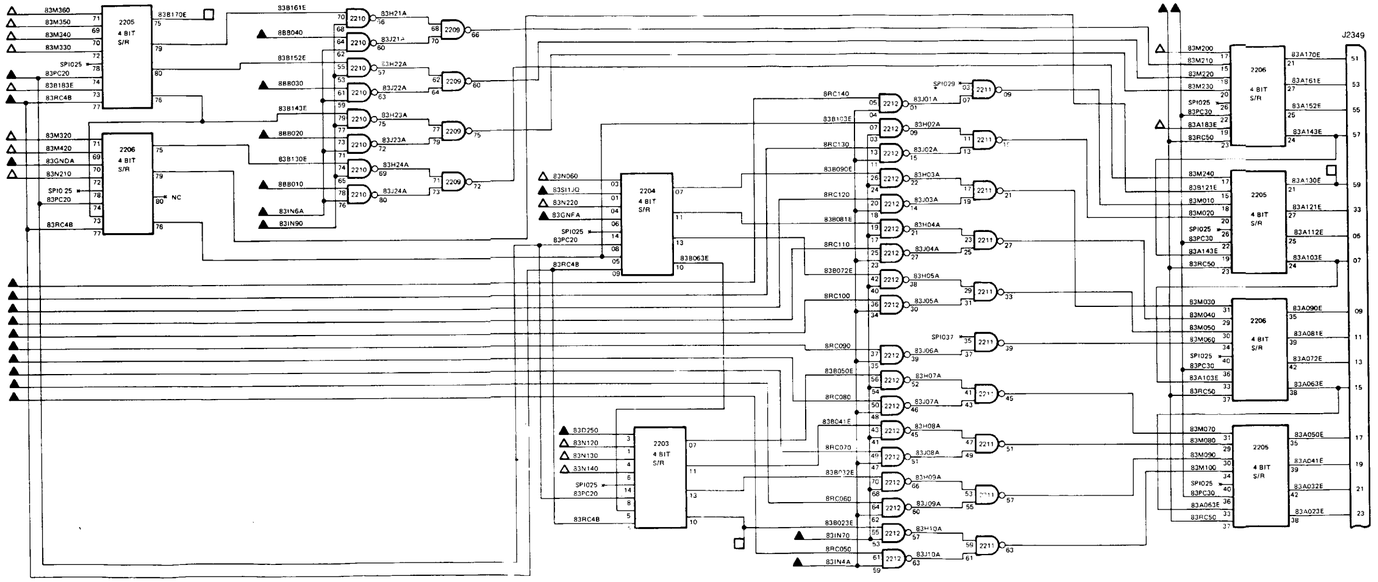
Change 3 FO-183. DIU Data Receive Detector Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1.
   RIU BAY 1 CARD CAGE (1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1123, A1139, A1149 AND A1227

MS200797B

INPUT		INPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	SOURCE FO-SH
88B010	17102	83PC30	18100
88B020	17102	83RC4B	18100
88B030	17102	83RC50	18100
88B040	17102	83S11JG	18300
8RC050	14900		
8RC050	26803		
8RC050	27501		
8RC060	14900		
8RC060	26803		
8RC060	27501		
8RC070	14900		
8RC070	26803		
8RC070	27501		
8RC080	14900		
8RC080	26803		
8RC080	27501		
8RC090	14900		
8RC090	26803		
8RC090	27501		
8RC100	14600		
8RC100	26802		
8RC100	26803		
8RC100	27501		
8RC110	14600		
8RC110	26802		
8RC110	26803		
8RC110	27501		
8RC120	14600		
8RC120	26803		
8RC120	27501		
8RC130	14600		
8RC130	26802		
8RC130	26803		
8RC130	27501		
8RC140	14600		
8RC140	26802		
8RC140	26803		
8RC140	27501		
83D250	18200		
83GNDA	18200		
83GNFA	18200		
83IN4A	18300		
83IN6A	18300		
83IN7C	18300		
83IN90	18300		
83PC20	18100		

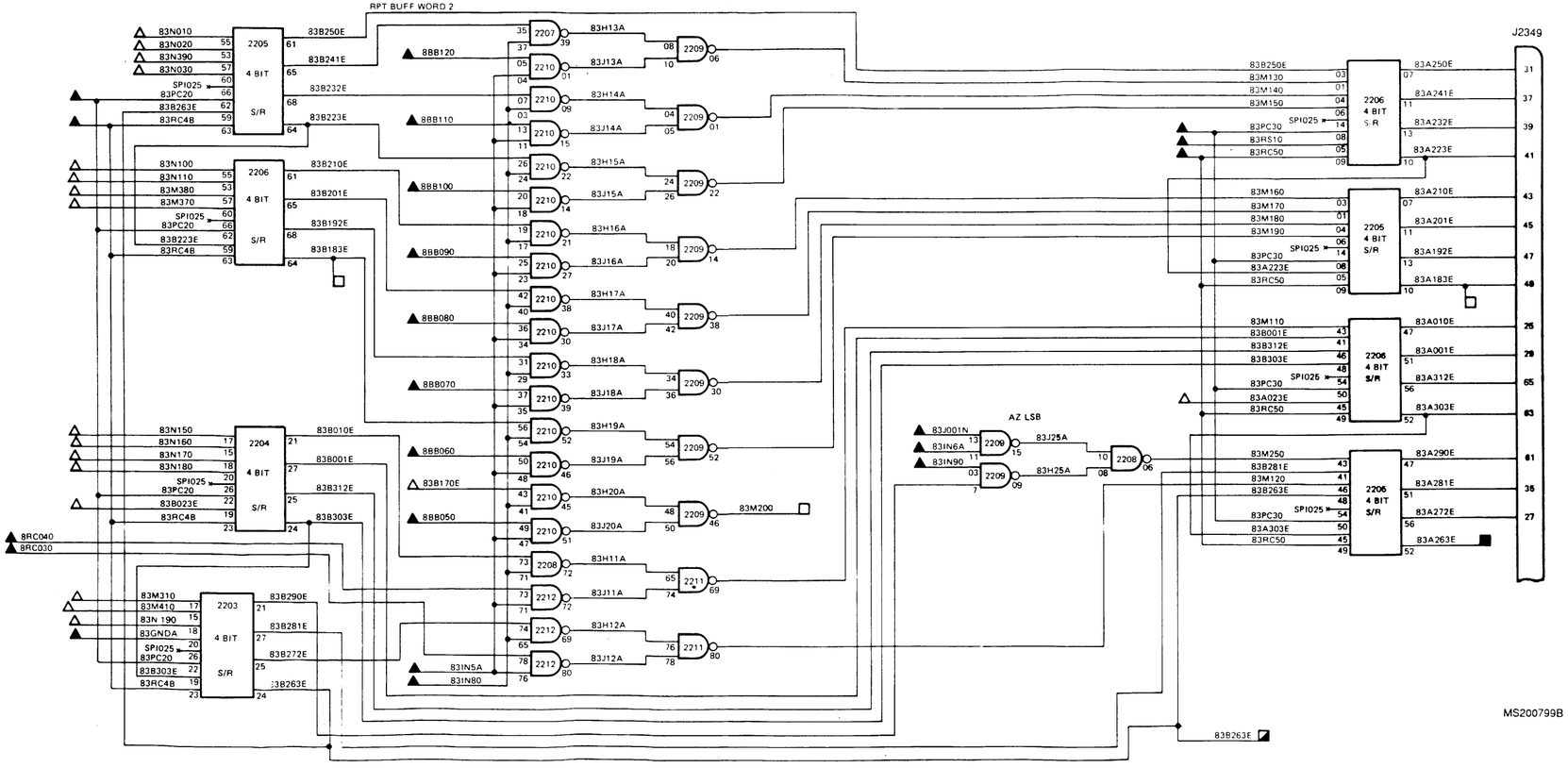


Change 3 FO-184. DIU Data Registers Logic Diagram (Sheet 1 of 4)

NOTES: UNLESS OTHERWISE SPECIFIED

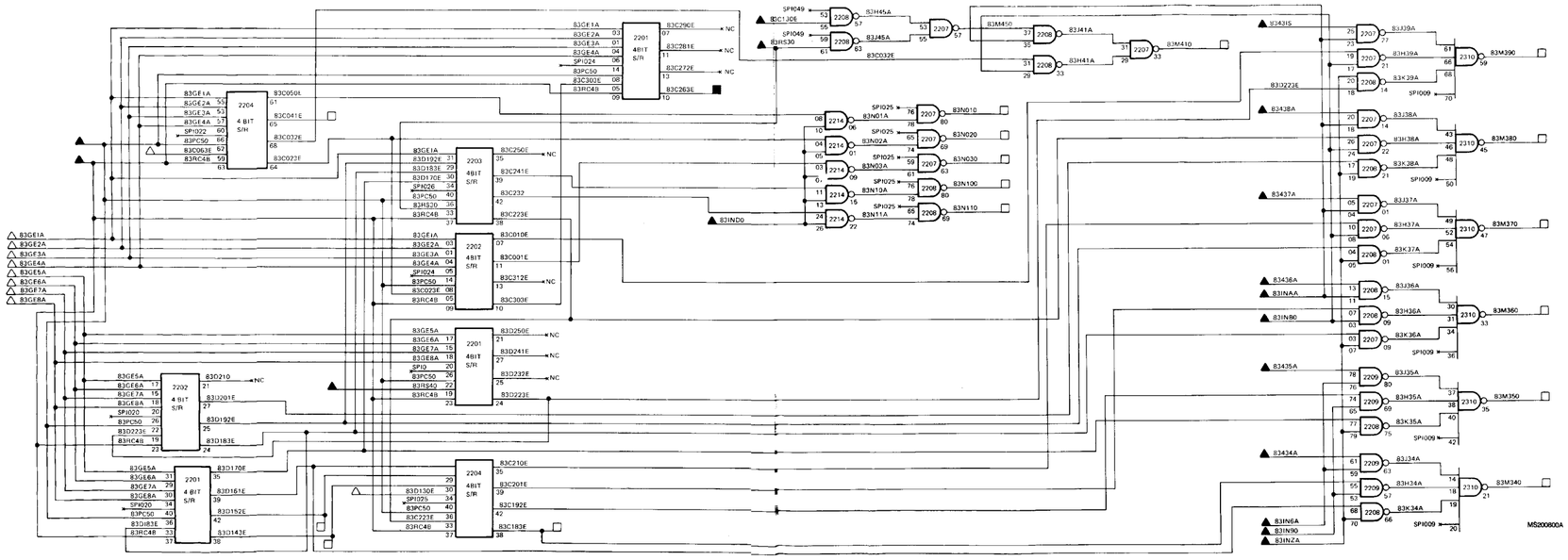
1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1. RIU BAY 1 CARD CAGE (1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1123, A1139, A1149 AND A1227

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
8BB050	17102	83A263E	18100, 20502, 28401
8BB060	17102	83B263E	20502, 26803, 28401
8BB070	17102		
8BB080	17102		
8BB090	17102		
8BB100	17102		
8BB110	17102		
8BB120	17102		
8BJ001N	17101		
8RC030	14900		
8RC030	26803		
8RC030	27501		
8RC040	14900		
8RC040	26803		
8RC040	27501		
83GNDA	18200		
83IN5A	18300		
83IN6A	18300		
83IN80	18300		
83IN90	18300		
83PC20	18100		
83PC30	18100		
83RC4B	18100		
83RC50	18100		
83RS10	18100		



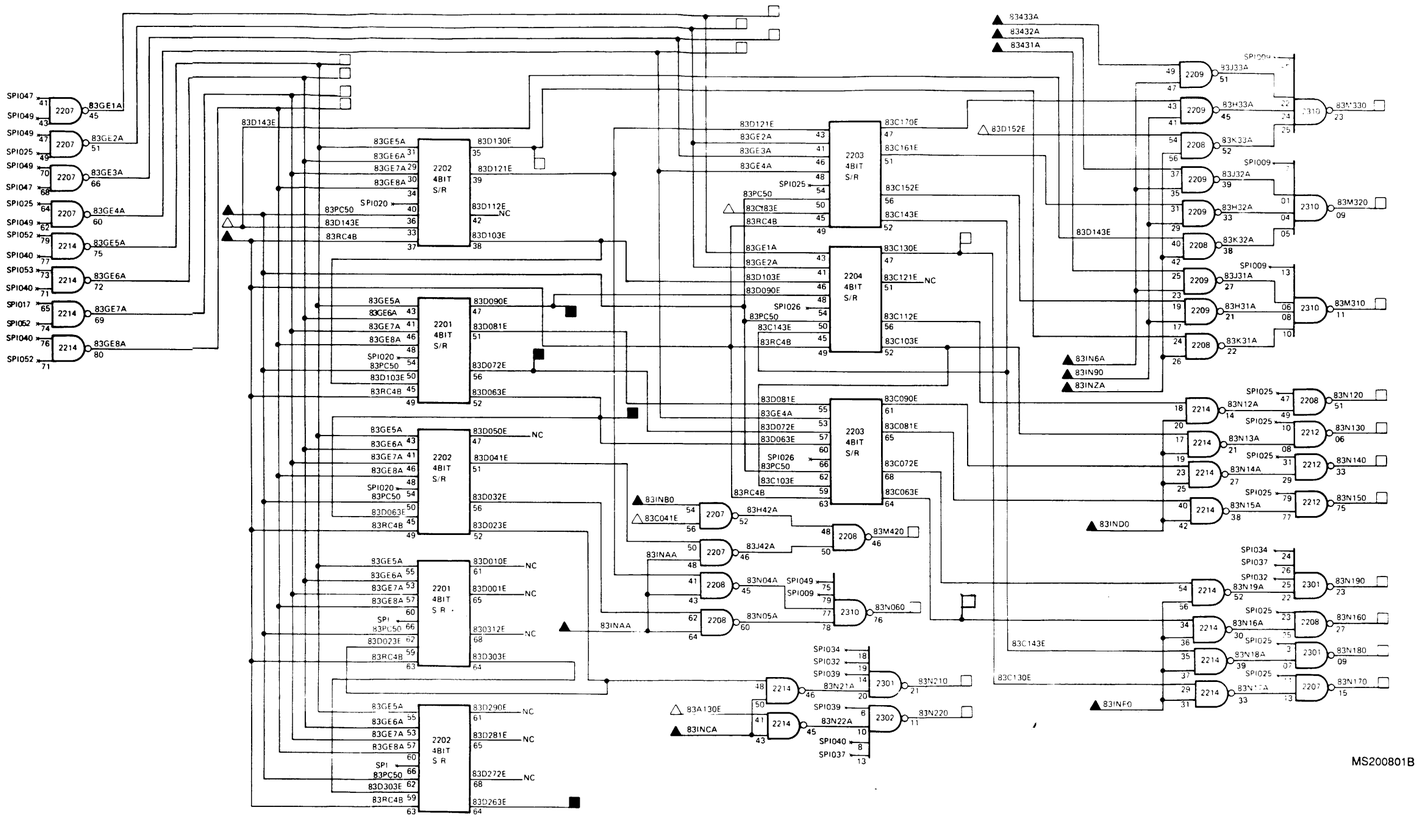
Change 3 FO-184. DIU Data Registers Logic Diagram (Sheet 2 of 4)

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
83INAA	18300	83C263E	18102, 20502, 26803, 28401
83INB0	18300		
83IND0	18300		
83INZA	18300		
83IN6A	18300		
83IN90	18300		
83PC50	18102		
83RC4B	18102		
83RS30	18102		
83RS40	18102		
83431S	18102		
83434A	18102		
83435A	18102		
83436A	18102		
83437A	18102		
83438A	18102		



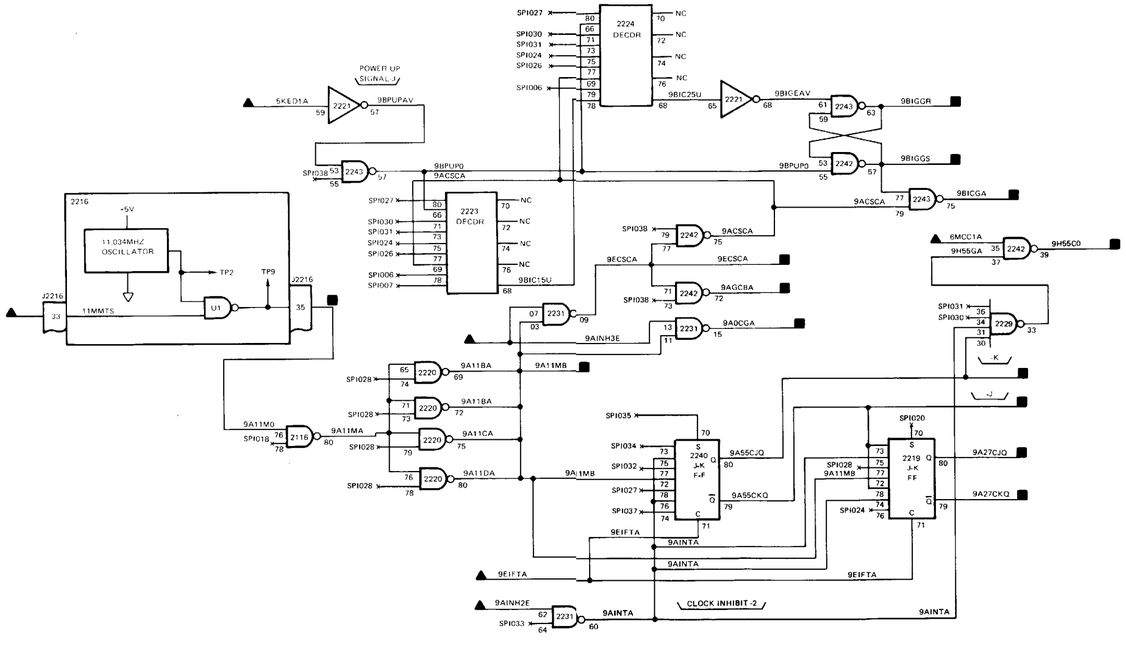
Change 3 FO-184. DIU Data Registers Logic Diagram (Sheet 3 of 4)

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
83INAA	18300	83D063E	18300
83INB0	18300	83D072E	18300
83INCA	18300	83D090E	18300
83IND0	18300	83D263E	18100, 20502, 26803, 28401
83INF0	18300		
83INZA	18300		
83IN6A	18300		
83IN90	18300		
83PC50	18100		
83RC4B	18100		
83431A	18100		
83432A	18100		
83433A	18100		



Change 3 FO-184. DIU Data Registers Logic Diagram (Sheet 4 of 4)

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
11MMTS	26803	9AGCBA	05800, 05900, 26802, 26803
5KED1A	13402	9A0CGA	20502
5KED1A	26803	9A11MB	26803
5KED1A	31301	9A11MO	19102
6MCC1A	05600	9A27CJQ	18600
6MCC1A	26803	9A27CKQ	19103, 26803
6MCC1A	27901	9A55CJQ	19000
9AINH2E	20501	9A55CKQ	19000
9AINH3E	20501	9BICGA	18600, 18700
9AINH3E	26803	9BIGGR	18600, 18700, 19200, 20501
9EIFTA	18801	9BIGGS	18700, 19400
		9ECSCA	18801, 18802
		9H55C0	19400

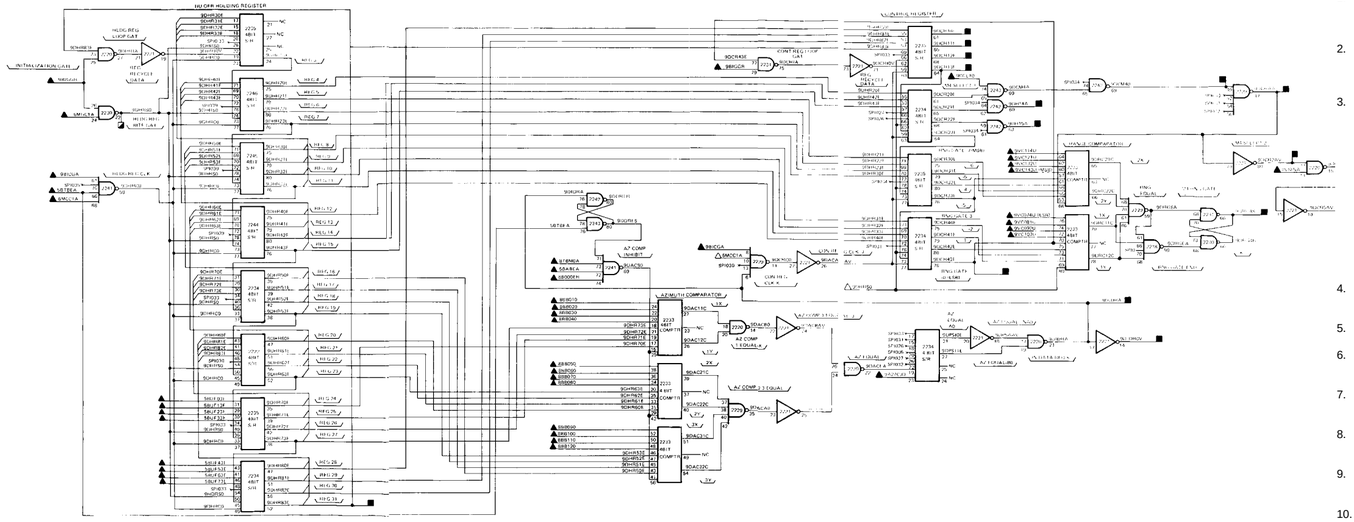


Change 2 FO-185. IIU Clock and Initialization Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1.
   RIU BAY 1 CARD CAGE (1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1123, A1139, A1149 AND A1227

INPUT		INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
5BABEA	13302	8B0D0ER	17102	9DCM40	18700, 19000
5BABEA	26803	8TBNDA	16900	9DCR4A	18700, 19000, 20201
5BTBEA	13302	8VSINA	12603	9DCR10E	18700
5BTBEA	26803	9A27CUQ	18500	9DCR11E	18700
5BUF03E	13301	9B1CGA	18500	9DCR12E	18700
5BUF03E	26803	9B1GGR	18500	9DCR13E	18700
5BUF03E	28201	9CCL10	28702	9DCR2AV	18801, 18900, 20502
5BUF13E	13301	9CCL10	29801	9DCR43E	20502
5BUF13E	26803	9VC074U	19600	9DCR54V	26803
5BUF13E	28201	9VC081U	19600	9DMR5D	19400
5BUF23E	13301	9VC092U	19600	9DMR83E	20502
5BUF23E	26803	9VC103U	19600	9DM14A	12603, 26803
5BUF23E	28201	9VC114U	19600	9DM15A	12603, 26803
5BUF33E	13301	9VC121U	19600	9D1DRA	13101, 26803
5BUF33E	26803	9VC132U	19600	9D1DRQV	18700
5BUF33E	28201	9VC143U	19600	9DRG4S	19400
5BUF43E	13301				
5BUF43E	26803				
5BUF43E	28201				
5BUF53E	13301				
5BUF53E	26803				
5BUF53E	28201				
5BUF63E	13301				
5BUF63E	26803				
5BUF63E	28201				
5BUF73E	13301				
5BUF73E	26803				
5BUF73E	28201				
6MCC1A	05600				
6MCC1A	26803				
6MCC1A	27901				
6MGC1A	05600				
6MGC1A	26803				
6MGC1A	27901				
8BB010	17102				
8BB020	17102				
8BB030	17102				
8BB040	17102				
8BB050	17102				
8BB060	17102				
8BB070	17102				
8BB080	17102				
8BB090	17102				
8BB100	17102				
8BB110	17102				
8BB120	17102				

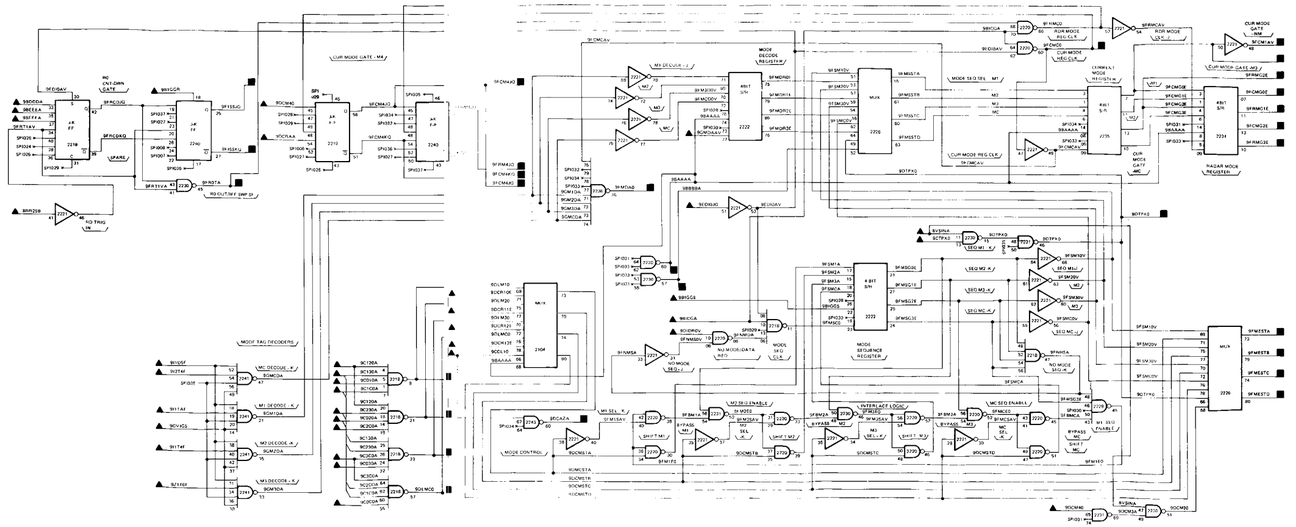


Change 2 FO-186. IIU Range and Azimuth Compare Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1. RIU BAY 1 CARD CAGE (1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1123, A1139, A1149 AND A1227

INPUT		INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
8RR2SB	14600	911T4F	26803	9BAAAA	18802, 19400
8RR2SB	26803	911T4F	19101	9BBBBA	18801, 19400
8RR2SB	27502	911T4F	26803	9DCA2A	12603, 26803
8V51NA	12603	911T6F	19101	9DLM10	20502
9BD0DA	20501	911T6F	26803	9DLM1D	20502
9BEEEA	20501	91274F	19102	9DLM20	20502
9BFFFA	20501	91274F	26803	9DLM30	20502
9B1C6A	18500			9DTPX0	19000, 26803
9B1GGR	18500			9FCMC0	19200
9B1GGS	18500			9FCMG0E	19103, 26803
9CCL10	28702			9FCMG2E	19200, 26803
9CCL1D	29801			9FCM1AV	19103, 26803
9CTPX0	27801			9FCM4JQ	19101, 19400, 26803
9C0C0A	26803			9FCM4KQ	19103, 26803
9C0C0A	28701			9FISSJQ	26803
9C0I0A	26803			9FISSKQ	19700
9C0I0A	28701			9FMDAD	18802
9C0Z0A	26803			9FMESTA	18900, 20502, 26803
9C0Z0A	28701			9FMESTB	18900, 20502, 26803
9C0Z0A	26803			9FMESTC	18900, 20502, 26803
9C0Z0A	28701			9FMESTD	18900, 20502, 26803
9C1C0A	26803			9FRMG0E	09900, 20502, 26803, 27302
9C1C0A	28701			9FRMG1E	09900, 20502, 26803, 27302
9C12GA	26803			9FRMG2E	09900, 20502, 26803, 27302
9C12GA	28701			9FRMG3E	09900, 20502, 26803, 27302
9C13GA	26803			9FRM4JQ	08300, 20502, 26803, 27302
9C13GA	28701			9FRDTA	08300, 19700, 27301
9C2C0A	26803				
9C2C0A	28701				
9C23GA	26803				
9C23GA	28701				
9C3C0A	26803				
9C3C0A	28701				
9DCM40	18600				
9DCRAA	18600				
9DCR10E	18600				
9DCR11E	18600				
9DCR12E	18600				
9DCR13E	18600				
9D1DROV	18600				
9EDIOJQ	18802				
9GMDAAV	18802				
9GVIGS	19000				
911D5F	19101				
911D5F	26803				
911TAF	19101				



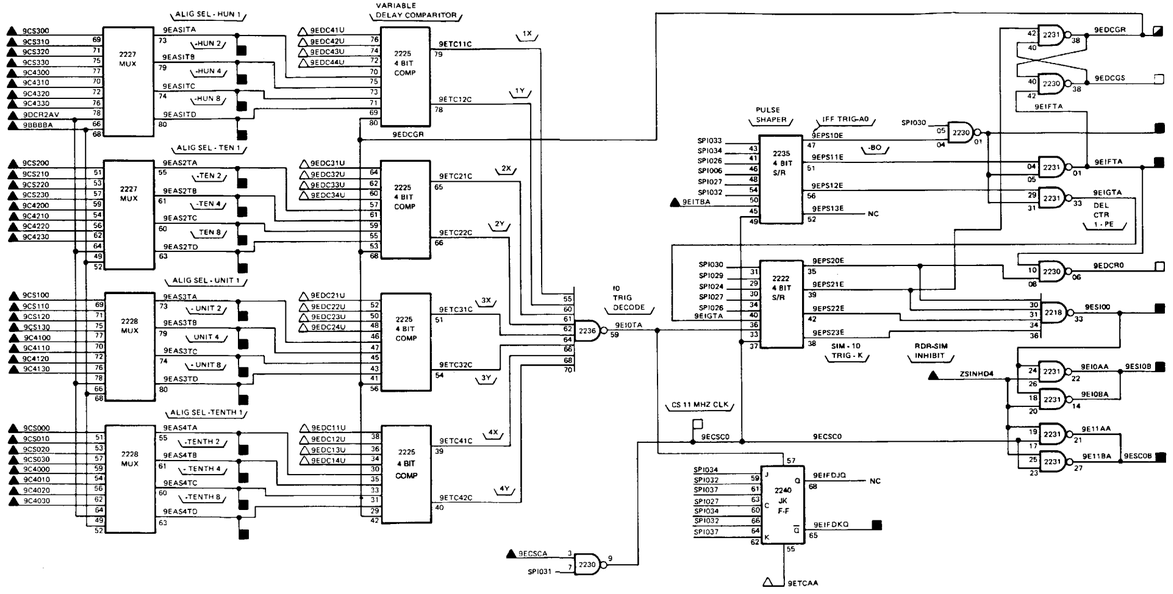
Change 3 FO-187. IIU Mode Sequencer and Gating Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1.
   RIU BAY 1 CARD CAGE (1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A2217 AND A2332.

MS200804A

INPUT		INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
ZSINHD4	26803	9C4110	26803	9EAS1TA	20502
ZSINHD4	27002	9C4110	29801	9EAS1TB	20502
ZSINHD4	27803	9C412C	26803	9EAS1TC	20502
9BBBBA	18700	9C4120	29801	9EAS1TD	20502
9CS000	26803	9C4130	26803	9EAS2TA	20502
9CS000	29801	9C4130	29801	9EAS2TB	20502
9CS010	26803	9C4200	26803	9EAS2TC	20502
9CS010	29801	9C4200	29801	9EAS2TD	20502
9CS020	26803	9C4210	26803	9EAS3TA	20502
9CS020	29801	9C4210	29801	9EAS3TB	20502
9CS030	26803	9C4220	26803	9EAS3TC	20502
9CS030	29801	9C4220	29801	9EAS3TD	20502
9CS100	26803	9C4230	26803	9EAS4TA	20502
9CS100	29801	9C4230	29801	9EAS4TB	20502
9CS110	26803	9C4300	26803	9EAS4TC	20502
9CS110	29801	9C4300	29801	9EAS4TD	20502
9CS120	26803	9C4310	26803	9EDCGR	19200
9CS120	29801	9C4310	29801	9EIFDKQ	19800
9CS130	26803	9C4320	26803	9EIFTA	18500
9CS130	29801	9C4320	29801	9EPSAA	20501
9CS200	26803	9C4330	26803	9ESC0B	26803, 28701, 31802
9CS200	29801	9C4330	29801	9ES10B	26803, 28701
9CS210	26803	9DCR2AV	18600	9ES100	20501
9CS210	29801	9ECSCA	18500		
9CS220	26803	9EITBA	18900		
9CS220	29801	9EITBA	26803		
9CS230	26803				
9CS230	29801				
9CS300	26803				
9CS300	29801				
9CS310	26803				
9CS310	29801				
9CS320	26803				
9CS320	29801				
9CS330	26803				
9CS330	29801				
9C4000	26803				
9C4000	29801				
9C4010	26803				
9C4010	29801				
9C4020	26803				
9C4020	29801				
9C4030	26803				
9C4030	29801				
9C4100	26803				
9C4100	29801				



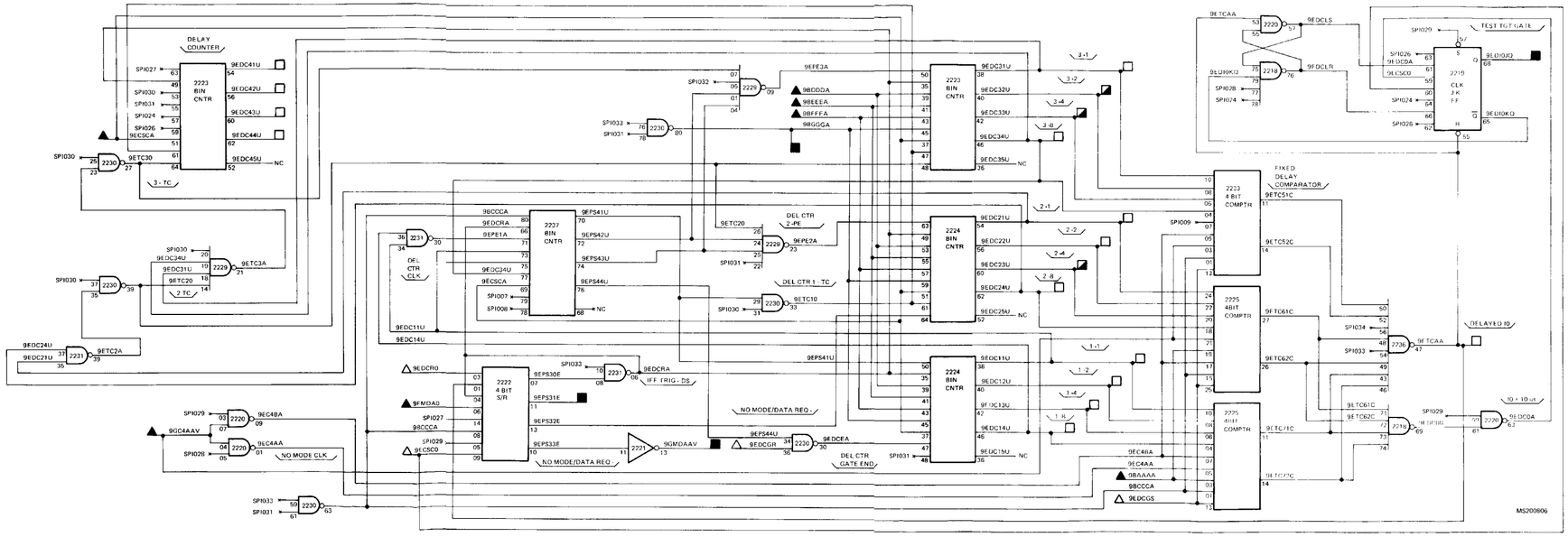
Change 3 FO-188. IIU Adjustable Precision Time Delay Logic Diagram (Sheet 1 of 2)

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, RIU BAY 1 CARD CAGE (1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A2217 AND A2332.

MS200805A

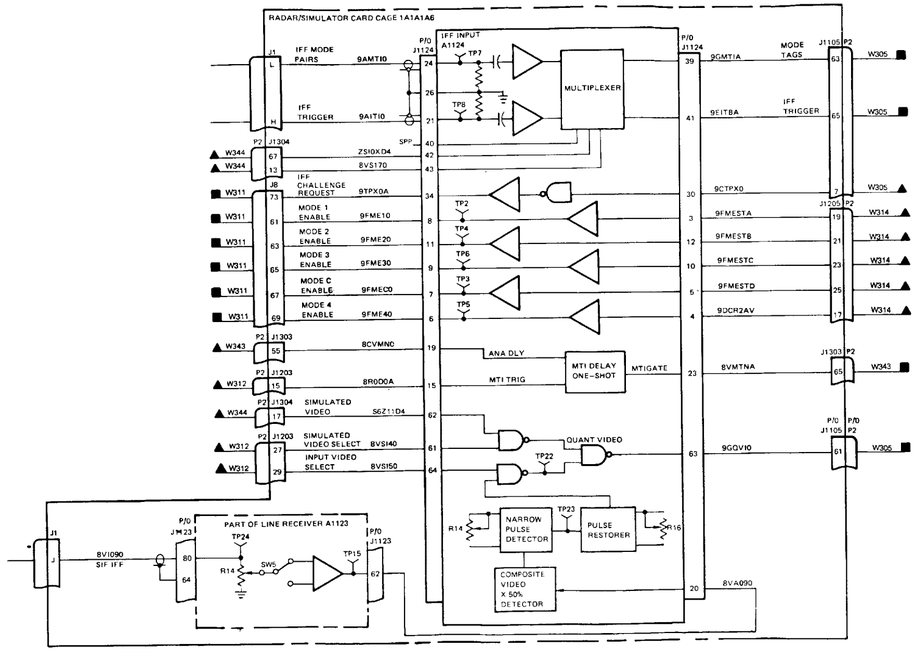
INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
9BAAAA	18700	9BGGGA	19000, 19400
9BDDDA	20501	9EDC23U	19200
9BEEEA	20501	9EDC32U	19200
9BFFFA	20501	9EDC33U	19200
9ECSCA	18500	9EDIOJG	18700, 19901
9FMDAQ	18700	9EPS31E	19600, 19901, 19903, 20201
9GC4AAV	19000	9GMDAAV	18700, 19000



Change 3 FO-188. IIU Adjustable Precision Time Delay Logic Diagram (Sheet 2 of 2)

INPUT	
SIGNAL	SOURCE FO-SH
S6Z11D4	28801
ZSI0XD4	20802
ZSI0XD4	28802
8CVMN0	28902
8R000A	26901
8VS140	14000
8VS140	26901
8VS150	14000
8VS150	26901
8VS170	14000
8VS170	28801
9CTPX0	27801
9DCR2AV	18600
9FMESTA	18700
9FMESTB	18700
9FMESTC	18700
9FMESTD	18700

OUTPUT	
SIGNAL	DESTINATION FO-SH
8VMTNA	14100, 28902
9EITBA	18801
9FMEC0	33100
9FME10	33100
9FME20	33100
9FME30	33100
9FME40	33100
9GMT1A	19000, 26803
9GQV10	19000, 20501, 20502, 26803
9TPX0A	33100



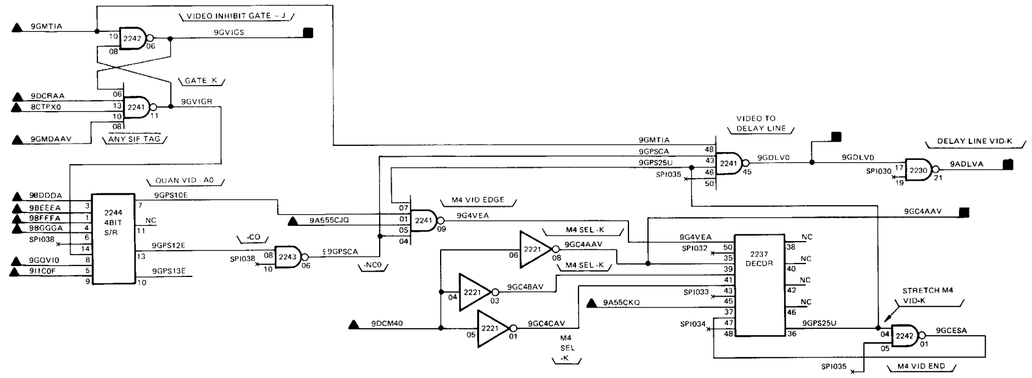
Change 3 FO-189. IIU IFF Interface Circuit Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1. RSU (1A1A1A6)
3. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
4. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
5. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
6. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
7. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.

MS 200807A

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
9A55CJQ	18500	9ADLVA	20501
9A55CKQ	18500	9GC4AAV	18802
9BDDDA	20501	9GDLV0	19101, 20502, 26803
9BEEEA	20501	9GVIGS	18700
9BFFFA	20501		
9BGGGA	18802		
9DCM40	18600		
9DCRAA	18600		
9DTPXQ	18700		
9GMDAAV	18802		
9GMT1A	18900		
9GQV10	18900		
911C0F	19101		
911C0F	26803		



FO-190. IIU Video Quantizer Logic Diagram

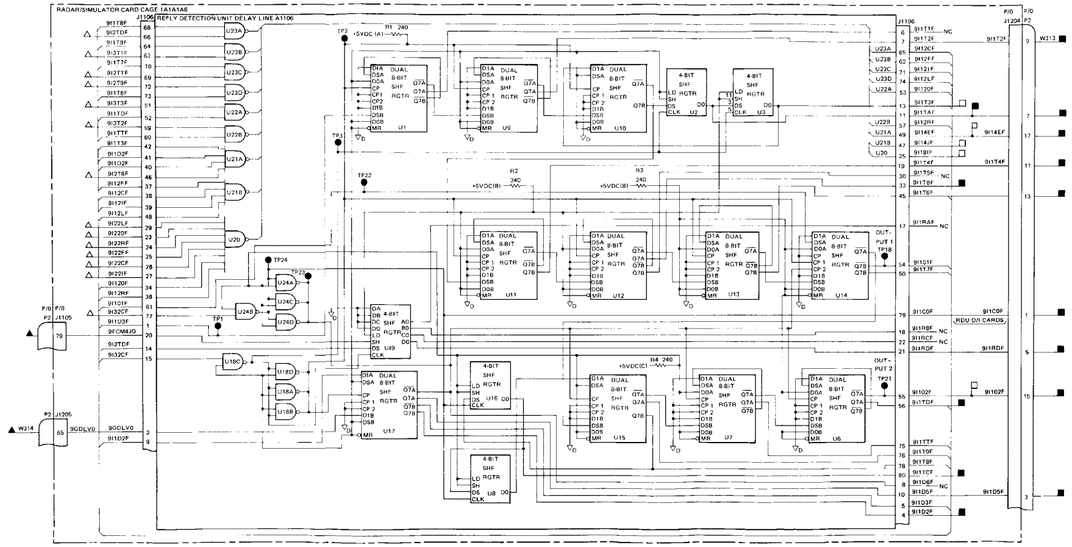
NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1.
   RIU BAY 1 CARD CAGE (1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A2217 AND A2332.

MS200808

I/O TABLES FOR FO-19101

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
9FCMAJG	18700	911C0F	19000, 19400, 19800
9GDLV0	19000	911D2F	19300
		911D5F	18700
		911R0F	19400, 26803
		911TAF	18700, 19300
		911TBF	19300
		911TCF	19300
		911TDF	19300
		911T2F	19200, 26803
		911T4F	18700
		911T6F	18700
		91102F	26803
		9114EF	19400, 26803



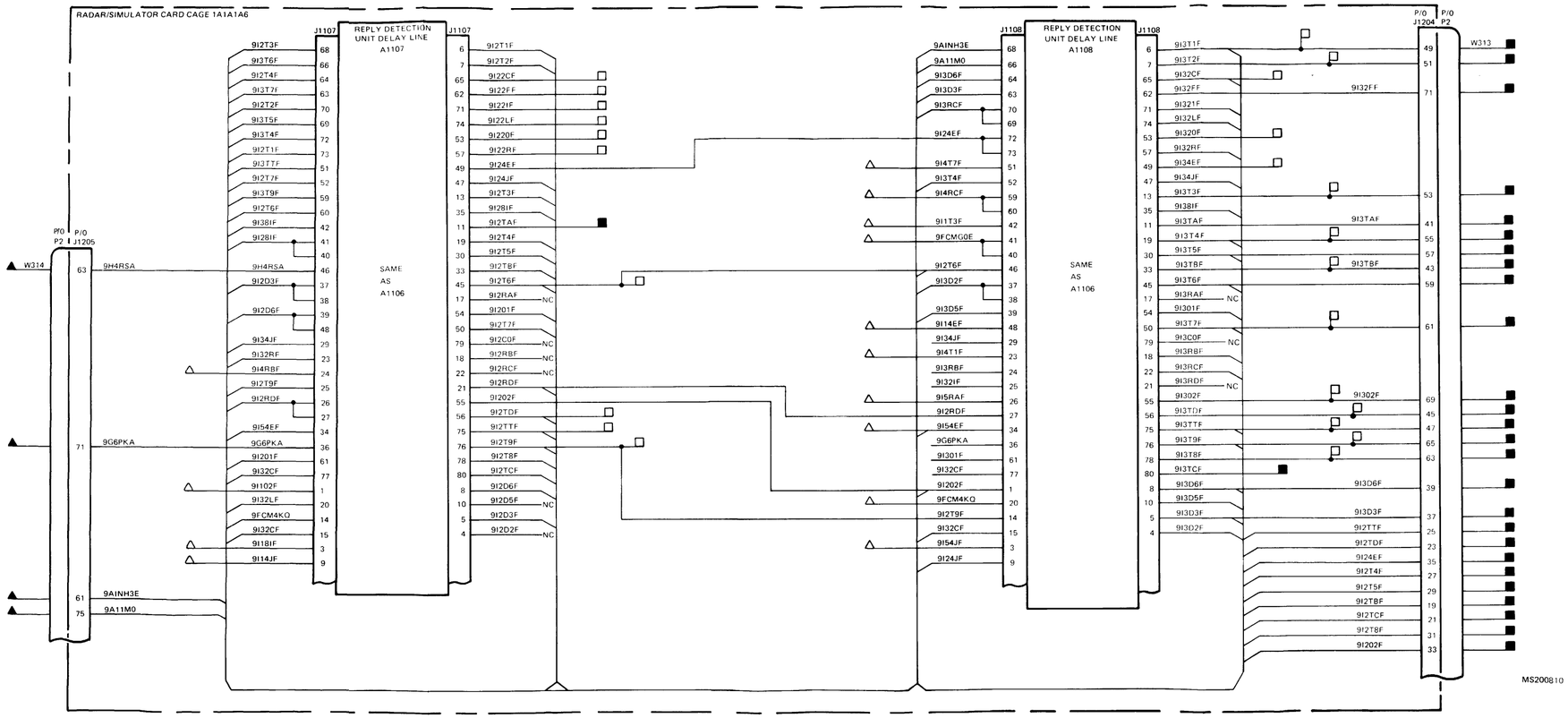
FO-191. IIU Delay Lines Logic Diagram (Sheet 1 of 3)

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1. RSU (1A1A1A6)
3. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
4. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
5. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
6. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
7. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.

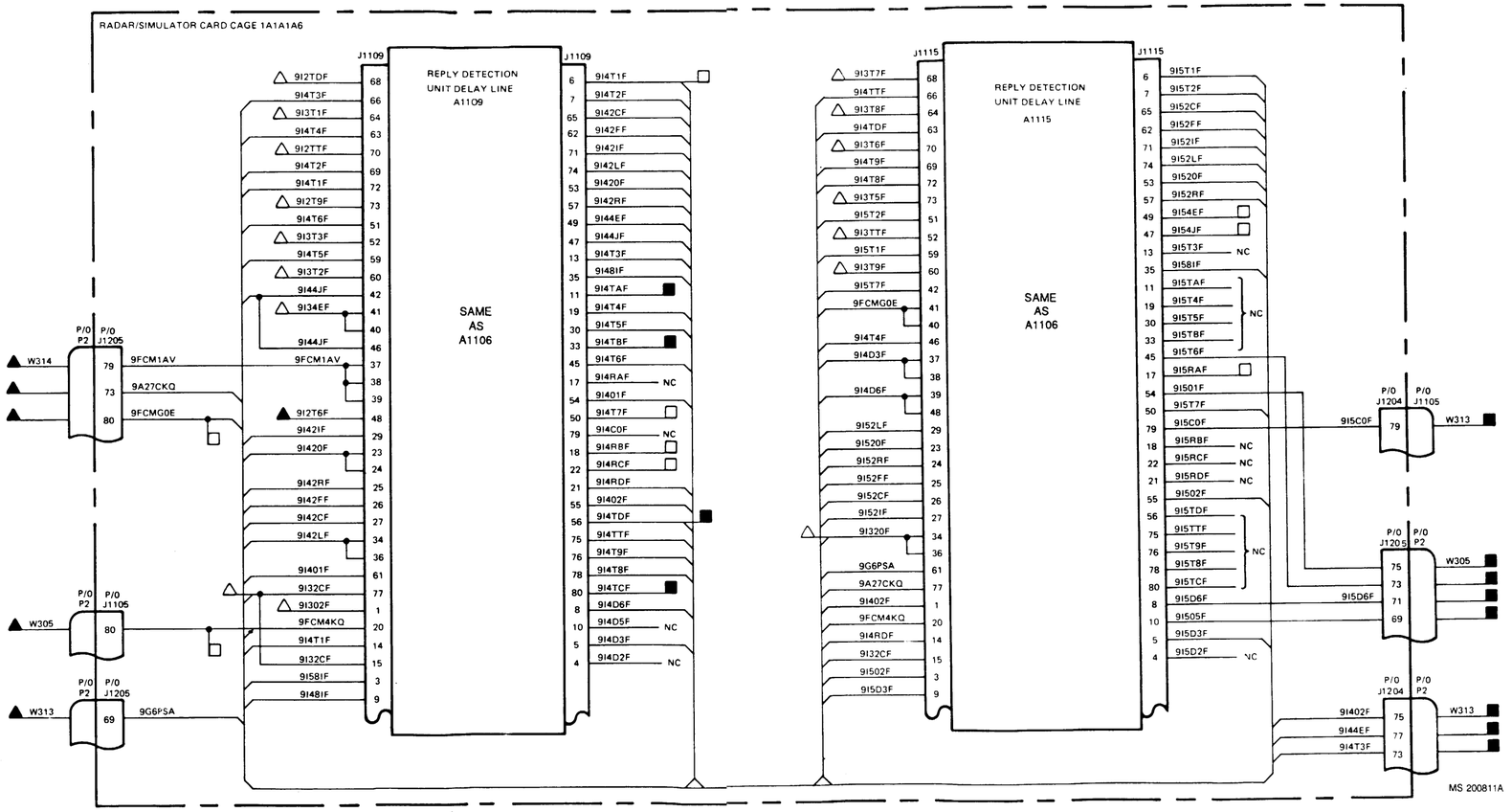
MS200809

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
9AINH3E	20501	9I2TAF	19300
9AINH3E	26803	9I2TBF	19400, 26803
9A11M0	18500	9I2TCF	19400, 26803
9G6PKA	19200	9I2TDF	19200, 19400, 26803
9G6PKA	26803	9I2TTF	19200, 19400, 26803
9H4RSA	19400	9I2T4F	18700
9H4RSA	26803	9I2T5F	19200, 26803
		9I2T8F	19200, 26803
		9I202F	26803
		9I24EF	19400, 20501, 26803
		9I3D3F	19200, 26803
		9I3D6F	19200, 26803
		9I3TAF	19400, 26803
		9I3TBF	19400, 26803
		9I3TCF	19300
		9I3TDF	19200, 19400, 26803
		9I3TTF	19200, 19400, 26803
		9I3T1F	19200, 19400, 26803
		9I3T2F	19200, 19400, 26803
		9I3T3F	19200, 19400, 26803
		9I3T4F	19400, 26803
		9I3T5F	19400, 26803
		9I3T6F	19200, 19400, 26803
		9I3T7F	19200, 19400, 26803
		9I3T8F	19200, 19400, 26803
		9I3T9F	19200, 19400, 26803
		9I302F	26803
		9I32FF	19400, 20501, 26803



FO-191. IIU Delay Lines Logic Diagram (Sheet 2 of 3).

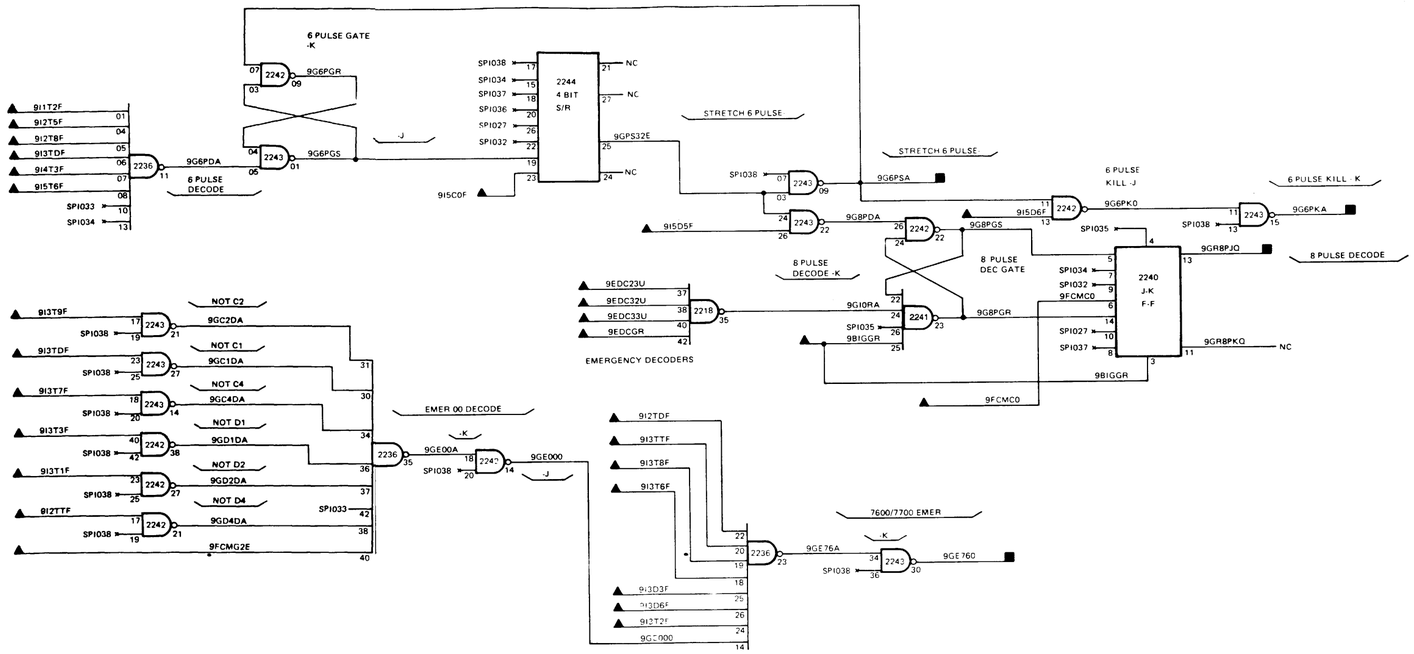
INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
9A27CKQ	18500	914TAF	19300
9FCMG0E	18700	914TBF	19300
9FCM1AV	18700	914TCF	19300
9FCM4KQ	18700	914TDF	19300
9G6PSA	19200	914T3F	19200, 26803
		91402F	26803
		9144EF	19400, 26803
		915C0F	19200, 26803
		915D5F	19200, 26803
		915D6F	19200, 26803
		915T6F	19200, 26803
		91501F	26803



Change 3 FO-191. IIU Delay Lines Logic Diagram (Sheet 3 of 3).

INPUT	
SIGNAL	SOURCE FO-SH
9B1GGR	18500
9EDCGR	18801
9EDC23U	18802
9EDC32U	18802
9EDC33U	18802
9FCMC0	18700
9FCMG2E	18700
9I1T2F	19101
9I2TDF	19102
9I2TTF	19102
9I2T5F	19102
9I2T8F	19102
9I3D3F	19102
9I3D6F	19102
9I3TDF	19102
9I3TTF	19102
9I3T1F	19102
9I3T2F	19102
9I3T3F	19102
9I3T6F	19102
9I3T7F	19102
9I3T8F	19102
9I3T9F	19102
9I4T3F	19103
9I5C0F	19103
9I5D5F	19103
9I5D6F	19103
9I5T6F	19103

OUTPUT	
SIGNAL	DESTINATION FO-SH
9GE760	19400
9GR8PJQ	08300, 26803, 27302
9G6PKA	19102
9G6PSA	19103, 26803



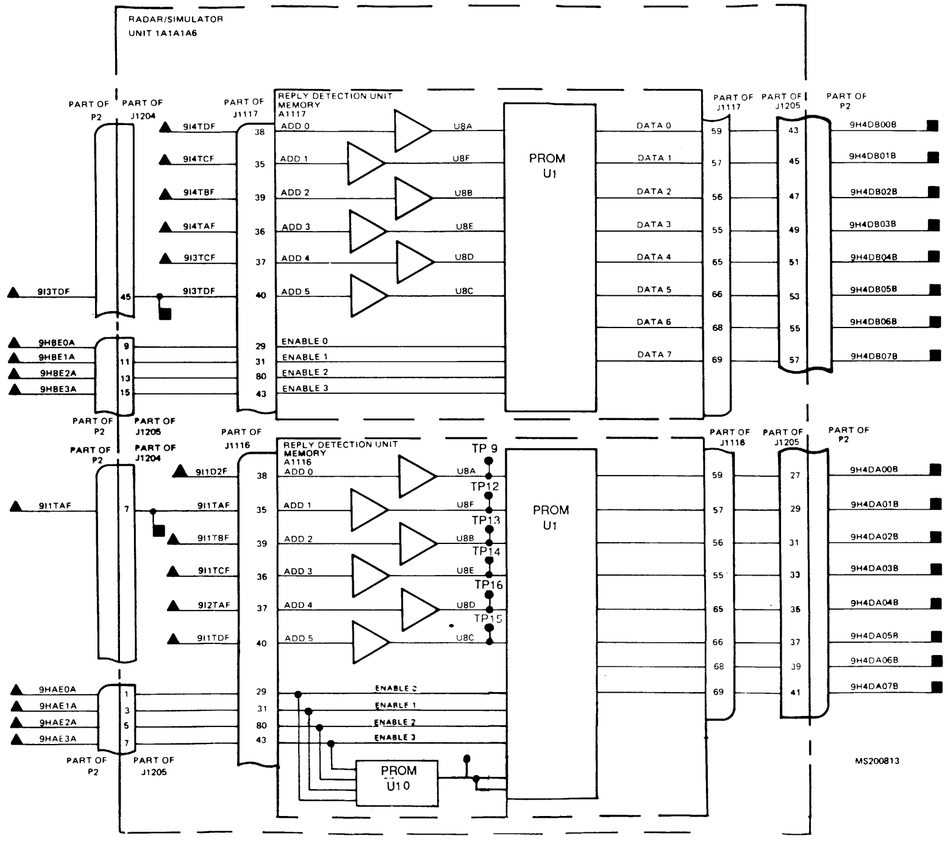
NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1. RIU BAY 1 CARD CAGE (1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1123, A1139, A1149 AND A1227

MS200812A

Change 3 FO-192. IIU Emergency Decoders Logic Diagram.

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
9HAE0A	19400	9H4DA00B	19400, 26803
9HAE0A	26803	9H4DA01B	19400, 26803
9HAE1A	19400	9H4DA02B	19400, 26803
9HAE1A	26803	9H4DA03B	19400, 26803
9HAE2A	19400	9H4DA04B	19400, 26803
9HAE2A	26803	9H4DA05B	19400, 26803
9HAE3A	19400	9H4DA06B	19400, 26803
9HAE3A	26803	9H4DA07B	19400, 26803
9HBE0A	19400	9H4DB00B	19400, 26803
9HBE0A	26803	9H4DB01B	19400, 26803
9HBE1A	19400	9H4DB02B	19400, 26803
9HBE1A	26803	9H4DB03B	19400, 26803
9HBE2A	19400	9H4DB04B	19400, 26803
9HBE2A	26803	9H4DB05B	19400, 26803
9HBE3A	19400	9H4DB06B	19400, 26803
9HBE3A	26803	9H4DB07B	19400, 26803
9I1D2F	19101		
9I1TAF	19101		
9I1TAF	26803		
9I1TBF	19101		
9I1TCF	19101		
9I1TDF	19101		
9I2TAF	19102		
9I3TCF	19102		
9I4TAF	19103		
9I4TBF	19103		
9I4TCF	19103		
9I4TDF	19103		



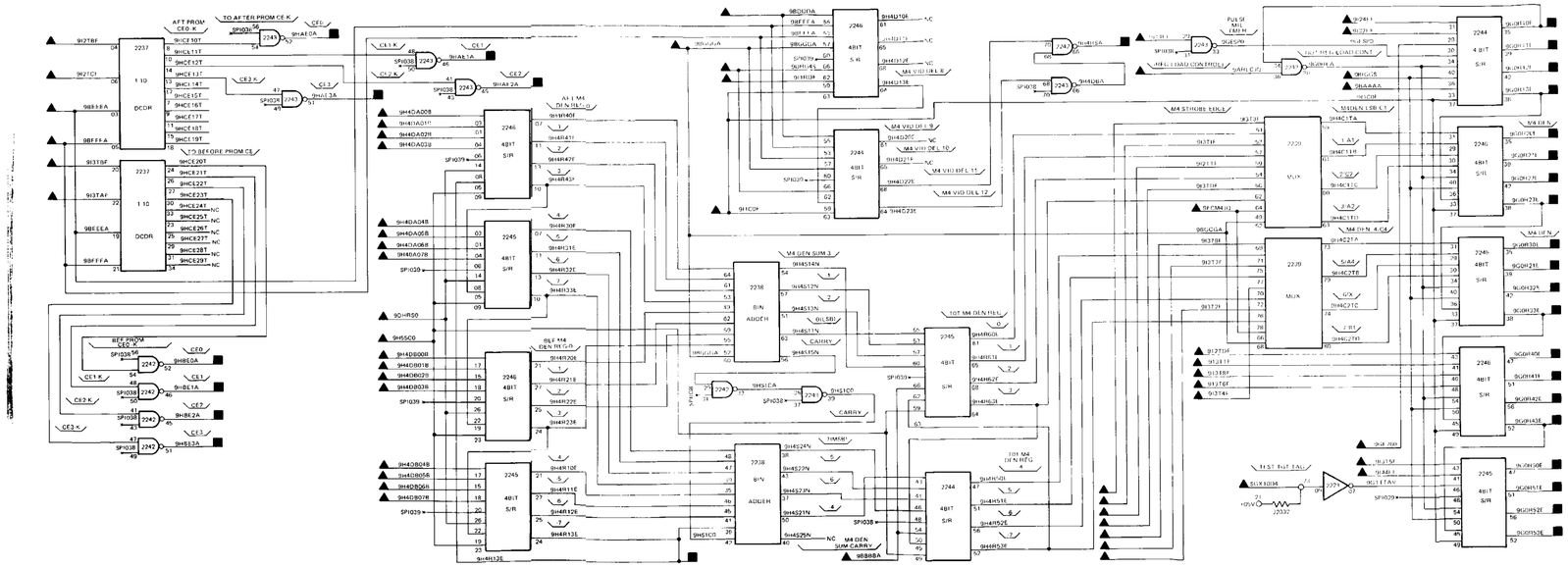
NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1. RSU (1A1A1A6.)
3. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
4. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
5. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
6. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
7. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.

MS200813A

Change 3 FO-193. IIU Before and After Density PROM Logic Diagram.

INPUT SIGNAL	INPUT SOURCE FO-SH	INPUT SIGNAL	INPUT SOURCE FO-SH	OUTPUT SIGNAL	OUTPUT DESTINATION FO-SH
S6X1D04	26803	9137 4F	19102	9GDR10E	19800, 20502
9ARLCJG	20501	9137 5F	19102	9GDR11E	19501, 20502
9BAAAA	18700	9137 6F	19102	9GDR12E	19501, 20502
9BBBBA	18700	9137 7F	19102	9GDR13E	19501, 20502
9BDDDA	20501	9137 8F	19102	9GDR20E	19501, 20502
9BEEEA	20501	9137 9F	19102	9GDR21E	19501, 20502
9BFFFA	20501	9132 FF	19102	9GDR22E	19501, 20502
9BGGGA	18802	9144 EF	19103	9GDR23E	19501, 20502
9B1GGS	18500			9GDR30E	19501, 20502
9DHRSO	18600			9GDR31E	19501, 20502
9DRG4S	18600			9GDR32E	19501, 20502
9FCM4JG	18700			9GDR33E	19501, 20502
9GE760	19200			9GDR40E	19501, 20502
9H4DA00B	19300			9GDR41E	19501, 20502
9H4DA01B	19300			9GDR42E	19501, 20502
9H4DA02B	19300			9GDR43E	19501, 20502
9H4DA03B	19300			9GDR50E	19502, 20502
9H4DA04B	19300			9GDR51E	19502, 20502
9H4DA05B	19300			9GDR52E	19501, 20502
9H4DA06B	19300			9GDR53E	20502
9H4DA07B	19300			9HAE0A	19300
9H4DB00B	19300			9HAE1A	19300
9H4DB01B	19300			9HAE2A	19300
9H4DB02B	19300			9HAE3A	19300
9H4DB03B	19300			9HBE0A	19300
9H4DB04B	19300			9HBE1A	19300
9H4DB05B	19300			9HBE2A	19300
9H4DB06B	19300			9HBE3A	19300
9H4DB07B	19300			9H4R5A	19102
9H55C0	18500			9H4R13E	20502
911C0F	19101				
910C0F	26803				
911R0F	19101				
9114EF	19101				
9121BF	19102				
912TCF	19102				
912T0F	19102				
912TTF	19102				
9124EF	19102				
913TAF	19102				
913TBF	19102				
913T0F	19102				
913TTF	19102				
913T1F	19102				
91312F	19102				
913T3F	19102				



FO-194. IIU Mode 4 Density Logic and Test Gate Generator Logic Diagram.

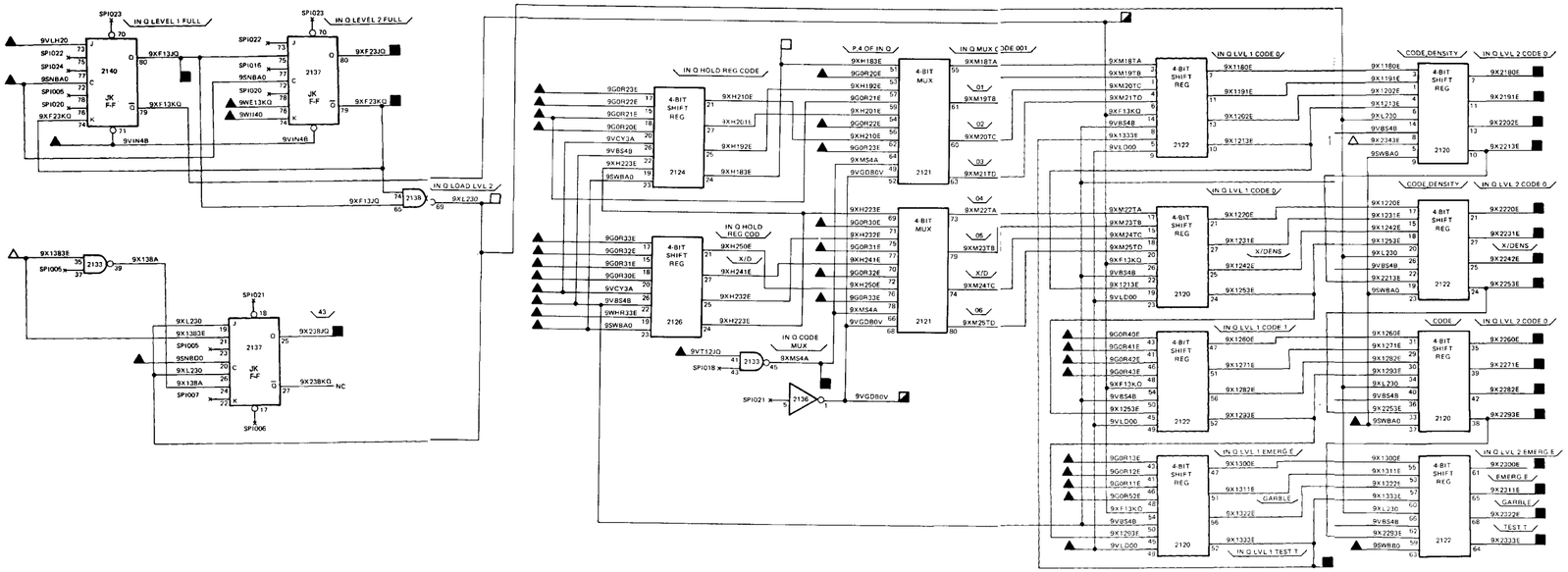
NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1. RIU BAY 1 CARD CAGE (1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1123, A1139, A1149 AND A1227

MS200814

INPUT SIGNAL	INPUT SOURCE FO-SH
9GDR11E	19400
9GDR12E	19400
9GDR13E	19400
9GDR20E	19400
9GDR21E	19400
9GDR22E	19400
9GDR23E	19400
9GDR30E	19400
9GDR31E	19400
9GDR32E	19400
9GDR33E	19400
9GDR40E	19400
9GDR41E	19400
9GDR42E	19400
9GDR43E	19400
9GDR52E	19400
9SNBA0	20502
9SNBD0	20502
9SWBA0	20502
9SWBB0	20502
9VBS4B	19600
9VCY3A	19600
9VIN4B	19600
9VLD00	19800
9VLN20	19800
9VT12JQ	19800
9WE13KQ	19901
9WHR33E	19901
9WI140	19903

OUTPUT SIGNAL	OUTPUT DESTINATION FO-SH
9VGDB0V	19700, 20202, 20502
9XF13JQ	20502
9XF13KQ	19800
9XF23JQ	19903, 20501, 20502
9XF23KQ	19901
9XMS4A	19800
9X1333E	20502
9X2180E	20003, 26803, 27401
9X2191E	20003, 26803, 27401
9X2202E	20003, 26803, 27402
9X2213E	20003, 26803, 27402
9X2220E	20004, 26803, 27402
9X2231E	20004, 26803, 27402
9X2242E	20004, 26803, 27402
9X2253E	20004, 26803, 27402
9X2260E	20004, 26803, 27402
9X2271E	20004, 26803, 27402
9X2282E	20004, 26803, 27402
9X2293E	20004, 26803, 27402
9X2322E	20005, 26803, 27402
9X2333E	20005, 26803, 27402
9X238JQ	20002, 26803, 27401



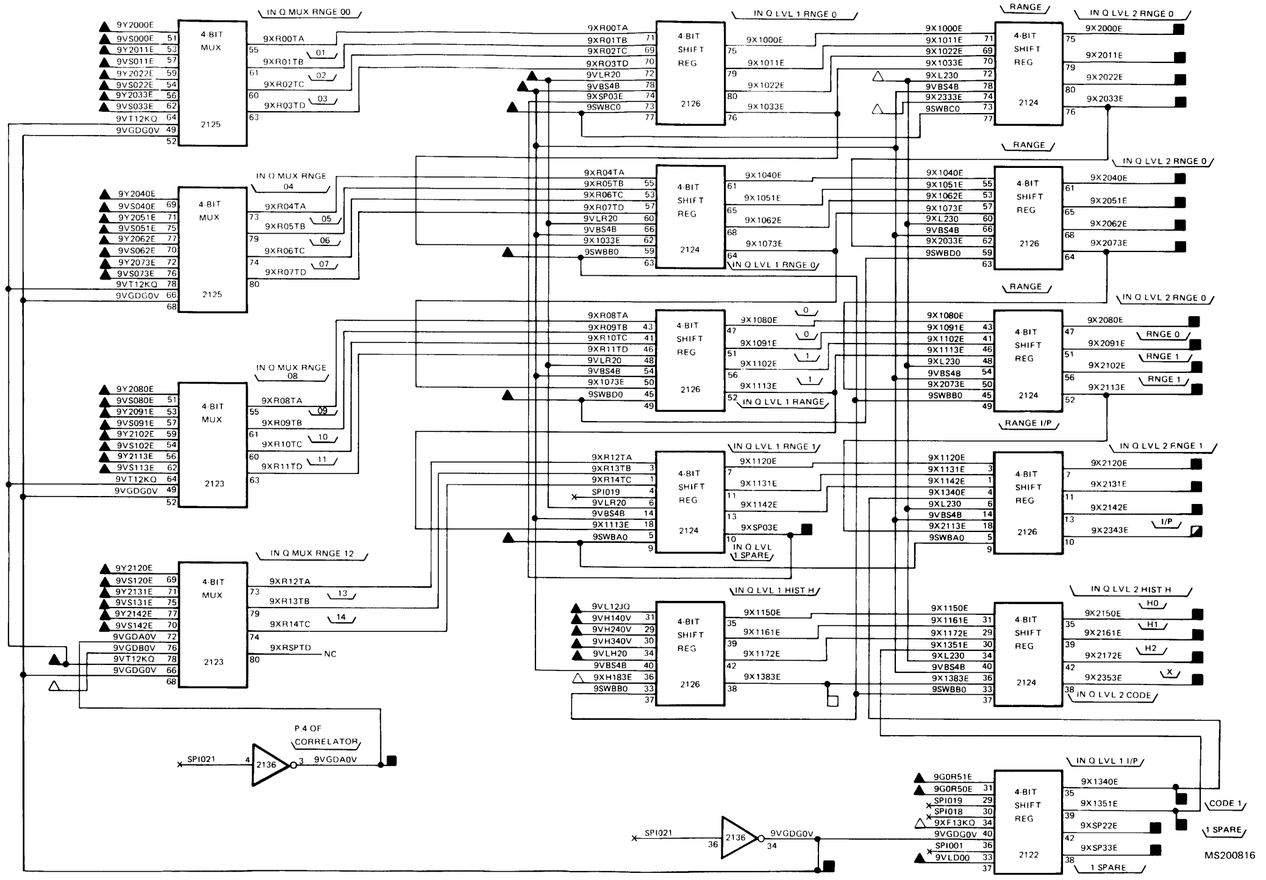
Change 3 FO-195. IIU IFF Input Queue Logic Diagram (Sheet 1 of 2).

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1. RIU BAY 1 CARD CAGE (1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1123, A1139, A1149 AND A1227

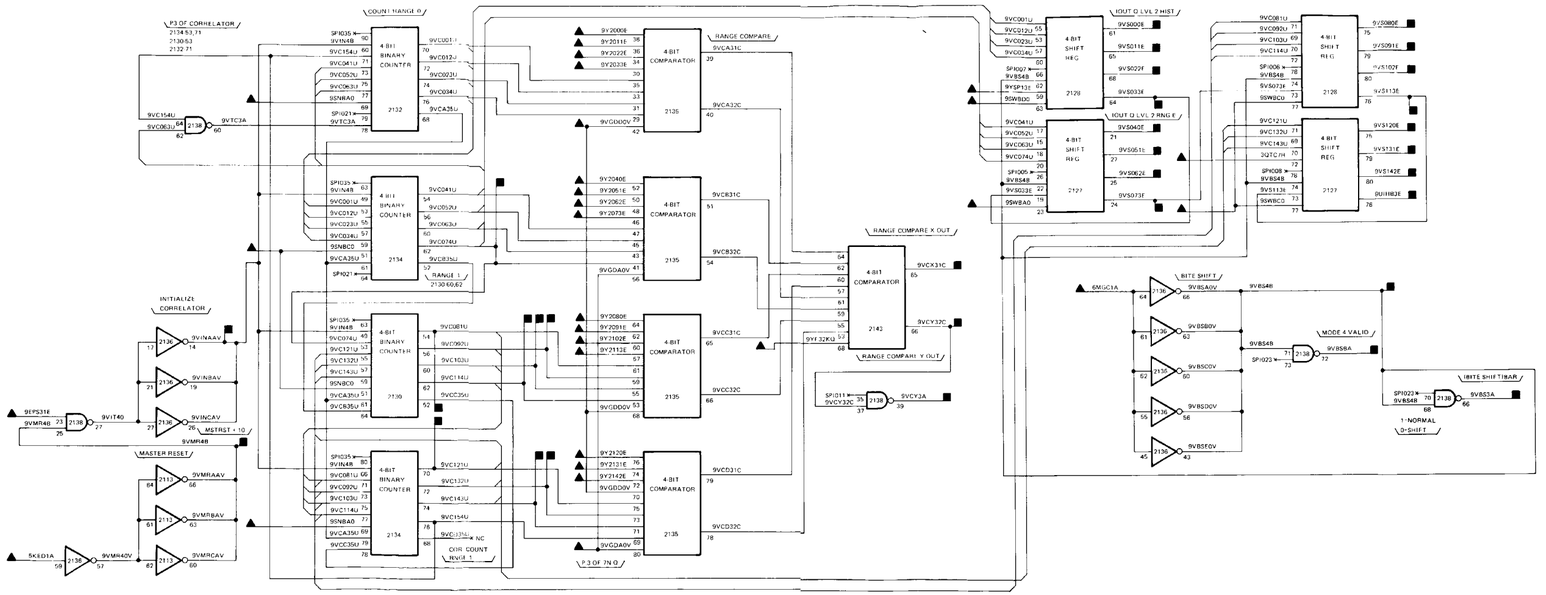
MS200815A

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
9G0R50E	19400	9VGDA0V	19600, 19800, 19902, 20202
9G0R51E	19400	9VGDG0V	19901, 20202
9SWBA0	20502	9XSP03E	20502
9SWBB0	20502	9XSP22E	20502
9SWBC0	20502	9XSP33E	20502
9SWBD0	20502	9X1340E	20502
9VBS4B	19600	9X1351E	20502
9VH140V	19800	9X2000E	20001, 26803, 27401
9VH240V	19800	9X2011E	20001, 26803, 27401
9VH340V	19800	9X2022E	20001, 26803, 27401
9VLD00	19800	9X2033E	20001, 26803, 27401
9VLH20	19800	9X2040E	20001, 26803, 27401
9VLR20	19800	9X2051E	20001, 26803, 27401
9VL12JQ	19800	9X2062E	20001, 26803, 27401
9VS000E	19600	9X2073E	20001, 26803, 27401
9VS011E	19600	9X2080E	20002, 26803, 27401
9VS022E	19600	9X2091E	20002, 26803, 27401
9VS033E	19600	9X2102E	20002, 26803, 27401
9VS040E	19600	9X2113E	20002, 26803, 27401
9VS051E	19600	9X2120E	20002, 26803, 27401
9VS062E	19600	9X2131E	20002, 26803, 27401
9VS073E	19600	9X2142E	20002, 26803, 27401
9VS080E	19600	9X2150E	20003, 26803, 27401
9VS091E	19600	9X2161E	20003, 26803, 27401
9VS102E	19600	9X2172E	20003, 26803, 27401
9VS113E	19600	9X2343E	20005, 20502, 26803, 27402
9VS120E	19600	9X2353E	19901, 20005, 26803, 27402
9VS131E	19600		
9VS142E	19600		
9VT12KQ	19800		
9Y2000E	20100		
9Y2011E	20100		
9Y2022E	20100		
9Y2033E	20100		
9Y2040E	20100		
9Y2051E	20100		
9Y2062E	20100		
9Y2073E	20100		
9Y2080E	20100		
9Y2091E	20100		
9Y2102E	20100		
9Y2113E	20100		
9Y2120E	20100		
9Y2131E	20100		
9Y2142E	20100		



FO-195. IIU IFF Input Queue Logic Diagram (Sheet 2 of 2)

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
3QTC7R	08300	9UIHB3E	20300
3QTC7R	26803	9VBS8A	19800
3QTC7R	27302	9VBS3A	19901
5KED1A	13400	9VBS4B	19501, 19502, 19800, 19901,
5KED1A	26803		20100, 20300
5KED1A	31301	9VCX31C	19800, 20100
6MGC1A	05600	9VCY3A	19501
6MGC1A	26803	9VCY32C	19800, 20100
6MGC1A	27901	9VCQ74U	18600, 19600
9EPS31E	18802	9VC081U	18600
9SNBA0	20502	9VC092U	18600
9SNBC0	20502	9VC103U	18600
9SWBA0	20502	9VC114U	18600
9SWBC0	20502	9VC121U	18600
9SWBD0	20502	9VC132U	18600
9VCQ74U	19600	9VC143U	18600
9VGBA0V	19502	9VIN4B	19501, 19800, 20100
9VGBD0V	19700	9VMR4B	19700, 19901, 19903, 20201,
9YF32KQ	20100		20202
9YSP13E	20100	9VSD00E	19502
9Y2000E	20100	9VSD11E	19502
9Y2011E	20100	9VSC22E	19502
9Y2022E	20100	9VSC33E	19502
9Y2033E	20100	9VSC40E	19502
9Y2040E	20100	9VSD51E	19502
9Y2051E	20100	9VSD62E	19502
9Y2062E	20100	9VSD73E	19502
9Y2073E	20100	9VSD80E	19502
9Y2080E	20100	9VSD91E	19502
9Y2091E	20100	9VS102E	19502
9Y2102E	20100	9VS113E	19502
9Y2113E	20100	9VS120E	19502
9Y2120E	20100	9VS131E	19502
9Y2131E	20100	9VS142E	19502
9Y2142E	20100		

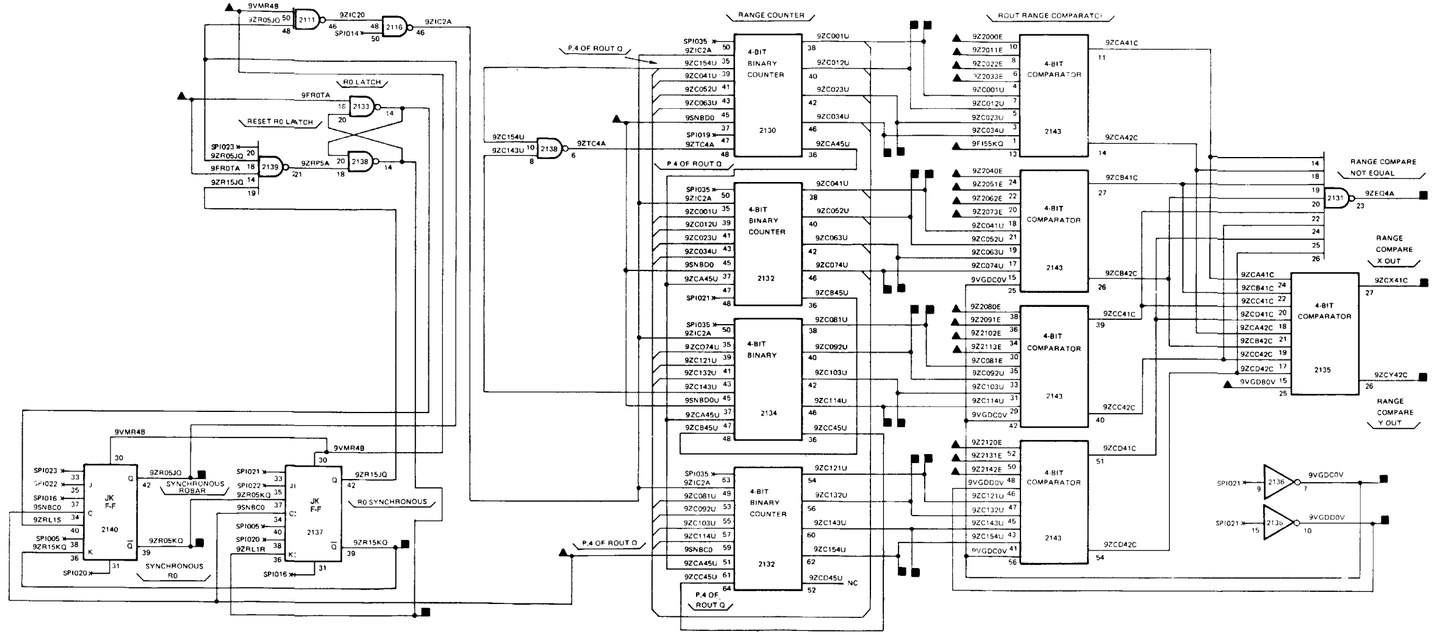


FO-196. IIU IFF Range Comparator Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1. RIU BAY 1 CARD CAGE (1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A2217, AND A2332

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
15SKQ	18700	9VGDC0V	20202
R0TA	18700	9VGDD0V	19600, 19902
R0TA	26802	9ZCA41C	20300
R0TA	27301	9ZCY42C	20300
NBC0	20502	9ZC001U	20502
NBD0	20502	9ZC012U	20502
15B0V	19501	9ZC023U	20502
V84B	19600	9ZC034U	20502
2000E	20300	9ZC041U	20502
2011E	20300	9ZC052U	20502
2022E	20300	9ZC063U	20502
2033E	20300	9ZC074U	20502
2040E	20300	9ZC081U	20502
2051E	20300	9ZC092U	20502
2062E	20300	9ZC103U	20502
2073E	20300	9ZC114U	20502
2080E	20300	9ZC121U	20502
2091E	20300	9ZC132U	20502
2102E	20300	9ZC143U	20502
2113E	20300	9ZC154U	20502
2120E	20300	9ZE64A	20400
2131E	20300	9ZRL1F	20202
2142E	20300	9ZR05JQ	19901, 19903, 20202
		9ZR05KQ	19901, 19903, 20201
		9ZR15KQ	20202



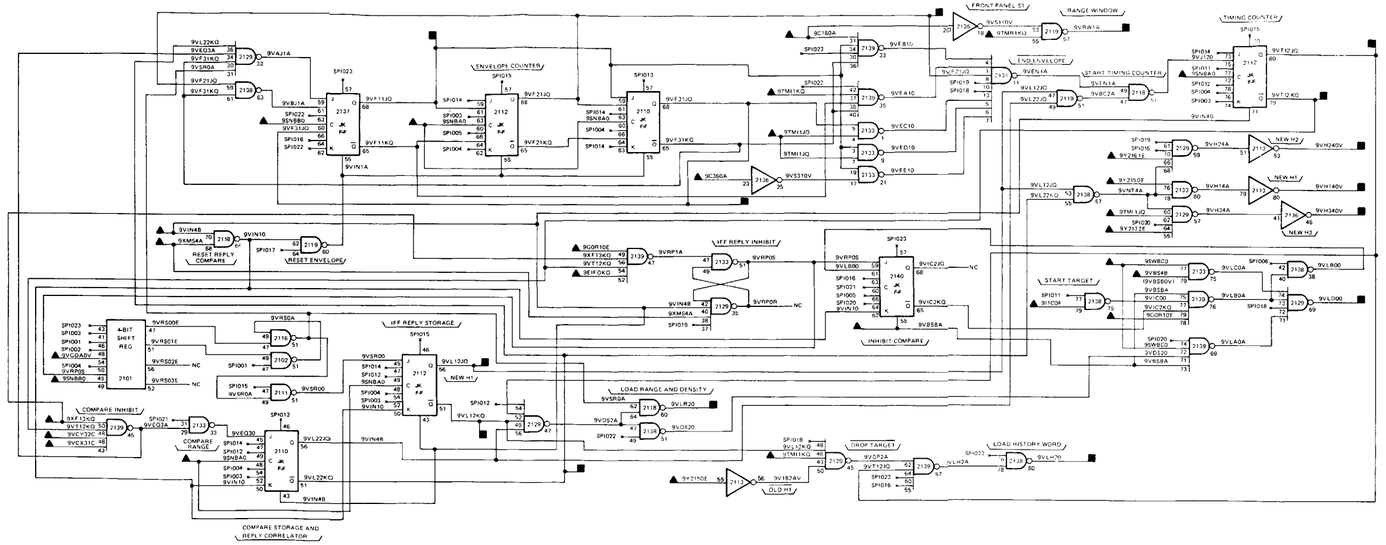
NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1. RIU BAY 1 CARD CAGE (1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A2227 AND A2332

MS200818A

Change 3 FO-197 IIU Radar Range Comparator Logic Diagram

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
9C180A	29801	9VF11JQ	20502
9C360A	29801	9VF21JQ	20502
9EIF0KQ	18801	9VF31JQ	20502
9GDR10E	19400	9VH140V	19502
9I1C0F	19101	9VH240V	19502
9J1C0F	26803	9VH340V	19502
9SNBA0	20502	9VL900	19501, 19502
9SNBB0	20502	9VLH20	19501, 19502
9SWBC0	20502	9VLR20	19502
9TM11JQ	20201	9VL12JQ	19502
9TM11KQ	20201	9VL12KQ	20100
9TMR1KQ	20201	9VL22KQ	20100
9VBS8A	19600	9VRW1A	12300, 26803, 27301
9VBS4B	19600	9VT12JQ	19501
9VCX31C	19600	9VT12KQ	19502
9VC132C	19600		
9VGDA0V	19502		
9VIN4B	19600		
9XF13KQ	19501		
9XM54A	19501		
9Y2150E	20100		
9Y2161E	20100		
9Y2172E	20100		

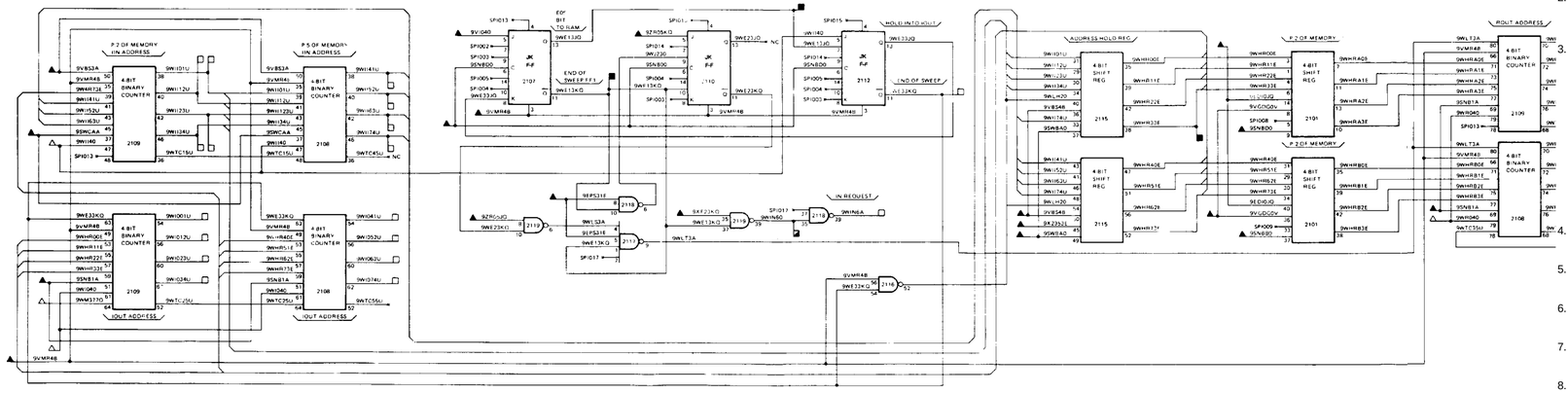


NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1. RIU BAY 1 CARD CAGE (1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XI FOR UNIT TO UNIT SIGNAL CABLING .
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A2217 AND A2332.

Change 3 FO-198. IIU Range and History Correlator Logic Diagram

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
9ED10JQ	18802	9WE13JQ	20005, 26803, 27402
9EPS31E	18802	9WE13KQ	19501
9SNB80	20502	9WHR33E	19501, 20502
9SNBD0	20502	9WIN60	20502
9SNBF1A	20502		
9SWBA0	20502		
9SWCAA	20502		
9VB53A	19600		
9VB54B	19600		
9VGDGOV	19502		
9VI040	20201		
9VMR4B	19600		
9XF23KQ	19501		
9X2353E	19502		
9X2353E	27402		
9ZR05JQ	19700		
9ZR05KQ	19700		

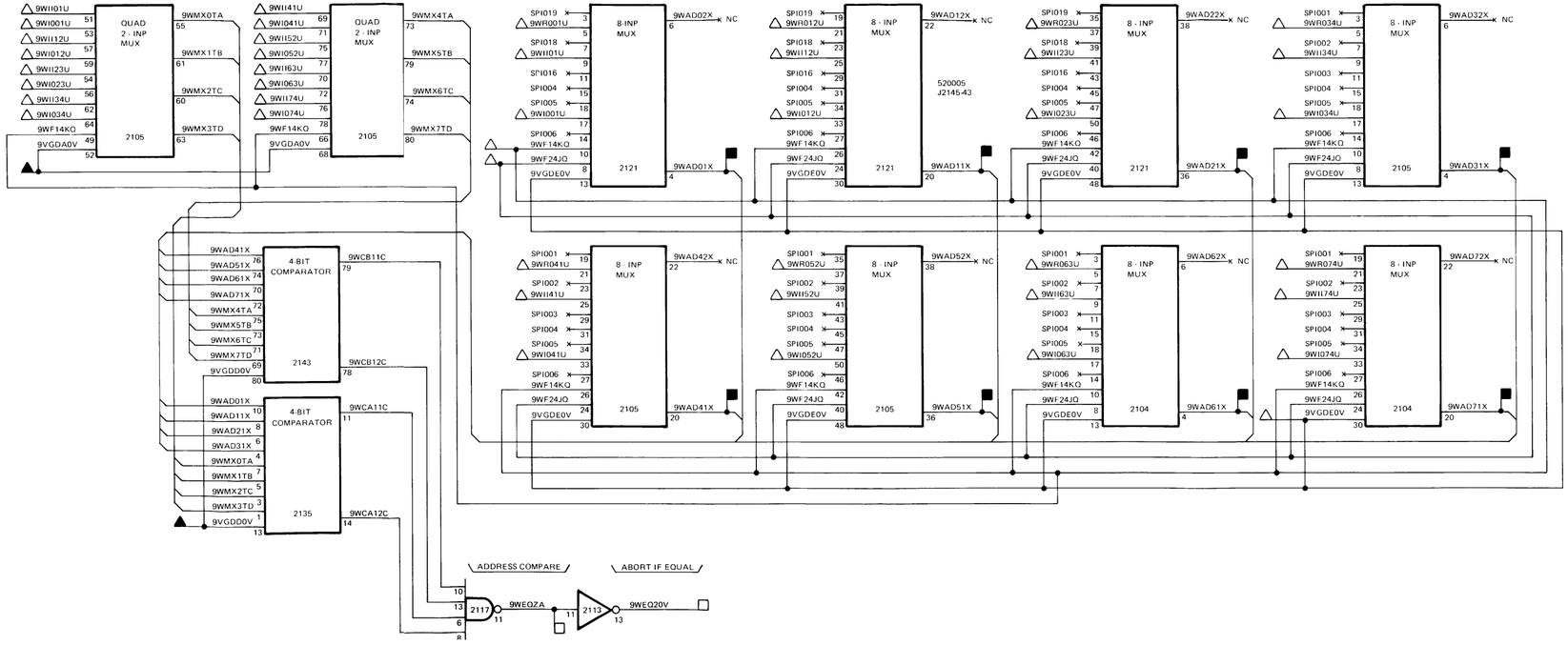


NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1. RIU BAY 1 CARD CAGE (1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A2217 AND A2332.

Change 3 FO-199. IIU Memory Control Logic Diagram (Sheet 1 of 3)

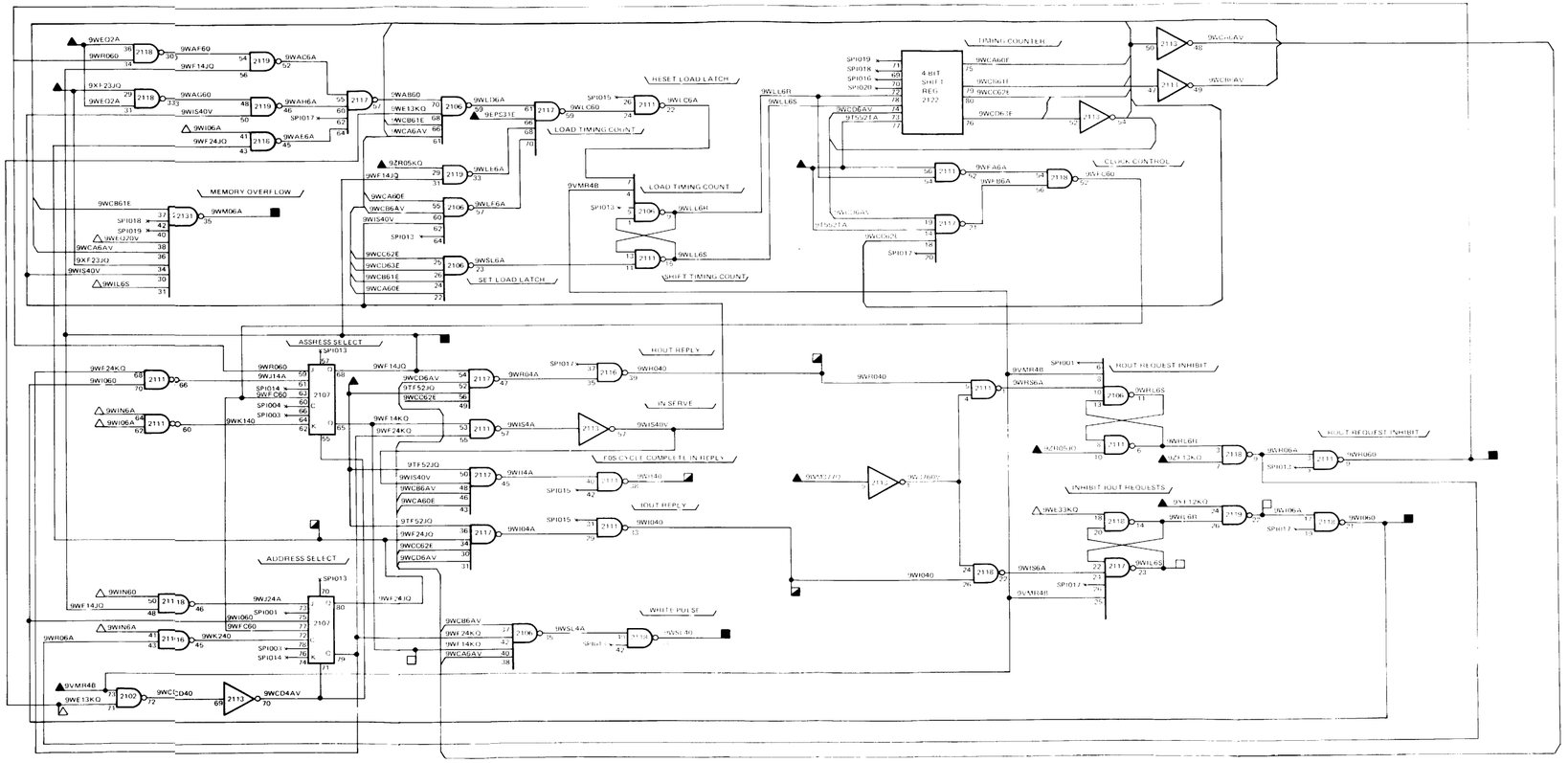
INPUT		OUTPUT	
	SOURCE		DESTINATION
SIGNAL	FO-SH	SIGNAL	FO-SH
9VGDA0V	19502	9WAD01X	20005, 20502, 26803
9VGDD0V	19700	9WAD11X	20005, 20502, 26803
9VGDE0V	20502	9WAD21X	20005, 20502, 26803
		9WAD31X	20005, 20502, 26803
		9WAD41X	20005, 20502, 26803
		9WAD51X	20005, 20502, 26803
		9WAD61X	20005, 20502, 26803
		9WAD71X	20005, 20502, 26803



FO-199. IIU Memory Control Logic Diagram (Sheet 2 of 3)

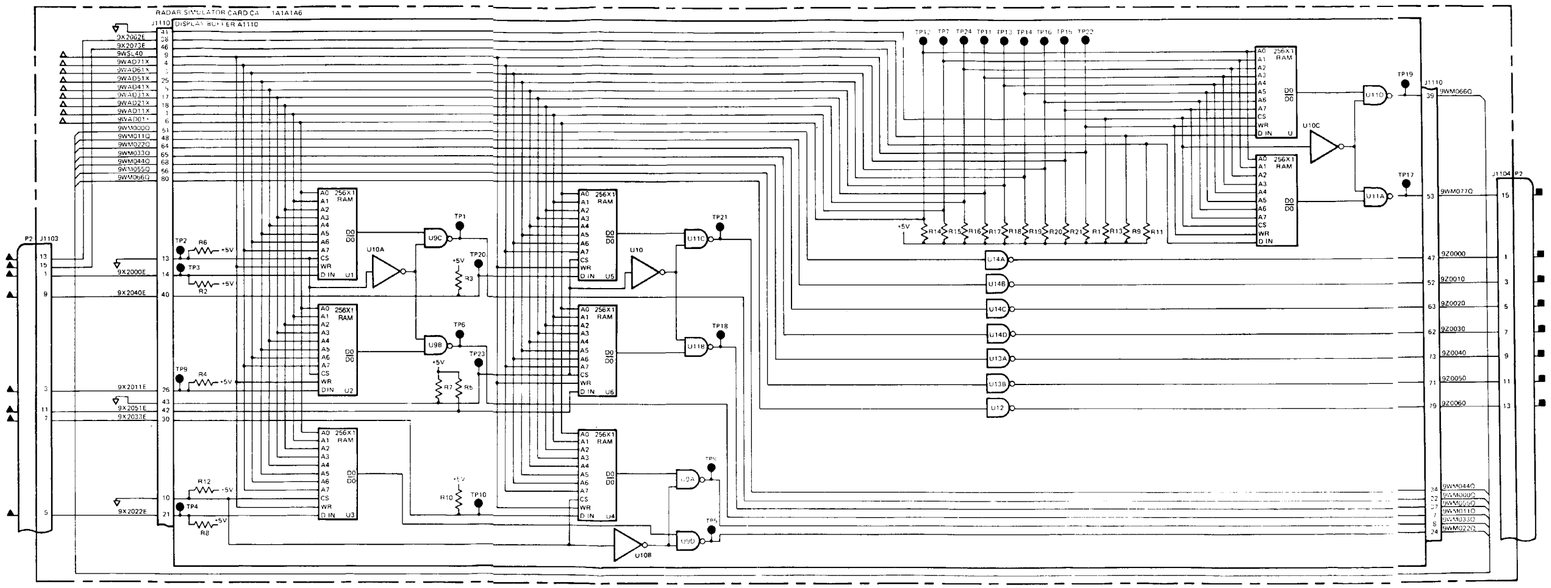
MS200821

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
9EPS31E	18802	9WF14JQ	20502
9TF52JQ	20202	9WF24JQ	20502
9T552TA	20202	9WII40	19501
9VMR4B	19600	9WI040	20100
9WM377Q	20005	9WI060	20502
9XF23JQ	19501	9WM06A	11900, 26803, 27302
9YF12KQ	20100	9WR040	20300
9ZF13KQ	20300	9WR060	20502
9ZR05JQ	19700	9WSL40	20005, 20502, 26803
9ZR05KQ	19700		



FO-199. IIU Memory Control Logic Diagram (Sheet 3 of 3)

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
9X2000E	19502	9WM077Q	20300, 26803
9X2000E	27401	9Z0000	20100, 20300, 26803
9X2011E	19502	9Z0010	20100, 20300, 26803
9X2011E	27401	9Z0020	20100, 20300, 26803
9X2022E	19502	9Z0030	20100, 20300, 26803
9X2022E	27401	9Z0040	20100, 20300, 26803
9X2033E	19502	9Z0050	20100, 20300, 26803
9X2033E	27401	9Z0060	20100, 20300, 26803
9X2040E	19502		
9X2040E	27401		
9X2051E	19502		
9X2051E	27401		
9X2062E	19502		
9X2062E	27401		
9X2073E	19502		
9X2073E	27401		



NOTES: UNLESS OTHERWISE SPECIFIED

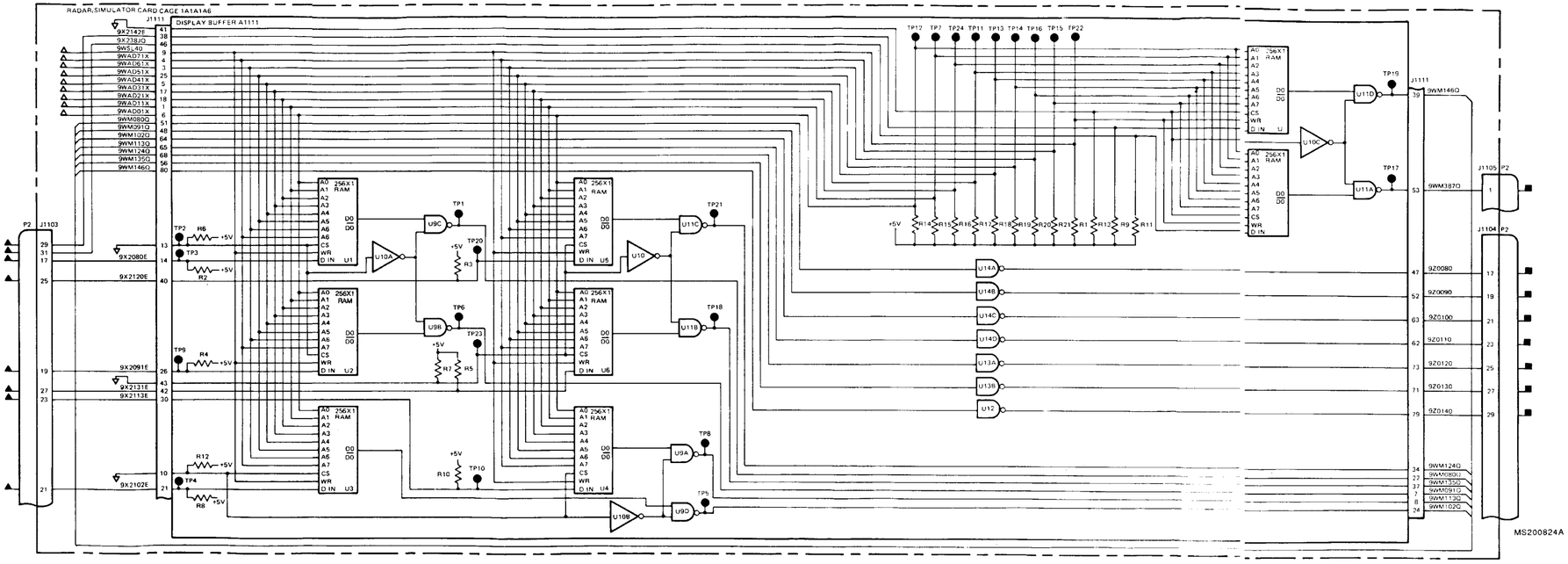
1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1. RSU (1A1A1A6.)
3. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
4. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
5. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
6. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
7. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.

MS200823A

Change 3 FO-200. IIU Target Start RAM Logic Diagram (Sheet 1 of 5)

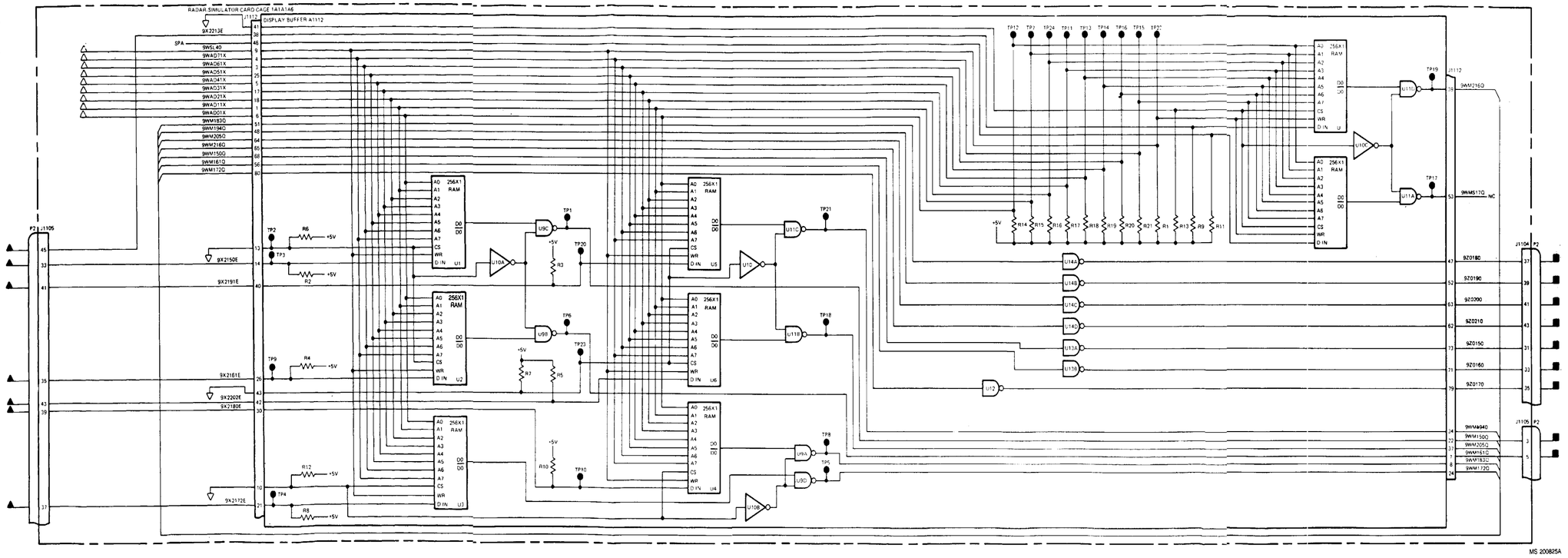
INPUT	
SIGNAL	SOURCE FO-SH
9X2080E	19502
9X2080E	27401
9X2091E	19502
9X2091E	27401
9X2102E	19502
9X2102E	27401
9X2113E	19502
9X2113E	27401
9X2120E	19502
9X2120E	27401
9X2131E	19502
9X2131E	27401
9X2142E	19502
9X2142E	27401
9X238JQ	19501
9X238JQ	27401

OUTPUT	
SIGNAL	DESTINATION FO-SH
9WM387Q	20300, 26803
9Z0080	20100, 20300, 26803
9Z0090	20100, 20300, 26803
9Z0100	20100, 20300, 26803
9Z0110	20100, 20300, 26803
9Z0120	20100, 20300, 26803
9Z0130	20100, 20300, 26803
9Z0140	20100, 20300, 26803



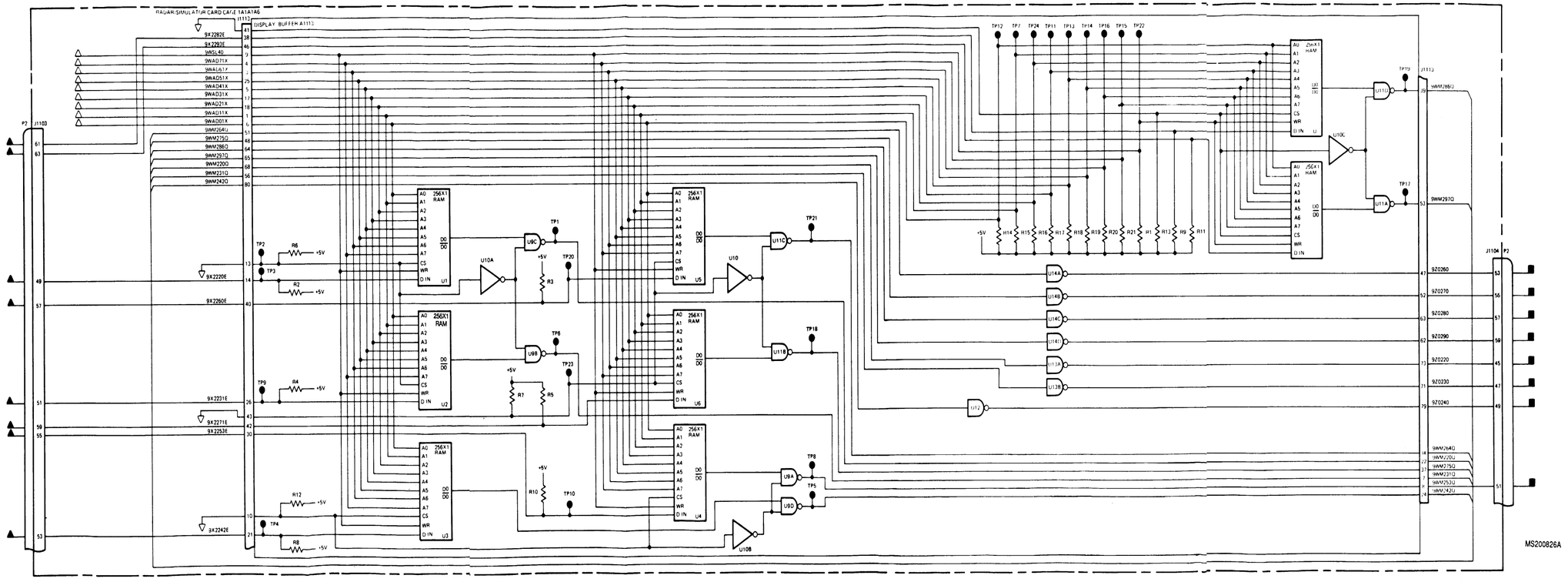
Change 3 FO-200. IIU Target Start RAM Logic Diagram (Sheet 2 of 5)

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
9X2150E	19502	9WM150Q	20300, 26803
9X2150E	27401	9WM161Q	20300, 26803
9X2161E	19502	9ZO150	20100, 20300, 26803
9X2161E	27401	9ZO160	20100, 26803
9X2172E	19502	9ZO170	20100, 20300, 26803
9X2172E	27401	9ZO180	20300, 26803
9X2180E	19501	9ZO190	20300, 26803
9X2180E	27401	9ZO200	20300, 26803
9X2191E	19501	9ZO210	20300, 26803
9X2191E	27401		
9X2202E	19501		
9X2202E	27402		
9X2213E	19501		
9X2213E	27402		



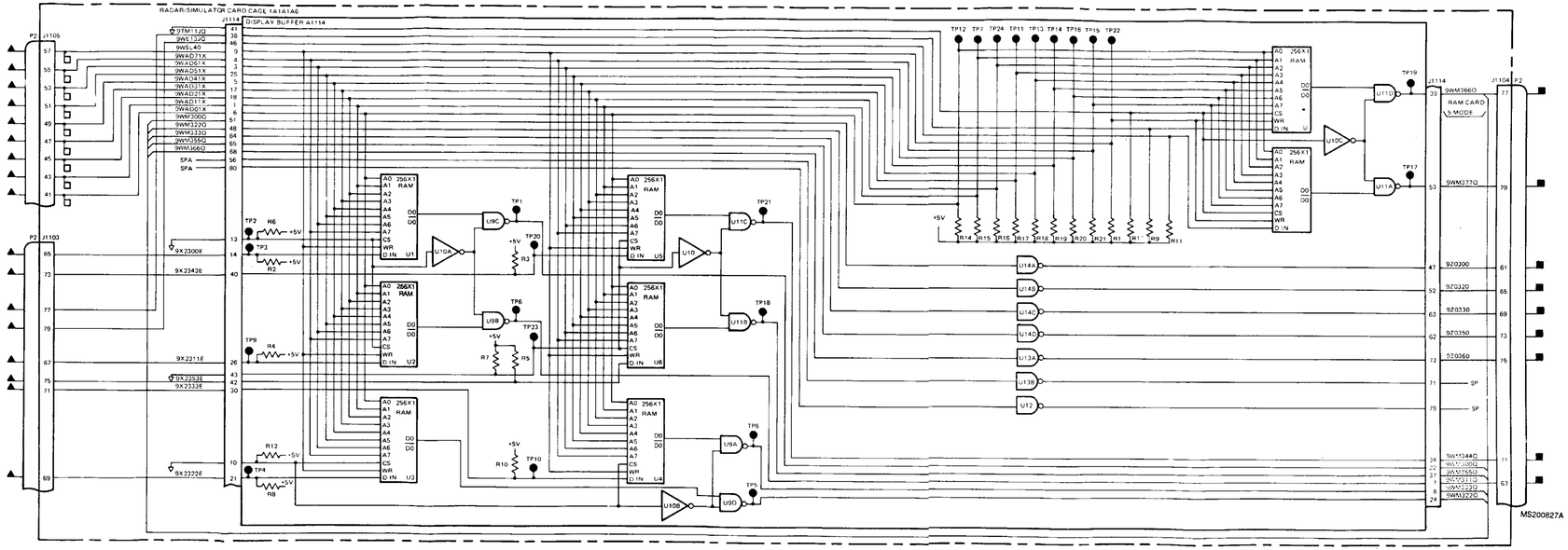
Change 3 FO-200. IIU Target Start RAM Logic Diagram (Sheet 3 of 5)

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
9X2282E	19501	9WM253Q	20300, 26803
9X2283E	27402	9Z0220	20300, 26803
9X2231E	19501	9Z0230	20300, 26803
9X2253E	27402	9Z0240	20300, 26803
9X2242E	19501	9Z0260	20300, 26803
9X2242E	27402	9Z0270	20300, 26803
9X2253E	19501	9Z0280	20300, 26803
9X2253E	27402	9Z0290	20300, 26803
9X2260E	19501		
9X2260E	27402		
9X2271E	19501		
9X2271E	27402		
9X2282E	19501		
9X2282E	27402		
9X2293E	19501		
9X2293E	27402		



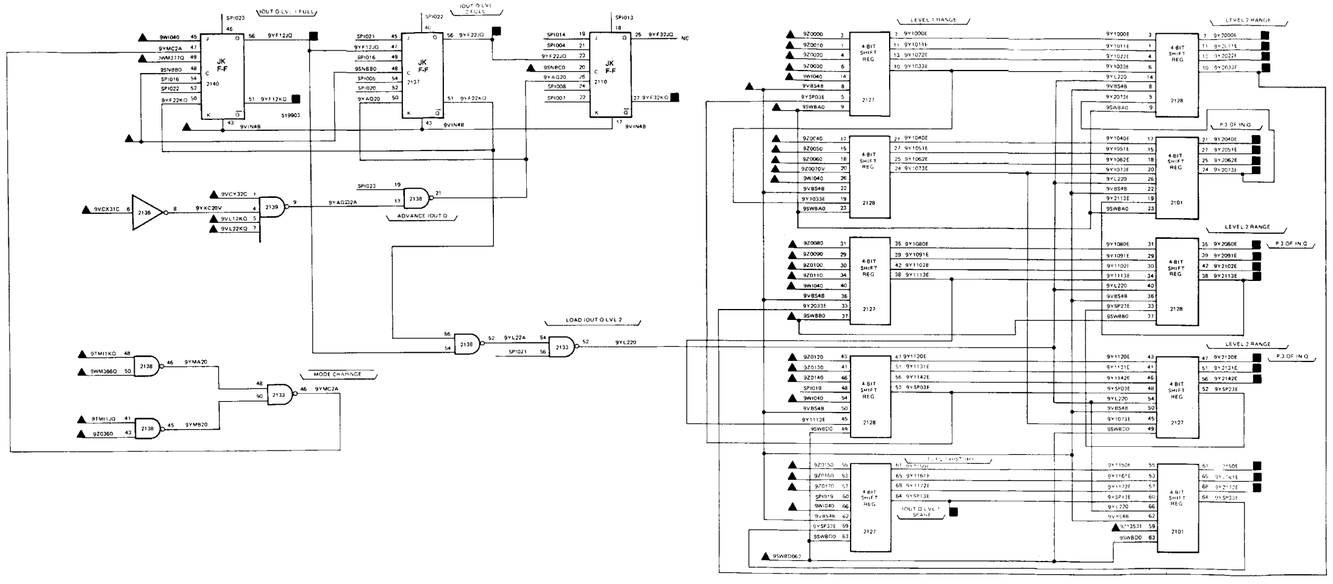
Change 3 FO-200. IIU Target Start RAM Logic Diagram (Sheet 4 of 5)

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
9TM11Q	20201	9WM311Q	20300, 26803
9WAD01X	19902	9WM344Q	20300, 26803
9WAD11X	19902	9WM366Q	20100, 26803
9WAD21X	19902	9WM377Q	19903, 20100, 20300, 26803
9WAD31X	19902	920300	20300, 26803
9WAD41X	19902	920320	20300, 26803
9WAD51X	19902	920330	20300, 26803
9WAD61X	19902	920350	20300, 26803
9WAD71X	19902	920360	20100, 26803
9WE13JQ	19901		
9WE13JQ	27402		
9WSL40	19903		
9X2300E	19501		
9X2300E	27402		
9X2311E	19501		
9X2311E	27402		
9X2322E	19501		
9X2322E	27402		
9X2333E	19501		
9X2333E	27402		
9X2343E	19502		
9X2343E	27402		
9X2353E	19502		
9X2353E	27402		



Change 3 FO-200. IIU Target Start RAM Logic Diagram (Sheet 5 of 5)

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
9SNBB0	20502	9YF12JQ	20502
9SNBC0	20502	9YF12KQ	19903
9SWBA0	20502	9YF22JQ	20501, 20502
9SWBB0	20502	9YF32KQ	19600
9WBB00	20502	9YSP13E	19600, 20502
9TMI1JQ	20201	9Y2000E	19502, 19600
9TMI1KQ	20201	9Y2011E	19502, 19600
9VB548	19600	9Y2022E	19502, 19600
9VCY31C	19600	9Y2033E	19502, 19600, 20502
9VCY32C	19600	9Y2040E	19502, 19600
9VIN48	19600	9Y2051E	19502, 19600
9VL12KQ	19800	9Y2062E	19502, 19600
9VL22KQ	19800	9Y2073E	19502, 19600
9W1040	19903	9Y2080E	19502, 19600
9WM366Q	20005	9Y2091E	19502, 19600
9WM377Q	20005	9Y2102E	19502, 19600
920000	20001	9Y2113E	19502, 19600
920010	20001	9Y2120E	19502, 19600
920020	20001	9Y2131E	19502, 19600
920030	20001	9Y2142E	19502, 19600
920040	20001	9Y2150E	19800
920050	20001	9Y2161E	19800
920060	20001	9Y2172E	19800
920070V	20300		
920080	20002		
920090	20002		
920100	20002		
920110	20002		
920120	20002		
920130	20002		
920140	20002		
920150	20003		
920160	20003		
920170	20003		
920360	20005		
921353E	20300		



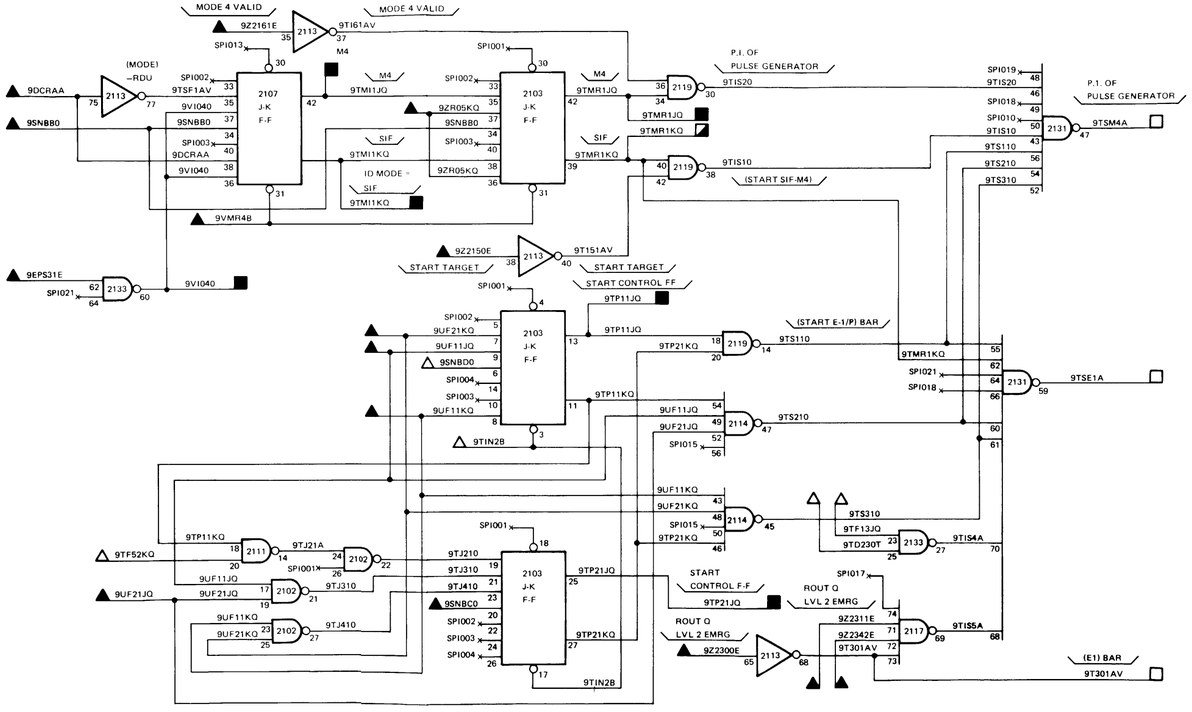
Change 3 FO-201. IIU IFF Output Queue Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1. RIU BAY 1 CARD CAGE (1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1123, A1139, A1149 AND A1227

INPUT SIGNAL	SOURCE FO-SH
9DCRAA	18600
9EPS31E	18802
9SNBB0	20502
9SNBC0	20502
9UF11JQ	20400
9UF11KQ	20400
9UF21JQ	20400
9UF21KQ	20400
9VMR4B	19600
9ZR05KQ	19700
9Z2150E	20300
9Z2150E	26802
9Z2150E	27301
9Z2161E	20300
9Z2161E	26802
9Z2161E	27302
9Z2300E	20300
9Z2300E	26802
9Z2300E	27301
9Z2311E	20300
9Z2342E	20300

OUTPUT SIGNAL	DESTINATION FO-SH
9TMI1JQ	19800, 20005, 20100, 20502, 26803
9TMI1KQ	19800, 20100
9TMR1JQ	20502
9TMR1KQ	19800, 20300
9TP11JQ	20502
9TP21JQ	20502
9VI040	19901



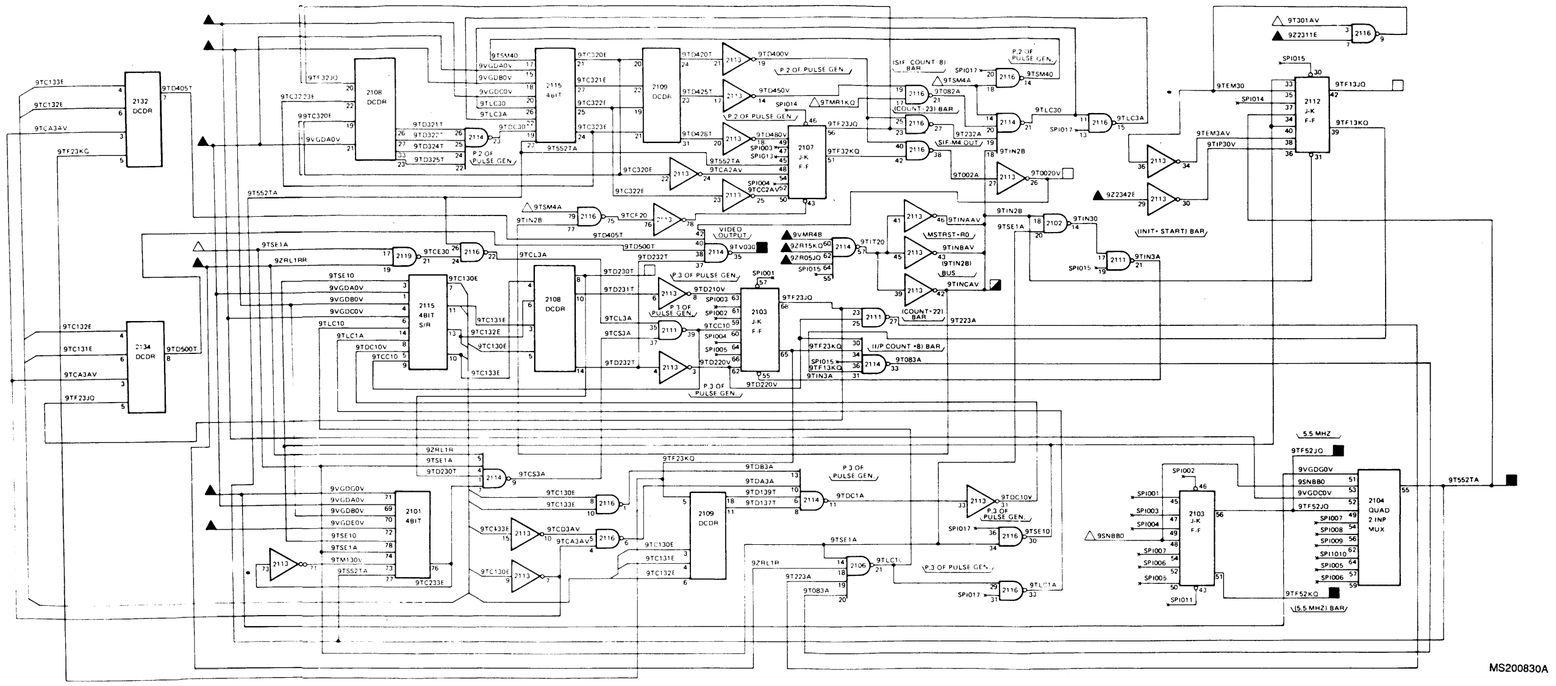
Change 3 FO-202. IIU Pulse Generator Logic Diagram (Sheet 1 of 2)

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1. RIU BAY 1 CARD CAGE (1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A2217 AND A2332

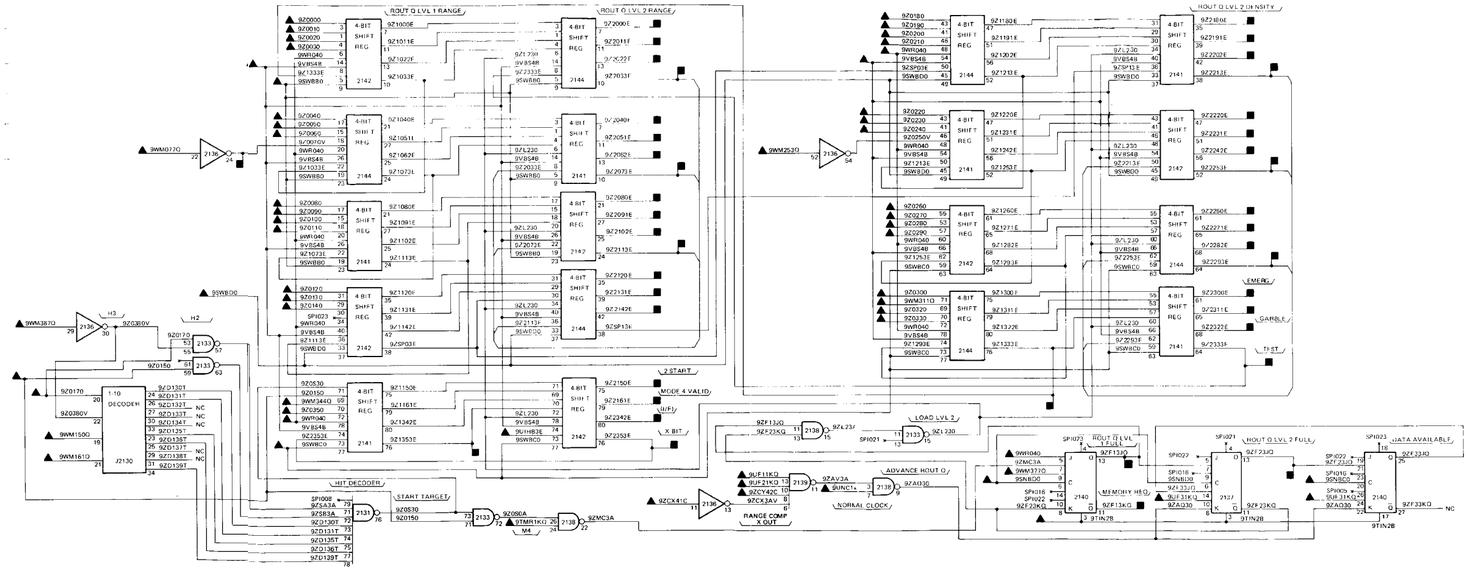
INPUT	
SIGNAL	SOURCE FO-SH
9VGDA0V	19502
9VGDB0V	19501
9VGDC0V	19700
9VGDE0V	20502
9VGDG0V	19502
9VMR4B	19600
9ZRL1R	19700
9ZR05JQ	19700
9ZR15KQ	19700
9Z2311E	20300
9Z2342E	20300

OUTPUT	
SIGNAL	DESTINATION FO-SH
9TF52JQ	19903
9TF52KQ	20502
9TIN2B	20300
9TV030	14202, 20502, 26803
9T0020V	20502
9T552TA	19903



Change 3 FO-202. IIU Pulse Generator Logic Diagram (Sheet 2 of 2)

INPUT SIGNAL	INPUT SOURCE FO-SH	INPUT SIGNAL	INPUT SOURCE FO-SH	OUTPUT SIGNAL	OUTPUT DESTINATION FO-SH
9SNBC0	20502	9ZD230	20004	9ZF13JQ	20502
9SNB90	20502	9ZD240	20004	9ZF13KQ	19903
9SWBB0	20502	9ZD260	20004	9ZF23JQ	20400, 20501, 20502
9SWBC0	20502	9ZD270	20004	9ZD070V	20100
9SWBD0	20502	9ZD280	20004	9ZJ333E	20502
9TIN2B	20202	9ZD290	20004	9ZJ353E	20100
9TMR1KQ	20201	9ZD300	20005	9ZZ000E	19700
9UF11KQ	20400	9ZD320	20005	9ZZ011E	19700
9UF21KQ	20400	9ZD330	20005	9ZZ022E	19700
9UF31KQ	20400	9ZD350	20005	9ZZ033E	19700
9UIHB3E	19600			9ZZ040E	19700
9UNC1A	20400			9ZZ051E	19700
9UNC1A	27301			9ZZ062E	19700
9VBS4B	19600			9ZZ073E	19700
9WM077Q	20001			9ZZ080E	19700
9WM150Q	20003			9ZZ091E	19700
9WM161Q	20003			9ZZ102E	19700
9WM253Q	20004			9ZZ113E	19700
9WM311Q	20005			9ZZ120E	19700
9WM344Q	20005			9ZZ131E	19700
9WM377Q	20005			9ZZ142E	19700
9WM387Q	20002			9ZZ150E	08503, 20201, 26803, 27301
9WR040	19903			9ZZ161E	08503, 20201, 26803, 27302
9ZCX41C	19700			9ZZ180E	08503, 26803, 27301
9ZCY42C	19700			9ZZ191E	08503, 26803, 27301
9ZD000	20001			9ZZ202E	08503, 26803, 27301
9ZD010	20001			9ZZ213E	08503, 26803, 27301
9ZD020	20001			9ZZ220E	08503, 26803, 27301
9ZD030	20001			9ZZ231E	08503, 26803, 27301
9ZD040	20001			9ZZ242E	08503, 26803, 27301
9ZD050	20001			9ZZ253E	08503, 26803
9ZD060	20001			9ZZ260E	08503, 26803
9ZD080	20002			9ZZ271E	08503, 26803, 27301
9ZD090	20002			9ZZ282E	08503, 26803, 27301
9ZD100	20002			9ZZ293E	08503, 26803, 27301
9ZD110	20002			9ZZ300E	08503, 20201, 26803, 27301
9ZD120	20002			9ZZ311E	20201, 20202
9ZD130	20002			9ZZ322E	08503, 26803, 27302
9ZD140	20002			9ZZ333E	08503, 20502, 26803, 27301
9ZD150	20003			9ZZ342E	20201, 20202
9ZD160	20003			9ZZ353E	08503, 26803, 27301
9ZD170	20003				
9ZD180	20003				
9ZD190	20003				
9ZD200	20003				
9ZD210	20003				
9ZD220	20004				

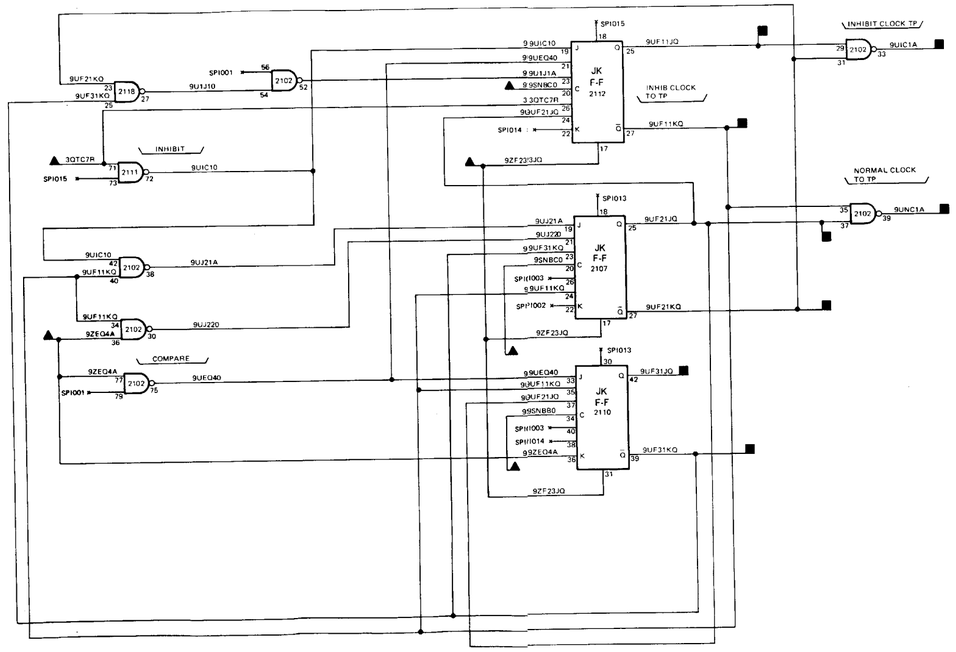


FO-203. IIU Radar Output Queue Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1.
   RIU BAY 1 CARD CAGE (1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A2217 AND A2332.

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
3QTC7R	08300	9UF11JQ	20201, 20502
3QTC7R	26803	9UF11KQ	20201, 20300
3QTC7R	27302	9UF21JQ	20201, 20502
9SNBB0	20502	9UF21KQ	20201, 20300
9SNBC0	20502	9UF31JQ	20502
9ZEQ4A	19700	9UF31KQ	20300
9ZF23JQ	20300	9UIC1A	08300, 26803, 27301
		9UNC1A	08300, 08400, 20300, 20502, 26802, 26803, 27301



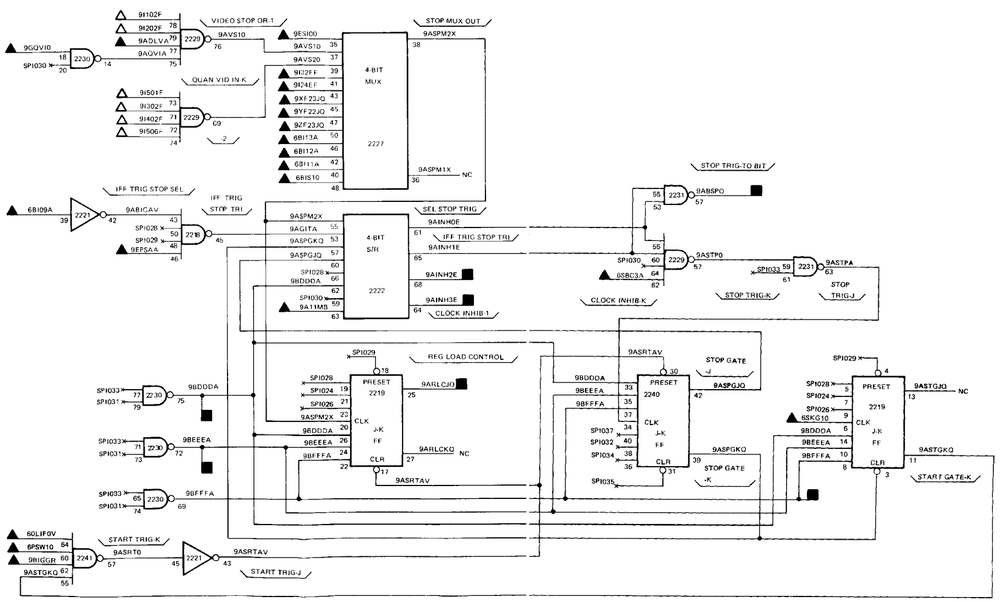
NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1.
   RIU BAY 1 CARD CAGE (1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A2217 AND A2332

Change 3 FO-204. IIU Input/Output Control Logic Diagram

INPUT SIGNAL	INPUT SOURCE FO-SH
6BIS10	05900
6BIS10	26803
6BIS10	27901
6BI09A	05900
6BI09A	26803
6BI09A	27901
6BI11A	26803
6BI11A	27901
6BI12A	26803
6BI12A	27901
6BI13A	26803
6BI13A	27902
6PSW10	26803
6PSW10	27902
6PSW10	30702
6SBC3A	05900
6SBC3A	26803
6SBC3A	27902
6SKG10	05900
6SKG10	26803
6SKG10	27902
6OLIFOV	05500
6OLIFOV	26803
9ADLVA	19000
9BIGGR	18500
9EPSAA	18801
9ESI00	18801
9GQV10	18900
9I24EF	19102
9J32FF	19102
9XF23JQ	19501
9YF22JQ	20100
9ZF23JQ	20300

OUTPUT SIGNAL	OUTPUT DESTINATION FO-SH
9ABSP0	05900, 26803, 27901
9AINH2E	18500
9AINH3E	18500, 19102
9ARLCJQ	19400
9BDDDA	18700, 18802, 19000, 19400
9BEEEA	18700, 18802, 19000, 19400
9BFFFA	18700, 18802, 19000, 19400



FO-205. IIU BITE Register and Control Logic Diagram (Sheet 1 of 2)

NOTES: UNLESS OTHERWISE SPECIFIED

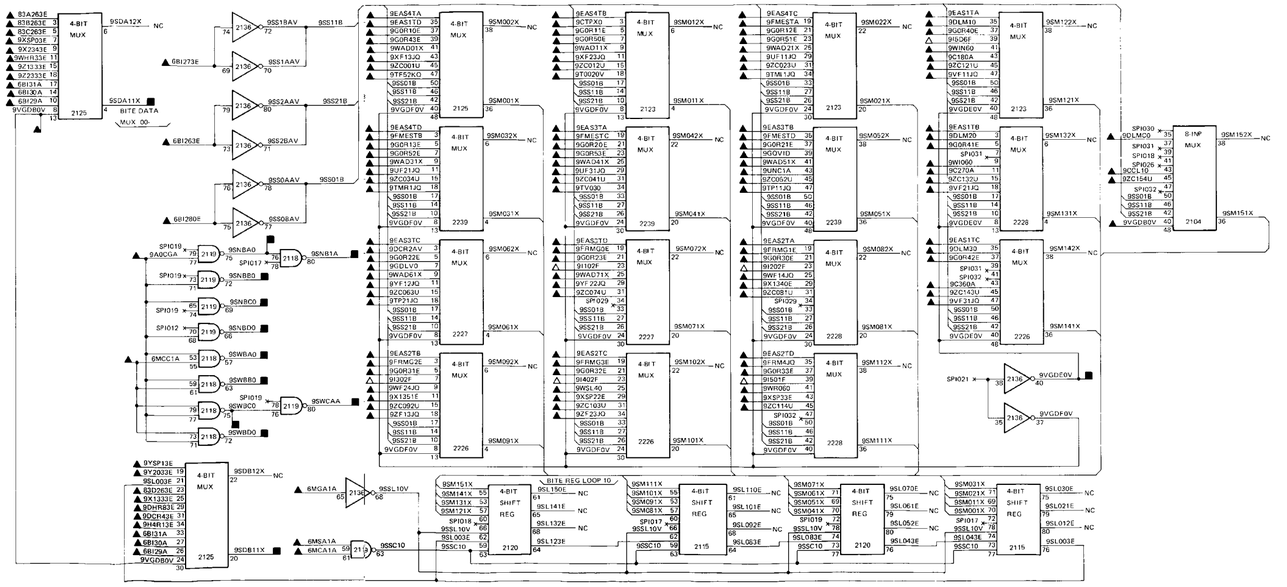
1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1. RIU BAY 1 CARD CAGE (1A1A1A4.)
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX .
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT TO UNIT SIGNAL CABLING .
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER
    - B. REFER TO TABLE 5-39 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-40 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A2217 AND A2332

INPUT SIGNAL	SOURCE FO-SH
6B1262E	05300
6B1262E	26803
6B1262E	27901
6B1273E	05300
6B1273E	26803
6B1273E	27901
6B1280E	05300
6B1280E	26803
6B1280E	27901
6B1291A	26802
6B1291A	26803
6B1291A	27901
6B130A	26803
6B130A	27901
6B131A	05300
6B131A	26802
6B131A	26803
6B131A	27901
6MCA1A	05600
6MCA1A	26803
6MCA1A	27901
6MCC1A	05600
6MCC1A	26803
6MCC1A	27901
6MGA1A	05600
6MGA1A	26803
6MGA1A	27201
6MSA1A	05600
6MSA1A	26803
6MSA1A	27201
83A263E	18402
83A263E	28401
83B263E	18402
83B263E	28401
83C263E	18403
83C263E	28401
83D263E	18404
83D263E	28401
9A0EGA	18500
9CCL10	28702
9CCL10	29801
9CTPXO	27801
9C180A	29801
9C270A	26803
9C270A	29801
9C360A	29801

INPUT SIGNAL	SOURCE FO-SH
9DCR2AV	18600
9DCR43E	18600
9DHR83E	18600
9DLMCO	18700
9DLM10	18700
9DLM20	18700
9DLM30	18700
9EAS1TA	18801
9EAS1TB	18801
9EAS1TC	18801
9EAS1TD	18801
9EAS2TA	18801
9EAS2TB	18801
9EAS2TC	18801
9EAS2TD	18801
9EAS3TA	18801
9EAS3TB	18801
9EAS3TC	18801
9EAS3TD	18801
9EAS4TA	18801
9EAS4TB	18801
9EAS4TC	18801
9EAS4TD	18801
9FMESTA	18700
9FMESTB	18700
9FMESTC	18700
9FMESTD	18700
9FRMG0E	18700
9FRMG0E	26802
9FRMG0E	27302
9FRMG1E	18700
9FRMG1E	26802
9FRMG1E	27302
9FRMG2E	18700
9FRMG2E	26802
9FRMG2E	27302
9FRMG3E	18700
9FRMG3E	26802
9FRMG3E	27302
9FRM4JQ	18700
9FRM4JQ	26802
9FRM4JQ	27302
9GLV0	19000
9GQVID	18900
9GQR10E	19400
9GQR11E	19400

INPUT SIGNAL	SOURCE FO-SH
9GQR12E	19400
9GQR13E	19400
9GQR20E	19400
9GQR21E	19400
9GQR22E	19400
9GQR23E	19400
9GQR30E	19400
9GQR31E	19400
9GQR32E	19400
9GQR33E	19400
9GQR40E	19400
9GQR41E	19400
9GQR42E	19400
9GQR43E	19400
9GQR50E	19400
9GQR51E	19400
9GQR52E	19400
9GQR53E	19400
9HHR33E	19901
9HJN60	19901
9HR060	19903
9HSL40	19903
9XF13JG	19501
9XF23JG	19501
9XSP03E	19502
9XSP22E	19502
9XSP33E	19502
9XT333E	19501
9XT340E	19502
9XT351E	19502
9X2343E	27402
9YF12JG	20100
9YF22JG	20100
9YSP13E	20100
9Y2033E	20100
92C001U	19700
92C012U	19700
92C023U	19700
92C034U	19700
92C041U	19700
92C052U	19700
92C063U	19700
92C074U	19700
92C081U	19700
92C092U	19700
92C103U	19700
92C114U	19700
92C121U	19700
92C132U	19700
92C143U	19700
92C154U	19700
92GDB0V	19501
92F13JQ	20300
92F23JQ	20300
92I333E	20300
92J333E	20300
92J333E	27301

OUTPUT SIGNAL	DESTINATION FO-SH
9SDA11X	05400, 26803, 27901
9SDB11X	05400, 26803, 27901
9SNBA0	19501, 19600, 19800
9SNBB0	19800, 19901, 20100, 20201, 20400
9SNBC0	19600, 19700, 20100, 20201, 20300, 20400
9SNBD0	19501, 19700, 19901, 20300
9SNB1A	19901
9SWBA0	19501, 19502, 19600, 19901, 20100
9SWBB0	19501, 19502, 20100, 20300
9SWBC0	19502, 19600, 19800, 20300
9SWBD0	19502, 19600, 20100, 20300
9SWCAA	19901
9VGDEDV	19902, 20202



FO-205. IIU BITE Register and Control Logic Diagram (Sheet 2 of 2)

By Order of the Secretary of the Army:

JOHN A. WICKHAM, JR.
General, United States Army
Chief of Staff

Official:

DONAD J. DELANDRO
Brigadier General , United States Army
The Adjutant General

Distribution:

To be distributed in accordance with DA Form 12-32, Organizational Maintenance requirements for AN/TSQ-73.

* U.S. GOVERNMENT PRINTING OFFICE: 1985-544-178/20058

RECOMMENDED CHANGES TO EQUIPMENT TECHNICAL PUBLICATIONS

THEN...JOT DOWN THE DOPE ABOUT IT ON THIS FORM. CAREFULLY TEAR IT OUT, FOLD IT AND DROP IT IN THE MAIL.

SOMETHING WRONG WITH PUBLICATION

FROM: (PRINT YOUR UNIT'S COMPLETE ADDRESS)

DATE SENT

PUBLICATION NUMBER	PUBLICATION DATE	PUBLICATION TITLE

BE EXACT PIN-POINT WHERE IT IS

PAGE NO.	PARA-GRAPH	FIGURE NO.	TABLE NO.

IN THIS SPACE, TELL WHAT IS WRONG AND WHAT SHOULD BE DONE ABOUT IT.

PRINTED NAME, GRADE OR TITLE AND TELEPHONE NUMBER	SIGN HERE

DA FORM 1 JUL 79 2028-2

PREVIOUS EDITIONS ARE OBSOLETE.

P.S.--IF YOUR OUTFIT WANTS TO KNOW ABOUT YOUR RECOMMENDATION MAKE A CARBON COPY OF THIS AND GIVE IT TO YOUR HEADQUARTERS.

THE METRIC SYSTEM AND EQUIVALENTS

'NEAR MEASURE

. Centimeter = 10 Millimeters = 0.01 Meters = 0.3937 Inches
1 Meter = 100 Centimeters = 1000 Millimeters = 39.37 Inches
1 Kilometer = 1000 Meters = 0.621 Miles

'VEIGHTS

Gram = 0.001 Kilograms = 1000 Milligrams = 0.035 Ounces
1 Kilogram = 1000 Grams = 2.2 lb.
1 Metric Ton = 1000 Kilograms = 1 Megagram = 1.1 Short Tons

LIQUID MEASURE

1 Milliliter = 0.001 Liters = 0.0338 Fluid Ounces
1 Liter = 1000 Milliliters = 33.82 Fluid Ounces

SQUARE MEASURE

1 Sq. Centimeter = 100 Sq. Millimeters = 0.155 Sq. Inches
1 Sq. Meter = 10,000 Sq. Centimeters = 10.76 Sq. Feet
1 Sq. Kilometer = 1,000,000 Sq. Meters = 0.386 Sq. Miles

CUBIC MEASURE

1 Cu. Centimeter = 1000 Cu. Millimeters = 0.06 Cu. Inches
1 Cu. Meter = 1,000,000 Cu. Centimeters = 35.31 Cu. Feet

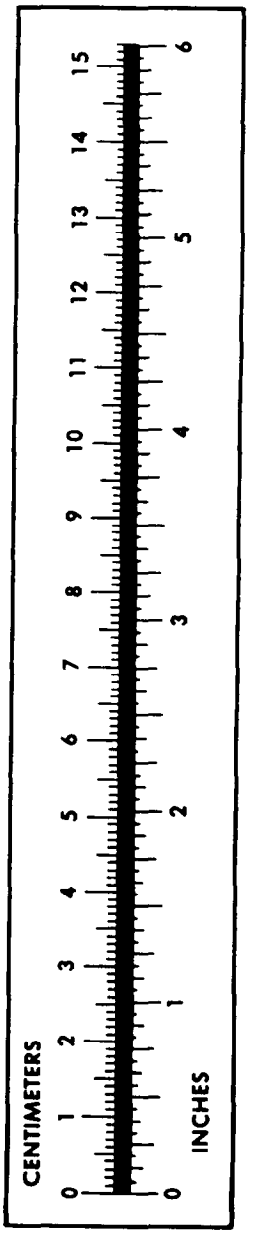
TEMPERATURE

5/9(°F - 32) = °C
212° Fahrenheit is evuivalent to 100° Celsius
90° Fahrenheit is equivalent to 32.2° Celsius
32° Fahrenheit is equivalent to 0° Celsius
9/5C° + 32 =°F

APPROXIMATE CONVERSION FACTORS

TO CHANGE	TO	MULTIPLY BY
Inches	Centimeters	2.540
Feet	Meters	0.305
Yards	Meters	0.914
Miles	Kilometers	1.609
Square Inches	Square Centimeters	6.451
Square Feet	Square Meters	0.093
Square Yards	Square Meters	0.836
Square Miles	Square Kilometers	2.590
Acres	Square Hectometers	0.405
Cubic Feet	Cubic Meters	0.028
Cubic Yards	Cubic Meters	0.765
Fluid Ounces	Milliliters	29.573
ıts	Liters	0.473
arts	Liters	0.946
allons	Liters	3.785
Ounces	Grams	28.349
Pounds	Kilograms	0.454
Short Tons	Metric Tons	0.907
Pound-Feet	Newton-Meters	1.356
Pounds per Square Inch	Kilopascals	6.895
Miles per Gallon	Kilometers per Liter	0.425
Miles per Hour	Kilometers per Hour	1.609

TO CHANGE	TO	MULTIPLY BY
Centimeters	Inches	0.394
Meters	Feet	3.280
Meters	Yards	1.094
Kilometers	Miles	0.621
Square Centimeters	Square Inches	0.155
Square Meters	Square Feet	10.764
Square Meters	Square Yards	1.196
Square Kilometers	Square Miles	0.386
Square Hectometers	Acres	2.471
Cubic Meters	Cubic Feet	35.315
Cubic Meters	Cubic Yards	1.308
Milliliters	Fluid Ounces	0.034
Liters	Pints	2.113
Liters	Quarts	1.057
ers	Gallons	0.264
ms	Ounces	0.035
ograms	Pounds	2.205
Metric Tons	Short Tons	1.102
Newton-Meters	Pounds-Feet	0.738
Kilopascals	Pounds per Square Inch	0.145
lometers per Liter	Miles per Gallon	2.354
meters per Hour	Miles per Hour	0.621



PIN: 057728-003